

PRODUCT NAME Malgow10/10C (VP)
(6 層システム基板)

UNIT NO. DPL84024G01

PWA NO.

PWB NO.

PAGE	SH.	FUNCTION	REV	PAGE	SH.	FUNCTION	REV	PAGE	SH.	FUNCTION	REV	PAGE	SH.	FUNCTION	REV
1	---	COVER SHEET	00	26	139	ME JTAG	00	51	210	PC-Card POWER	00	76	335	ACCELEROMETER (FRONT)	00
2	---	COVER & REV. SHEET	00	27	144	DDR2 SO-DIMM (Ch.A) 1	00	52	211	PC-CARD I/F	00	77	340	SUPER I/O	00
3	---	BLOCK DIAGRAM	00	28	145	DDR2 SO-DIMM (Ch.A) 2	00	53	213	MEDIA POWER	00	78	400	Ethernet PHY	00
4	---	PCB Connection	00	29	146	DDR2 SO-DIMM (Ch.B) 1	00	54	214	MEDIA BRIDGE I/F	00	79	401	LAN Power	00
5	100	CK505	00	30	147	DDR2 SO-DIMM (Ch.B) 2	00	55	216	Smart Card Reader	00	80	403	LAN I/F & LAN LED	00
6	105	SOCKET-P 1	00	31	148	DDR2 TERMINATION	00	56	217	Smart Card I/F	00	81	422	1394 EEPROM	00
7	106	SOCKET-P 2	00	32	160	ICH9M 1	00	57	230	DOCKING I/F 1	00	82	423	1394 I/F	00
8	107	SOCKET-P 3	00	33	161	ICH9M 2	00	58	231	DOCKING I/F 2	00	83	444	Bluetooth2 I/F	00
9	108	SOCKET-P 4	00	34	162	ICH9M 3	00	59	251	EXPRESSCARD	00	84	460	USB POWER	00
10	109	SOCKET-P	00	35	163	ICH9M 4	00	60	252	EXPRESSCARD I/F	00	85	463	USB CHARGER 1	00
11	115	THERMAL SENSOR	00	36	164	ICH9M 5	00	61	259	WLAN / WiMAX Power	00	86	464	USB CHARGER 2	00
12	120	CANTIGA (G)MCH 1	00	37	165	ICH9M 6	00	62	260	PCI-E MINI CARD	00	87	465	USB CHARGER 3	00
13	121	CANTIGA (G)MCH 2	00	38	166	ICH9M 7	00	63	261	PCI-E MINI (3G/ROBSON)	00	88	530	LCD PS	00
14	122	CANTIGA (G)MCH 3	00	39	167	ICH9M 8	00	64	267	3G(WWAN) LED	00	89	531	LCD I/F	00
15	123	CANTIGA (G)MCH 4	00	40	168	ICH9M 9	00	65	300	FLASH ROM	00	90	533	CRT RGB	00
16	124	CANTIGA (G)MCH 5	00	41	169	IMVP-6+ POWER OK	00	66	301	MDC Device Lock	00	91	534	CRT DDC	00
17	125	CANTIGA (G)MCH 6	00	42	190	SATA HDD I/F	00	67	310	PC Health PJ	00	92	535	CRT H/VSync	00
18	126	CANTIGA (G)MCH 7	00	43	195	SATA ODD I/F	00	68	311	F/T PJ	00	93	541	LVDS Jumper	00
19	127	CANTIGA (G)MCH 8	00	44	197	eSATA/USB I/F	00	69	320	EC/KBC 1	00	94	596	GFX I/F 1	00
20	128	CANTIGA (G)MCH 9	00	45	198	SATA REPEATER	00	70	321	EC/KBC 2	00	95	597	GFX I/F 2	00
21	129	CANTIGA (G)MCH 10	00	46	200	CARD CONT 1	00	71	322	EC/KBC 3	00	96	615	Speaker Conn.	00
22	130	CANTIGA (G)MCH 11	00	47	201	CARD CONT 2	00	72	323	KB I/F	00	97	750	EMI CAP.	00
23	131	CANTIGA (G)MCH 12	00	48	203	CARD CONT 4	00	73	325	LED	00	98	800	[PS]DC-IN	00
24	132	CANTIGA (G)MCH 13	00	49	204	CARD CONT 5	00	74	326	WIRELESS SW,LED	00	99	801	[PS]PVT-SW	00
25	133	CANTIGA (G)MCH 14	00	50	205	CARD CONT 6	00	75	330	TPM CONT	00	100	802	[PS]1st Battery	00

APPROVED BY	CHECKED BY	DESIGNED BY	TITLE	TOTAL	PAGE NO.	REV.MARK	DRAWING.NO.
[PC 設](PC 設6) K.Yamamori	[PC 設](PC 設6) M.Ando	[PC 設](PC 設7) T.Ichimura	FG6IN1	128	001	00	360065815

SCHEMATIC DIAGRAMS

PRODUCT NAME _____

UNIT NAME _____

UNIT NO. _____

PWA NAME _____

PWA NO. _____

PWB NAME _____

PWB NO. _____

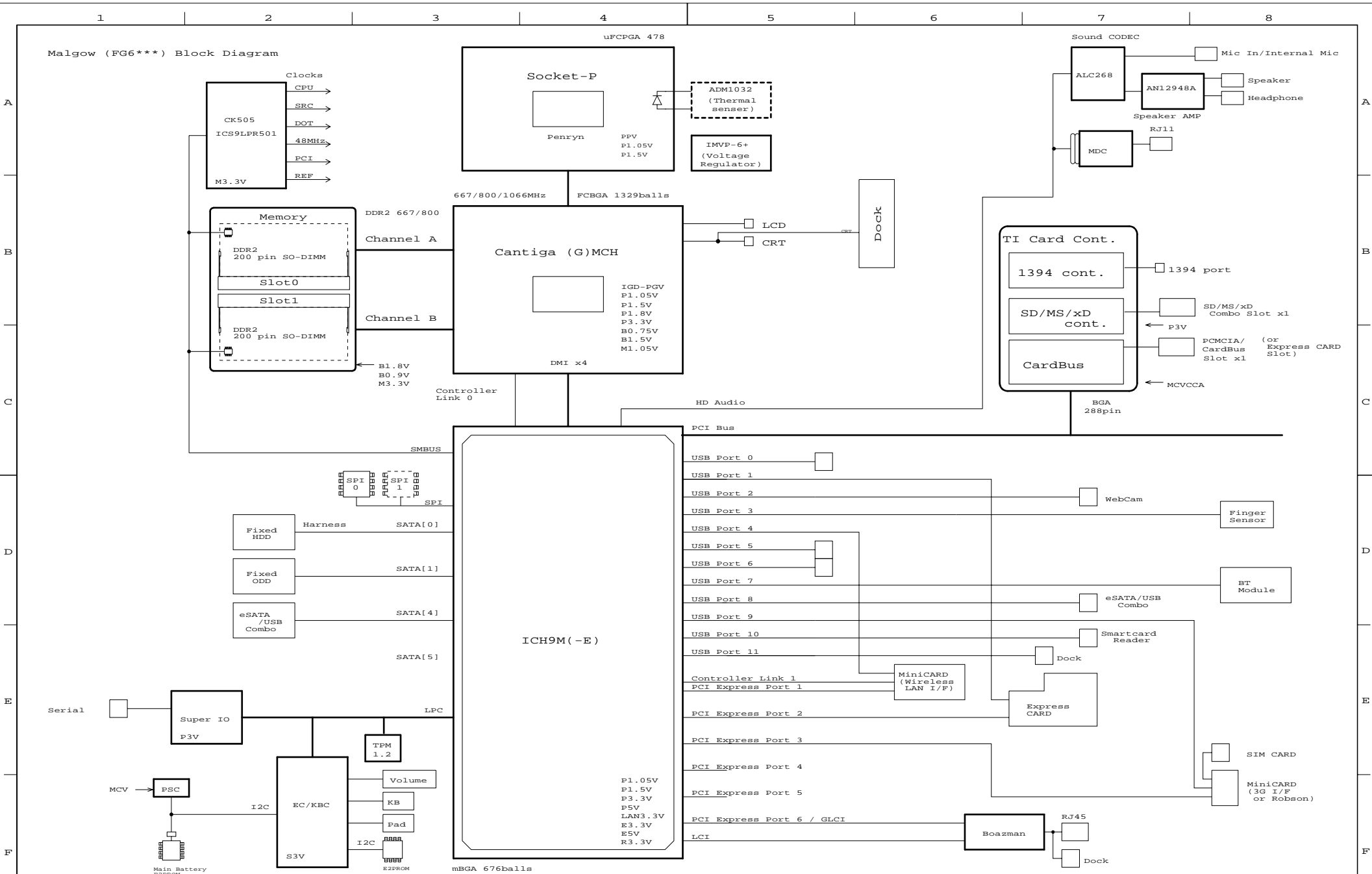
PAGE	SH.	FUNCTION	REV	PAGE	SH.	FUNCTION	REV
101	804	[PS]E5V/E3V	00	126	956	FGGUS* I/F	00
102	805	[PS]LOAD SW1	00	127	957	FGGSM* I/F	00
103	806	[PS]E10V	00	128	990	Accessories	00
104	807	[PS]CURRENT AMP	00	129			
105	808	[PS] CHARGE	00	130			
106	809	[PS]S5V/S3V	00	131			
107	810	[PS]AnalogInput	00	132			
108	811	[PS]PSC(1)	00	133			
109	812	[PS]PSC(2)	00	134			
110	813	[PS]CPUVCC	00	135			
111	815	[PS]1R8-E1V	00	136			
112	817	[PS]0R9-B0V	00	137			
113	818	[PS]LOAD SW2	00	138			
114	819	[PS]1R05-P1V	00	139			
115	820	[PS]1R5-P1V	00	140			
116	821	[PS]1R05M-E1V	00	141			
117	822	[PS]RTCVCC	00	142			
118	823	[PS]FANVCC(0)	00	143			
119	841	[PS]LAN-PS	00	144			
120	950	FGGSW* I/F	00	145			
121	951	RGB Unit I/F	00	146			
122	952	FGGSR* I/F	00	147			
123	953	FGGSN* I/F	00	148			
124	954	WebCam I/F	00	149			
125	955	FGGFS* I/F	00	150			

REV. MARK	CONTENTS	APPROVED BY	REVISED BY	REGISTERED
00	ISSUE(Release for CS)	2008/03/04 [PC 設] (PC 設 2) K.Yamaki	2008/03/04 [PC 設] (PC 設 1) T.Ichimura	
00	ISSUE(Release for VP)	2008/05/06 [PC 設] (PC 設 6) K.Yamakmori	2008/05/05 [PC 設] (PC 設 7) T.Ichimura	

APPROVED BY	CHECKED BY	DESIGNED BY	TITLE	TOTAL	PAGE NO.	REV.MARK	DRAWING.NO.
		T.Ichimura	FG6IN1		002	00	360065815

2008.05.05

19:11



DESIGNED BY

T. Ichimura

2008.05.05

19:11

TITLE

FG6IN1

FUNCTION

BLOCK DIAGRAM

SH.NO.

003

PAGE NO.

003

REV.MARK

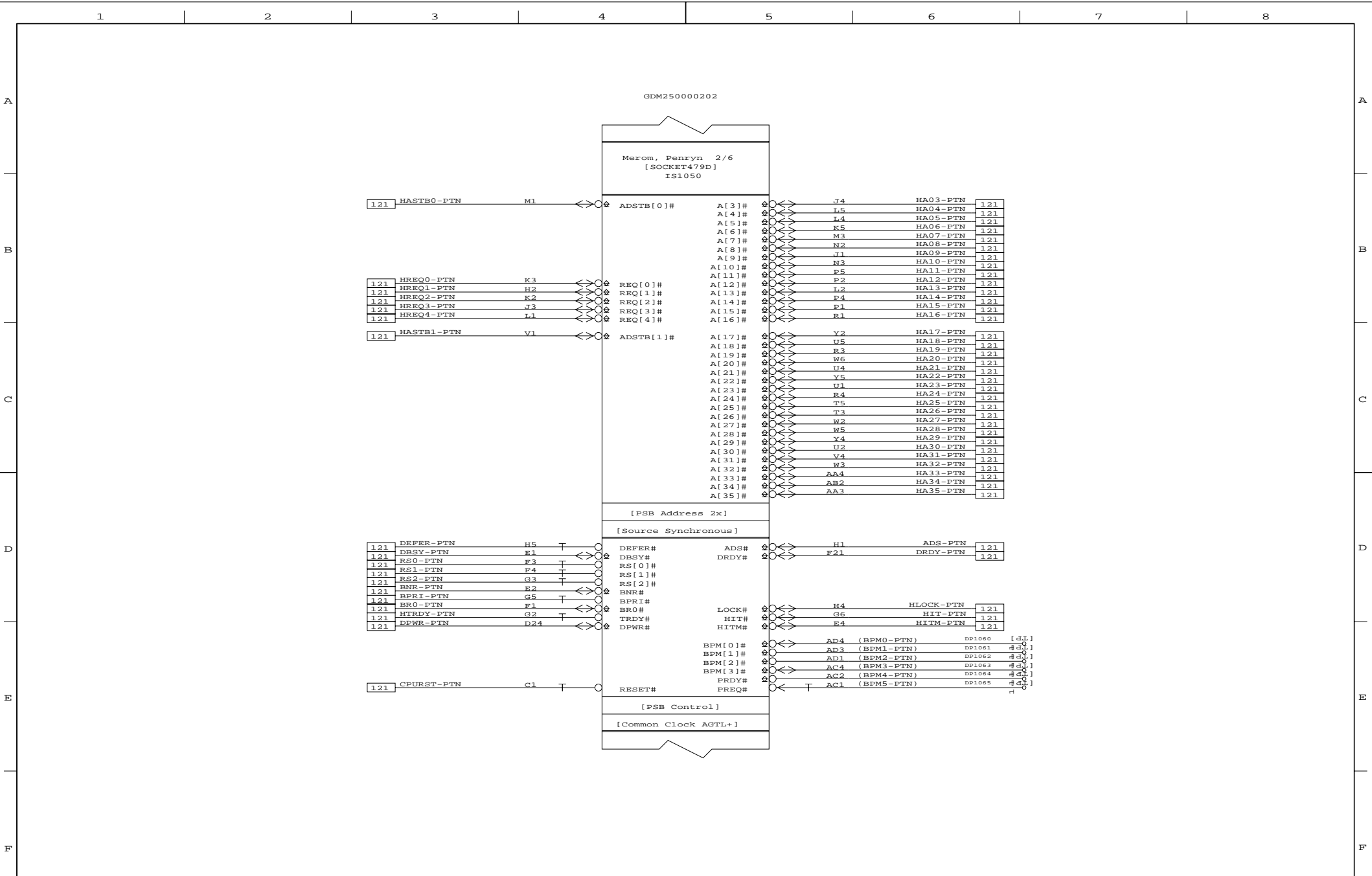
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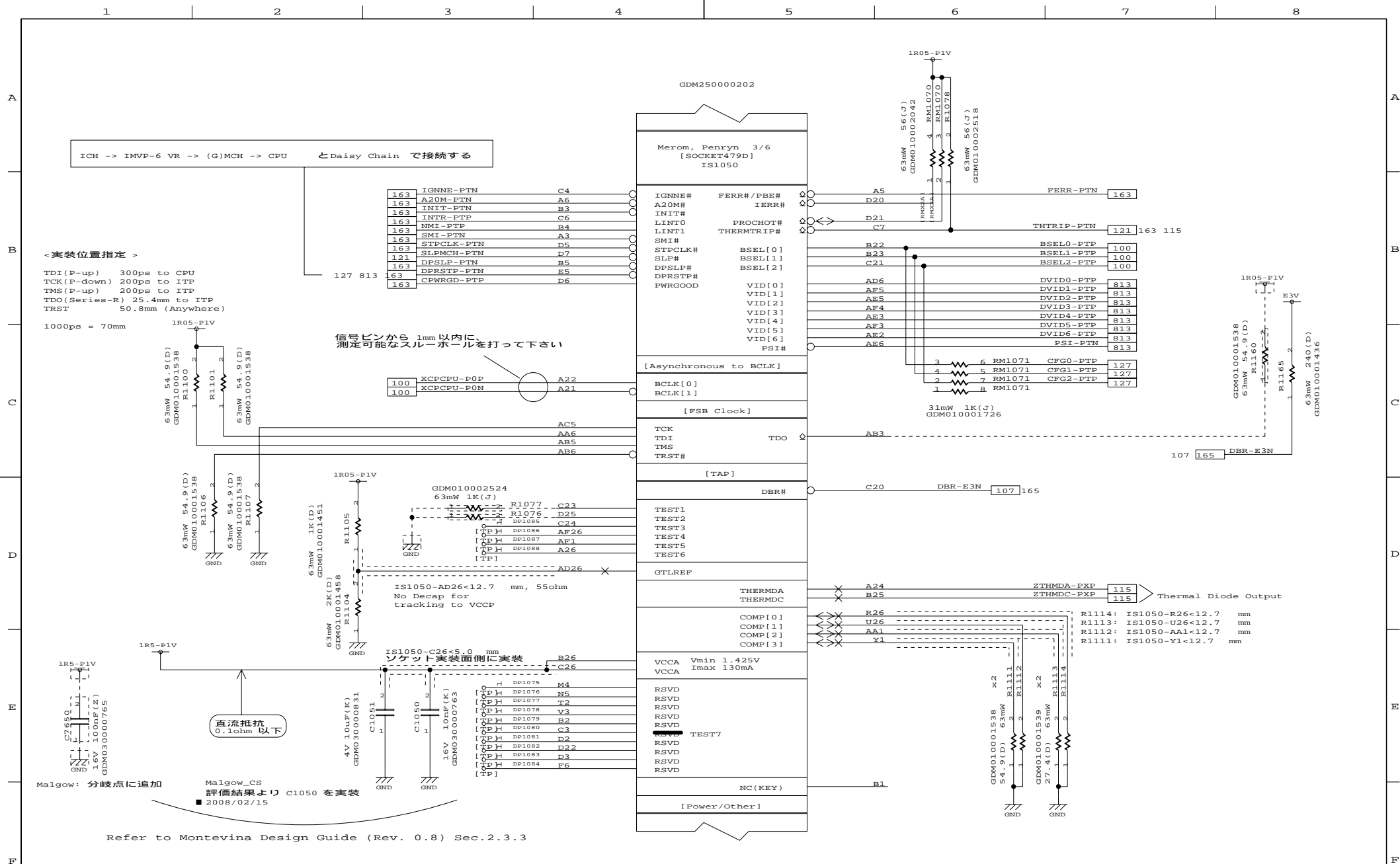
DRAWING.NO.

360065815

TOSHIBA CORPORATION

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T. Ichimura

2008.05.05

TITLE

FG6IN1

19:11

FUNCTION

SOCKET-P 3

SH.NO.

107

PAGE NO.

008

REV.MARK

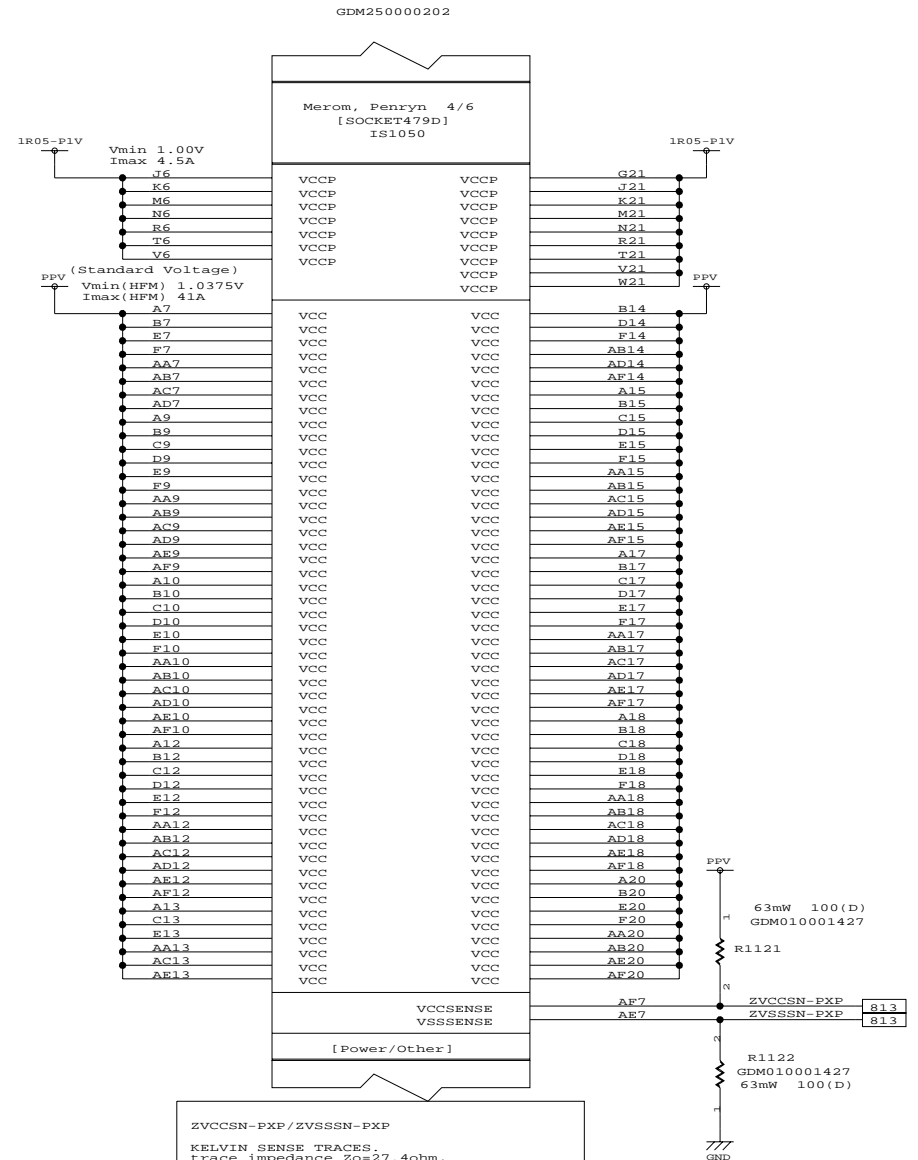
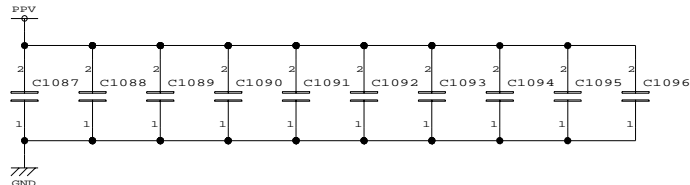
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DRAWING.NO.

360065815

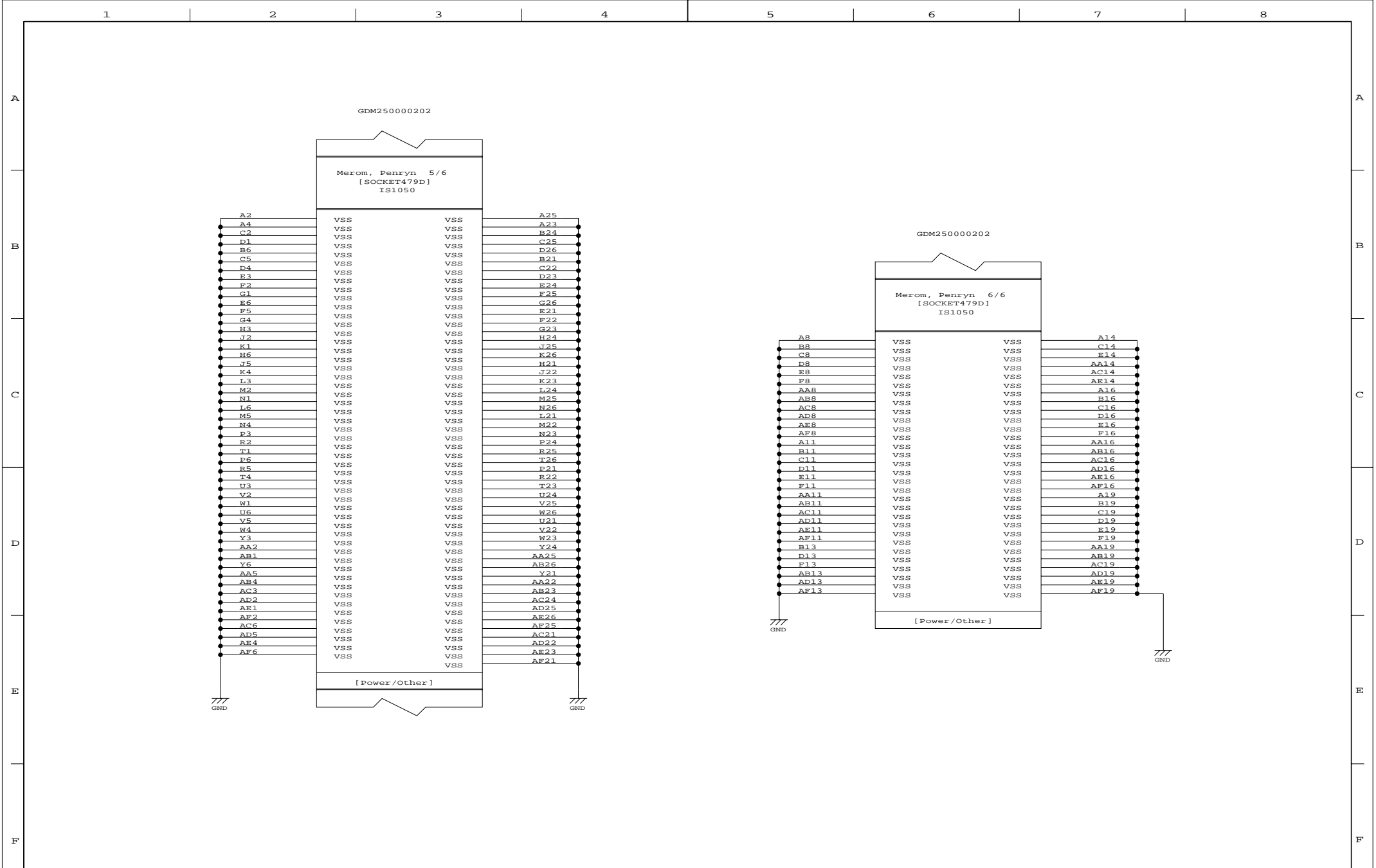
TOSHIBA CORPORATION

POSCAP H=2.1mm
電源ピンより 15mm 以内に配置

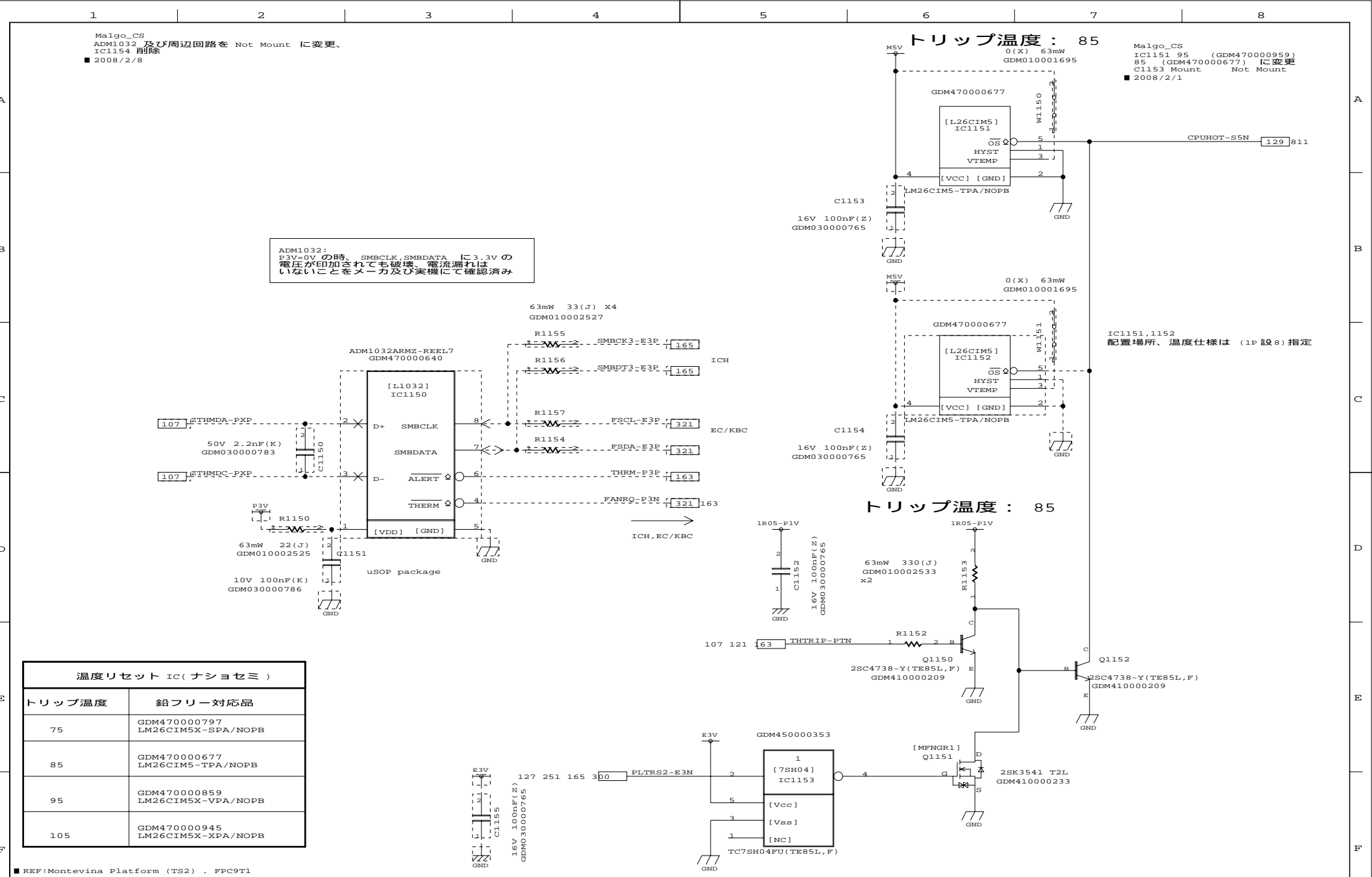


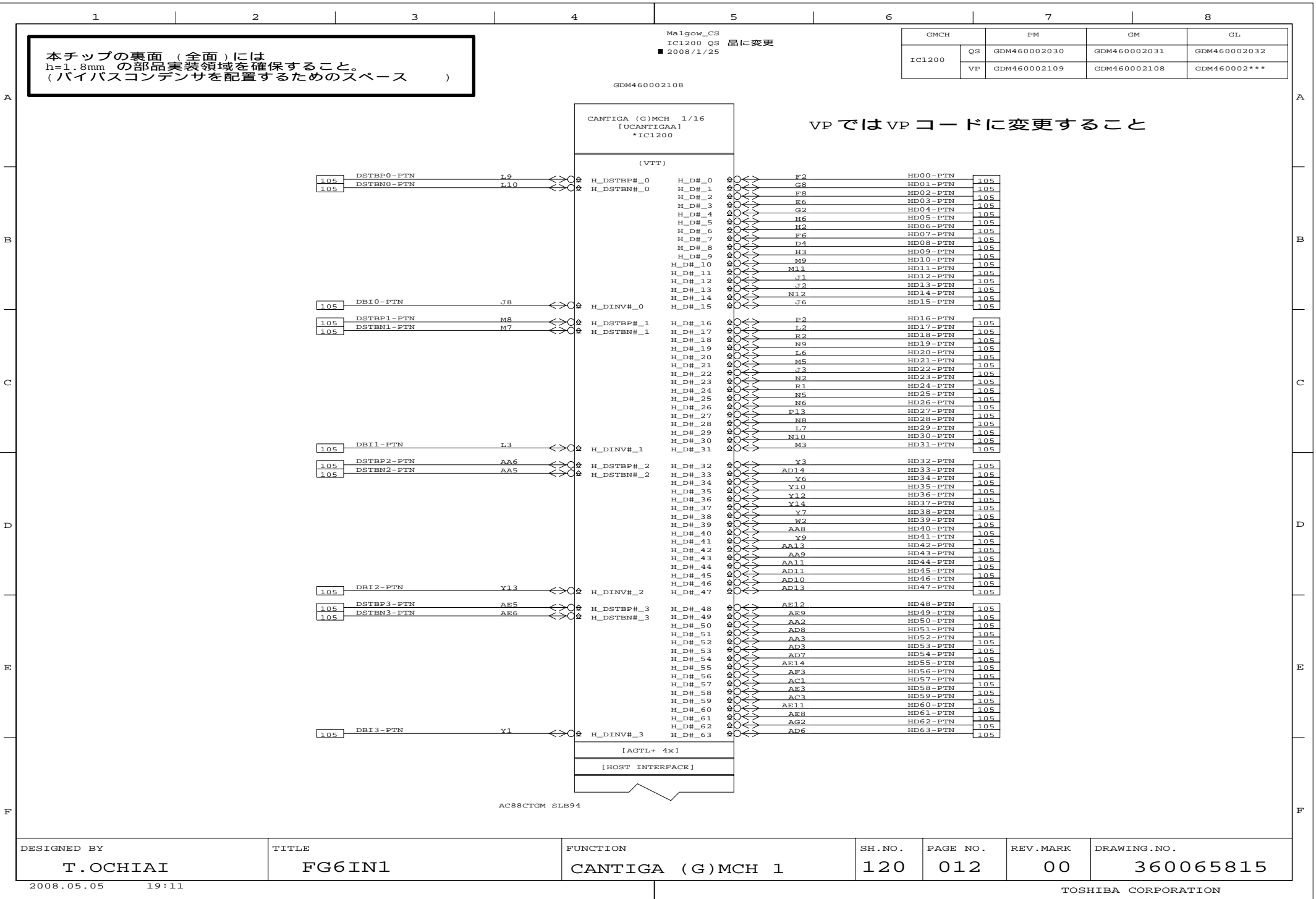
ZVCCSN-PXP/ZVSSSN-PXP

KELVIN SENSE TRACES.
trace impedance $Z_o=27.4\text{ohm}$,
trace width 18mils on 7mils spacing.
length matched to within 25mils.



DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
T.Ichimura	FG6IN1	SOCKET-P	109	010	00	360065815
2008.05.05	19:11					TOSHIBA CORPORATION







A

B

C

D

E

F

A

B

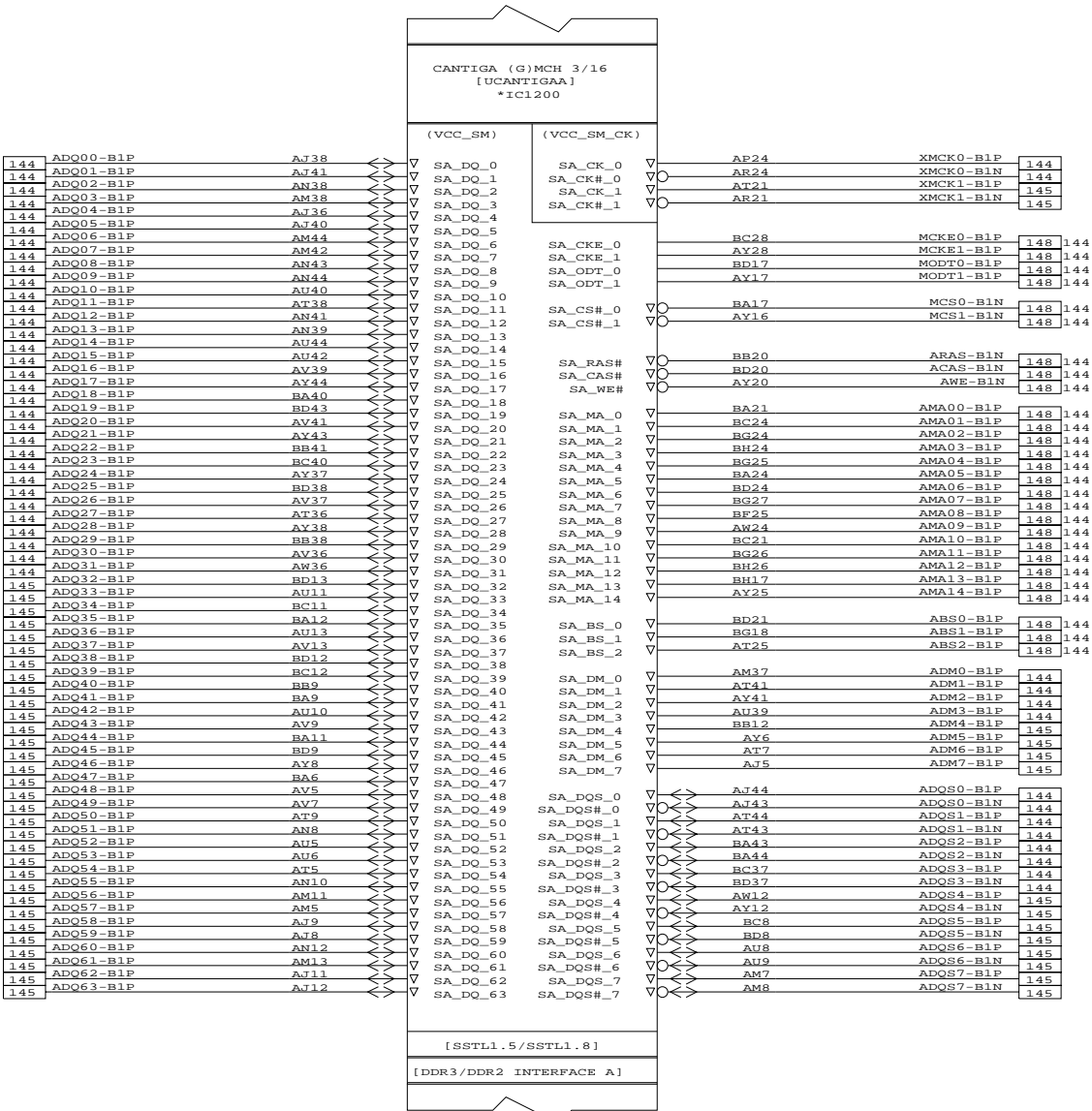
C

D

E

F

Malgow_CS
IC1200 QS 品に変更
GDM460002108 ■ 2008/1/25



DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
T.OCHIAI/T.Naruse	FG6IN1	CANTIGA (G)MCH 3	122	014	00	360065815

DESIGNED BY T.OCHIAI/T.Naruse		TITLE FG6IN1		FUNCTION CANTIGA (G)MCH 4		SH.NO. 123	PAGE NO. 015	REV.MARK 00	DRAWING.NO. 360065815
2008.05.05 19:11				TOSHIBA CORPORATION					

AMT	YES	NO
R1216	Not Mount	Mount
W1205	Mount	Not mount

Malgow_CS
SM_PWROK 回路削除， GND 直結ジャンパに変更
■ 2007/12/18

Malgow_CS
信号名変更 : PWROKM-E3P PWROKC-E3P
2008/2/8

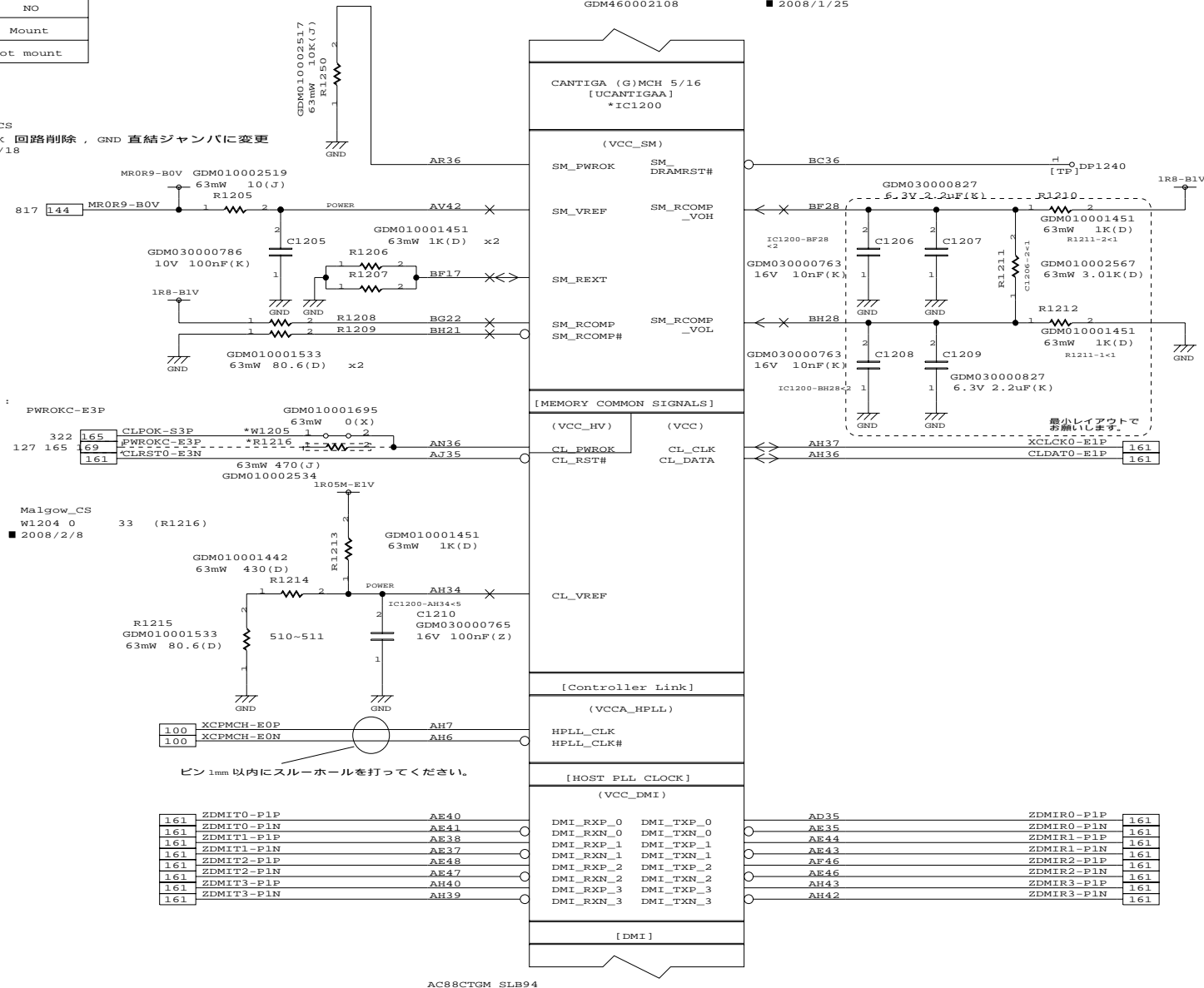
	PWROKCE3P	
	322	165
127	165	169
		161

Malgow_CS
W1204 0
■ 2008/2/8

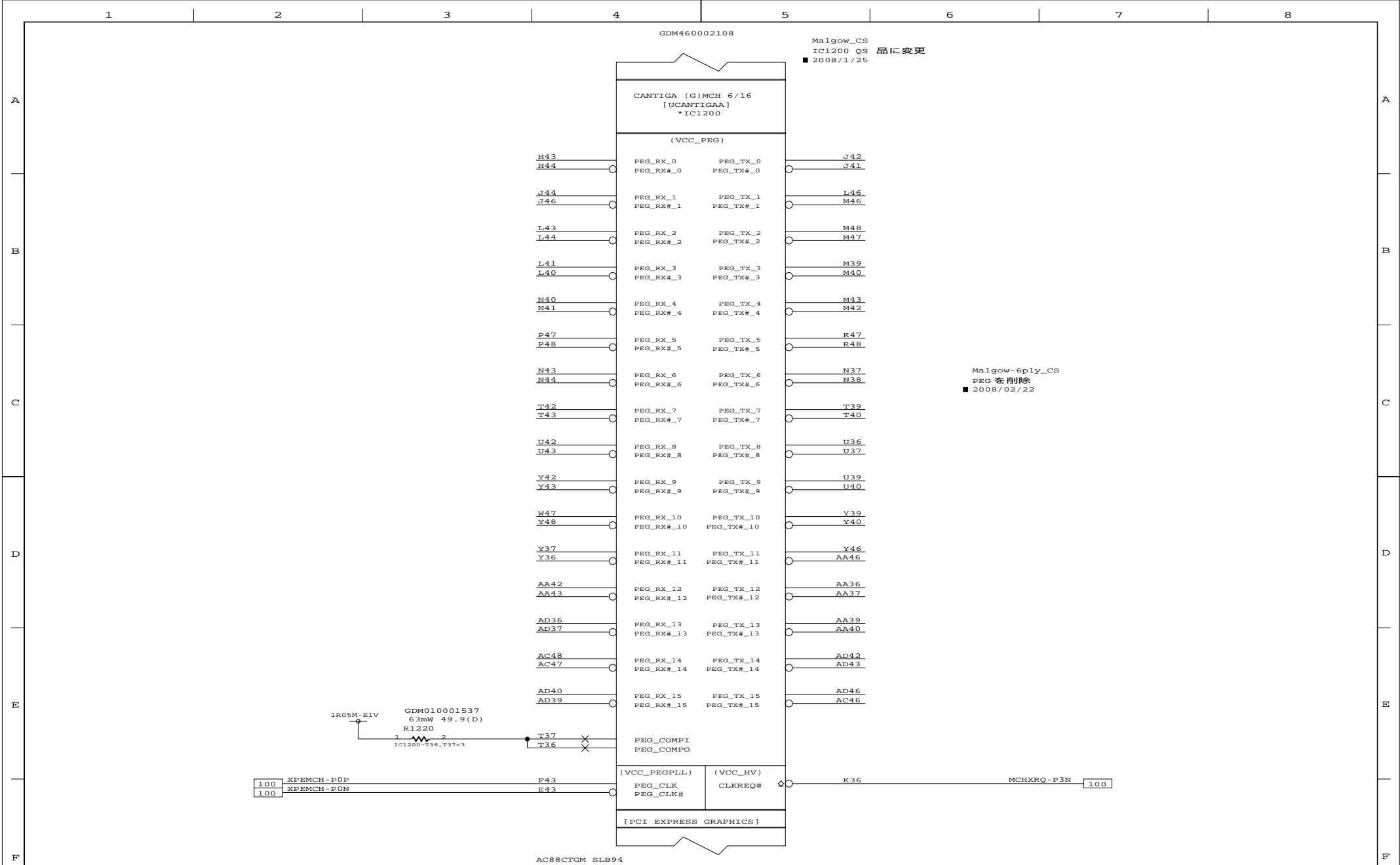
ピン 1mm 以内にスルーホールを打ってください。

GDM460002108

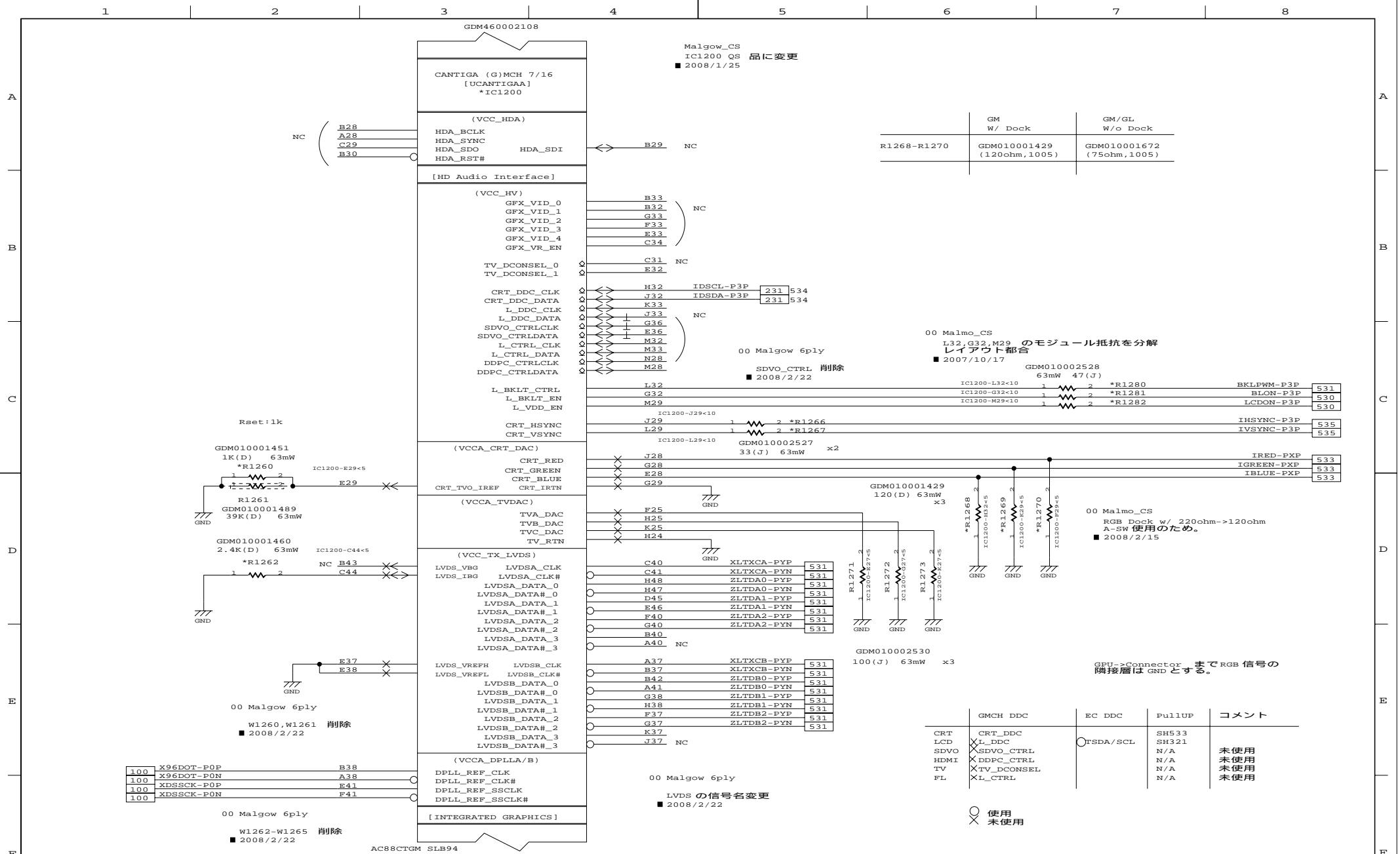
Malgow_CS
IC1200 QS 品に変更
■ 2008/1/25



360065815



DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
T.Naruse	FG6IN1	CANTIGA (G)MCH 6	125	017	00	360065815
2008.05.05	19:11					TOSHIBA CORPORATION



REF:Montevina(TS) . FPC9T1

DESIGNED BY

T.Naruse

2008.05.05

19:11

TITLE

FG6IN1

FUNCTION

CANTIGA (G)MCH 7

SH.NO.

126

PAGE NO.

018

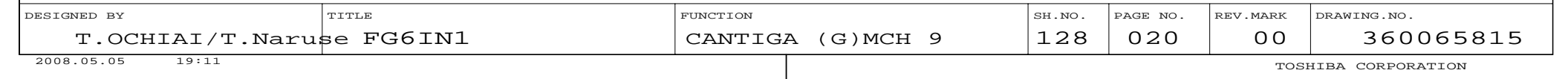
REV.MARK

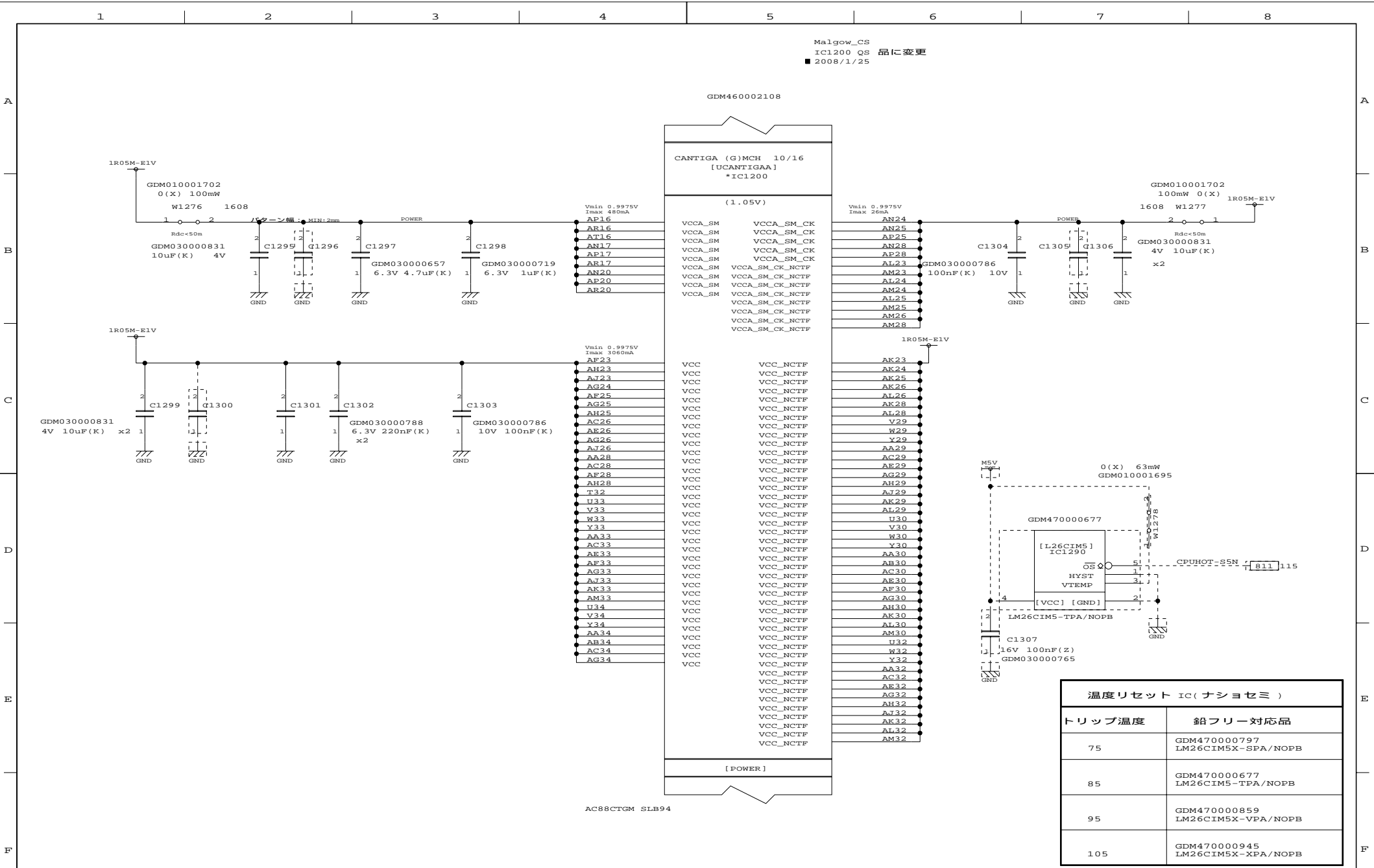
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DRAWING.NO.

360065815

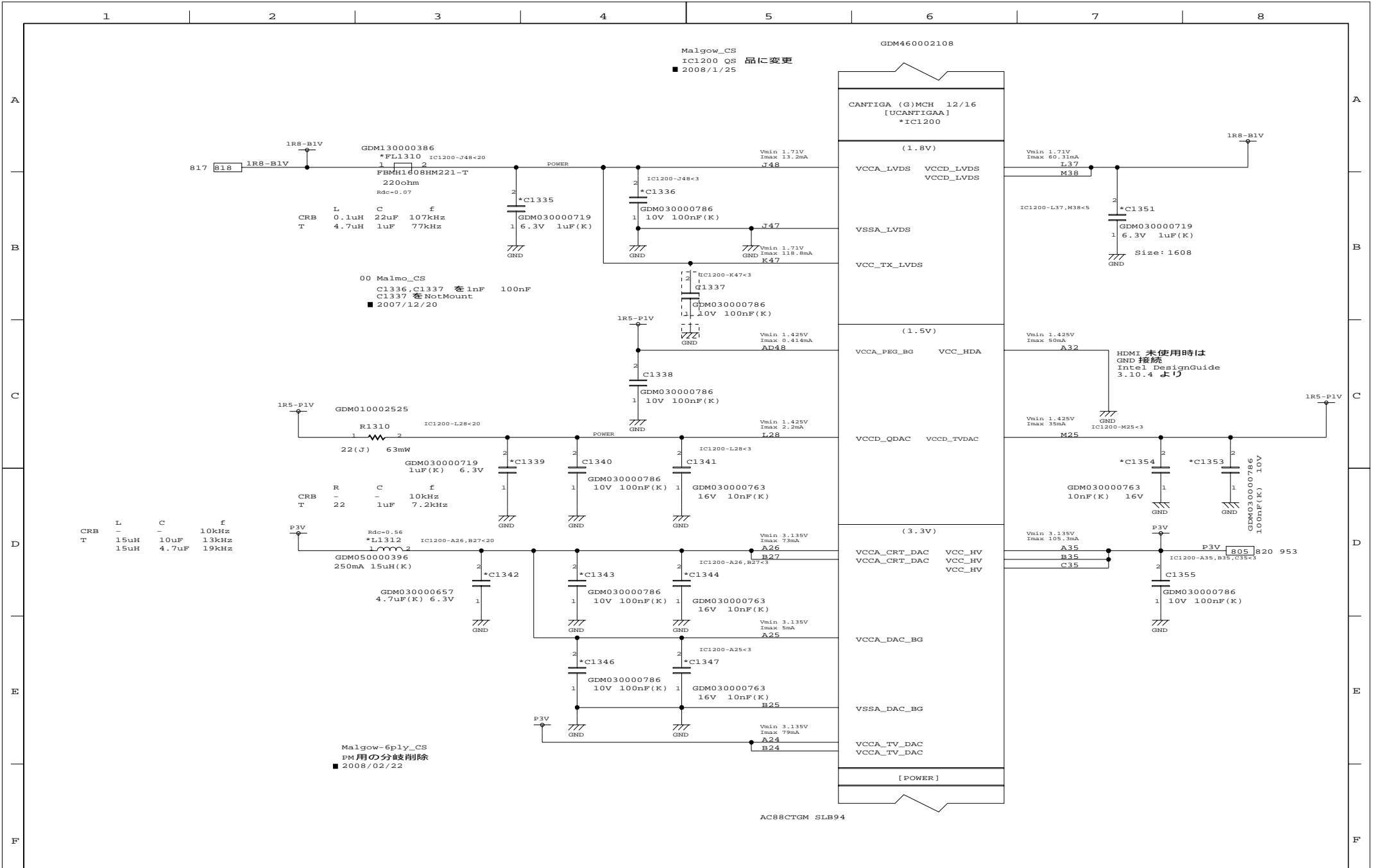
TOSHIBA CORPORATION





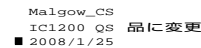
温度リセット IC(ナショセミ)	
トリップ温度	鉛フリー対応品
75	GDM470000797 LM26CIM5X-SPA/NOPB
85	GDM470000677 LM26CIM5-TPA/NOPB
95	GDM470000859 LM26CIM5X-VPA/NOPB
105	GDM470000945 LM26CIM5X-XPA/NOPB

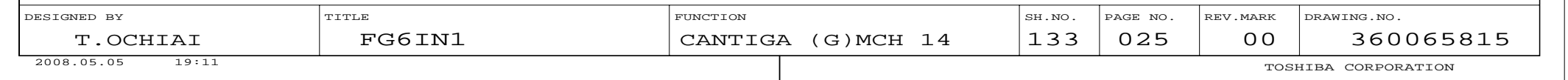




DESIGNED BY T.Naruse/T.OCHIAI	TITLE FG6IN1	FUNCTION CANTIGA (G)MCH 12	SH.NO. 131	PAGE NO. 023	REV.MARK 00	DRAWING.NO. 360065815
2008.05.05	19:11					TOSHIBA CORPORATION

互換性設定	Int.	Ext.	Reference	Int.	Ext.
VCC_AXX VCC_AXX_NCTF	1.05V	GND	C1380-C1387 C1388	Mount GDM030000786 (100nF, 1005)	NotMount GDM010001695 (0ohm, 1005)
IGD-PGV	1.05V	NC	FL9251, 9252 (Sb. 819)	Mount	NotMount





Malgows_CS

ME_JTAG コネクタをテストパッドに置換え

■ 2008/02/15

GDM010001695
63mW 0(X) x4

	METDO-E1P	*W1390	1	2
127	METCK-E1P	*R1350	1	2
127	METMS-E1P	*R1352	1	2
127	METDI-E1P	*R1351	1	2

動作確認でき次第削除

キーボード下等、アクセス可能な位置に配置して下さい。

1R05M-E1V

M-E3V

C1395

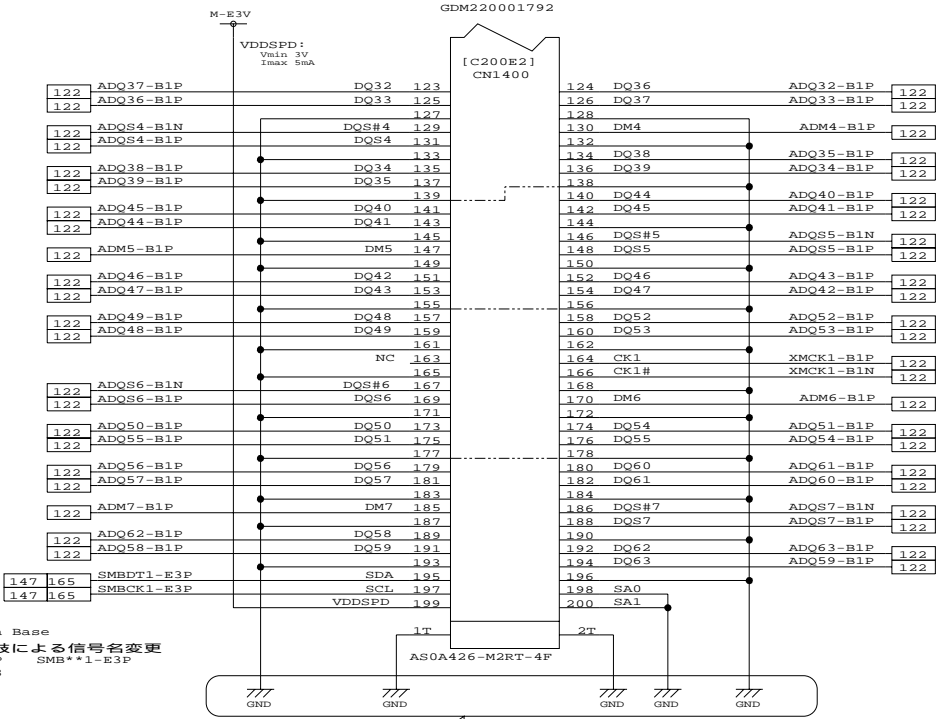
C1393

GDM030000786
1.0V 100nF(K) x2

2008.05.05 19:11

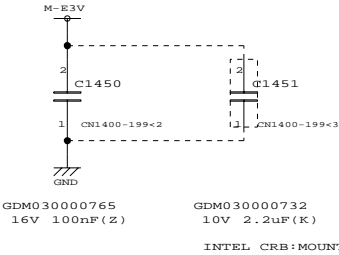
Hamilton10 CS
CN1410 もコネクタリスト
Rev01 にあわせる
GDM220001130 GDM220001792
■ 2006/9/26

5.2 Height Reverse



Santa Rosa Base
SM Bus 分岐による信号名変更
SMB***-E3P SMB*1-E3P
■ 2006/09/08

各バスに対して Reference Plane として、
ベタ GND を隣接層に配置してください
内層にバスを引く場合は両側の隣接層を
GND ベタにしてください



REF:Santa Rosa Base . FSRBS0

DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
T.Ichimura	FG6IN1	DDR2 SO-DIMM (Ch.A) 2	145	028	00	360065815

2008.05.05 19:11

Hamilton10 CS
CN1410 をコネクタリスト
RevD1 に合わせる GDM220001793
■ 2006/9/26

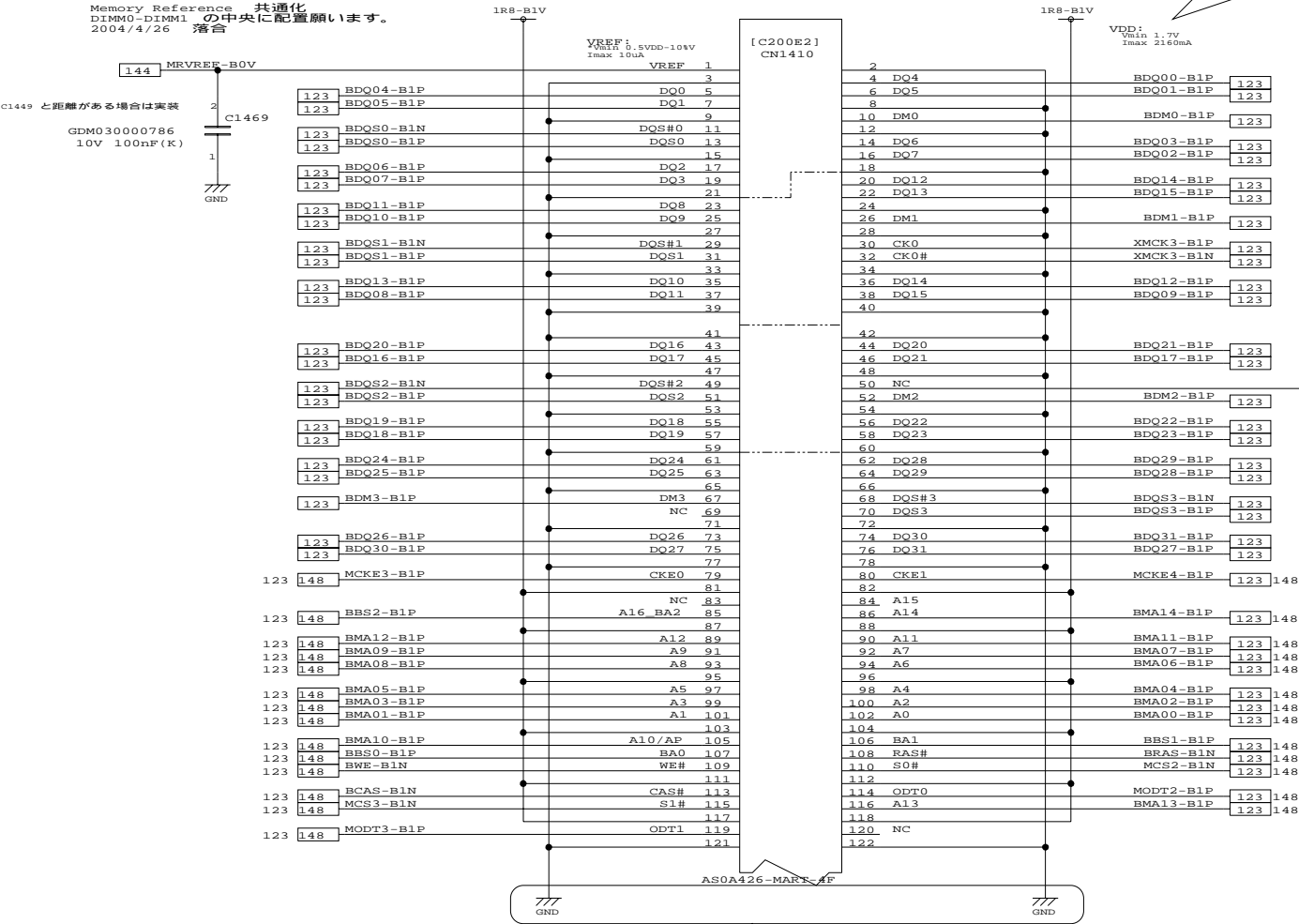
9.2 Height Reverse

Imax に関しては Samsung 1GB:
M470T2953BS0-CD5 のSpec. を使用

Imax:
Burst Write:2.16A
Auto Refresh:2.12A

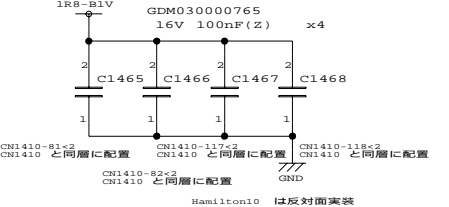
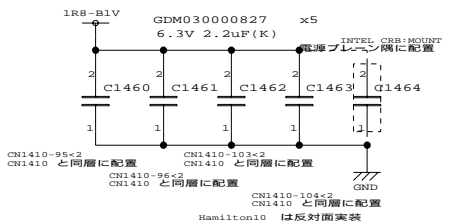
Malgow_CS
レイアウトより C1469 を実装
■ 2008/02/19

Memory Reference 共通化
DIMM0-DIMM1 の中央に配置願います。
2004/4/26 落合



TSDIMB-P3N 127 (GMCH-PM_EXT_TS#_0)

Santa Rosa Base
バスコン配置検討
C1464 をN.Nに変更
■ 2006/8/22



各バスに対して Reference Plane として、
ベタGNDを隣接層に配置してください
内層にバスを引く場合は両側の隣接層を
GNDベタにしてください

DESIGNED BY T.Ichimura	TITLE FG6IN1	FUNCTION DDR2 SO-DIMM (Ch.B) 1	SH.NO. 146	PAGE NO. 029	REV.MARK 00	DRAWING.NO. 360065815
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A
B
C
D
E
F

A
B
C
D
E
F

TOSHIBA CONFIDENTIAL

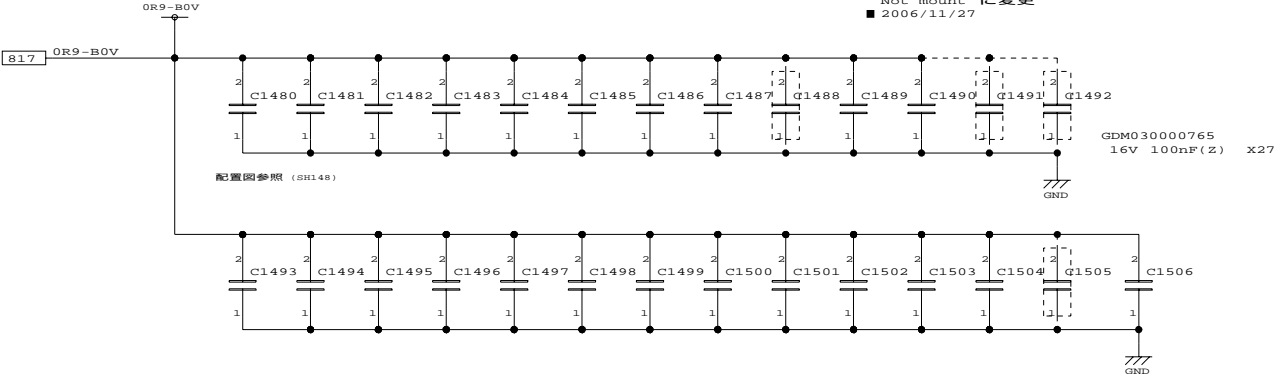
配置図参照 (SH148)

OR9-B0V:
VREF ± 0.04V
MAX 840mA

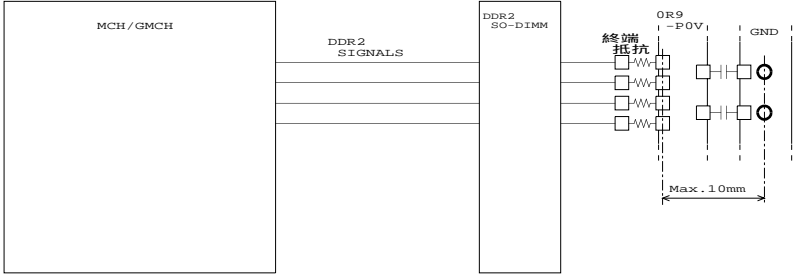
GDM010001721
31mW 56(J) X12

OR9-B0V

Newport10_CS
C1488,C1491,C1492,C1505
Not mount に変更
■ 2006/11/27



終端抵抗は、各信号の S O - D I M M 側終端に配置すること
信号 2 本当たり、コンデンサ 1 個配置すること
終端抵抗の Pad - (コンデンサ経由) - 内層 GND パターンまでの距離 : Max. 1.0mm



122	144	MCS1-B1N	1	8	RM1480
122	144	MODT1-B1P	2	7	RM1480
122	144	MODT0-B1P	3	6	RM1480
122	144	MCS0-B1N	4	5	RM1480
123	146	MODT2-B1P	1	8	RM1481
123	146	MCS2-B1N	2	7	RM1481
123	146	MODT3-B1P	3	6	RM1481
123	146	MCS3-B1N	4	5	RM1481
122	144	AMA04-B1P	1	8	RM1483
122	144	AMA14-B1P	2	7	RM1483
122	144	AMA11-B1P	3	6	RM1483
122	144	AMA09-B1P	4	5	RM1483
122	144	ABS0-B1P	1	8	RM1484
122	144	ACAS-B1N	2	7	RM1484
122	144	AWE-B1N	3	6	RM1484
122	144	AMA13-B1P	4	5	RM1484
123	146	BMA06-B1P	1	8	RM1486
123	146	BMA04-B1P	2	7	RM1486
123	146	BMA11-B1P	3	6	RM1486
123	146	BMA14-B1P	4	5	RM1486
123	146	BRAS-B1N	1	8	RM1487
123	146	BMA01-B1P	2	7	RM1487
123	146	BBS1-B1P	3	6	RM1487
123	146	BMA00-B1P	4	5	RM1487
122	144	AMA12-B1P	1	8	RM1489
122	144	AMA08-B1P	2	7	RM1489
122	144	AMA05-B1P	3	6	RM1489
122	144	AMA07-B1P	4	5	RM1489
122	144	AMA03-B1P	1	8	RM1490
122	144	AMA02-B1P	2	7	RM1490
122	144	AMA00-B1P	3	6	RM1490
122	144	AMA10-B1P	4	5	RM1490
123	146	BMA07-B1P	1	8	RM1491
123	146	BMA05-B1P	2	7	RM1491
123	146	BMA02-B1P	3	6	RM1491
123	146	BMA03-B1P	4	5	RM1491
122	144	ABS1-B1P	1	8	RM1488
122	144	ARAS-B1N	2	7	RM1488
122	144	AMA01-B1P	3	6	RM1488
122	144	AMA06-B1P	4	5	RM1488
123	146	BMA10-B1P	1	8	RM1492
123	146	BBS0-B1P	2	7	RM1492
123	146	BWE-B1N	3	6	RM1492
123	146	BCAS-B1N	4	5	RM1492
123	146	BBS2-B1P	1	8	RM1493
123	146	BMA12-B1P	2	7	RM1493
123	146	BMA09-B1P	3	6	RM1493
123	146	BMA08-B1P	4	5	RM1493
123	146	MCKE3-B1P	1	2	R1480
123	146	MCKE4-B1P	1	2	R1481
123	146	BMA13-B1P	1	2	R1482
122	144	ABS2-B1P	1	2	R1483
122	144	MCKE1-B1P	1	2	R1484
122	144	MCKE0-B1P	1	2	R1485

GDM010002518
63mW 56(J)

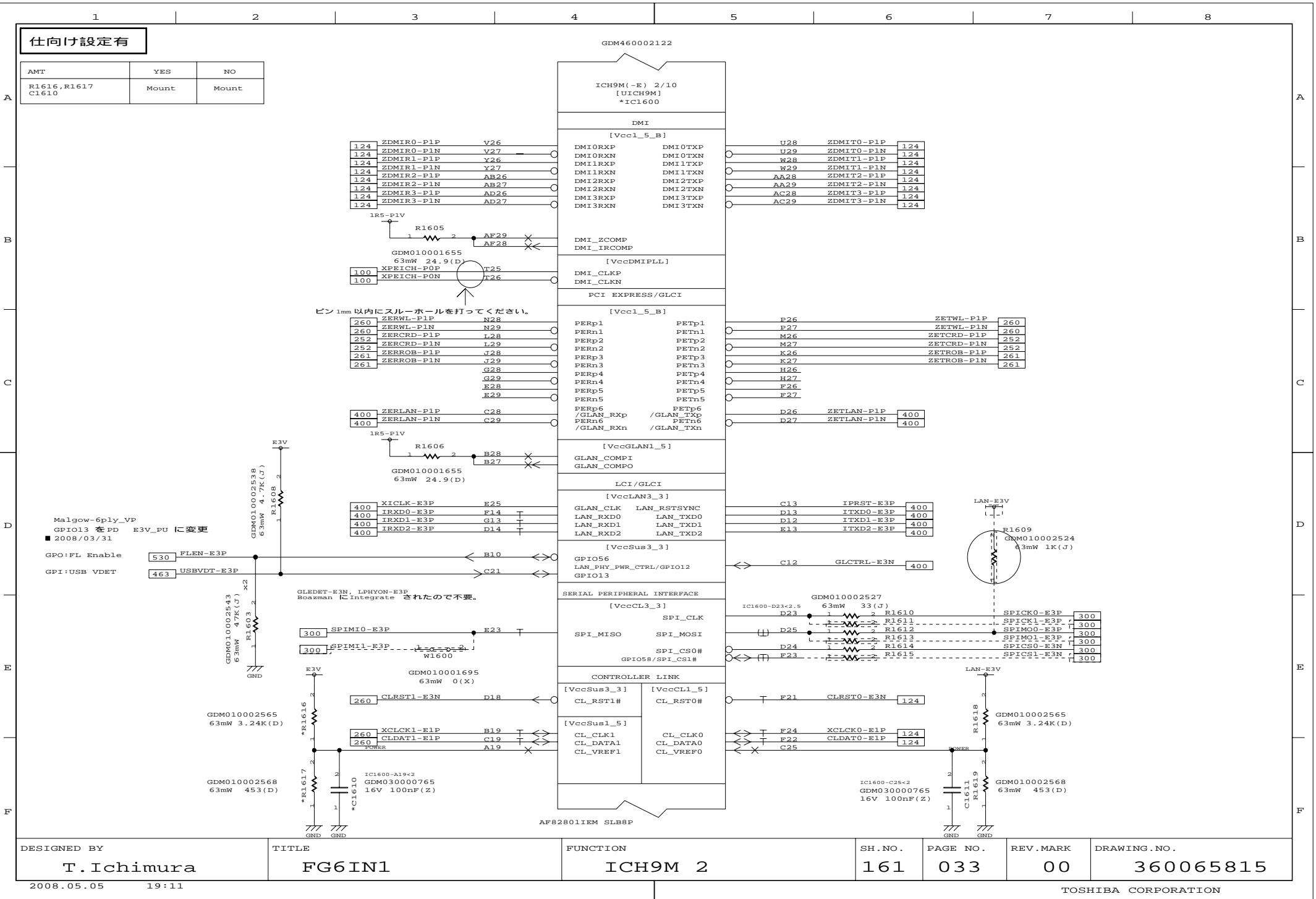
Ma1gow_CS
レイアウト 依頼によりモジュール抵抗を分解
■ 2008/02/10

■ REF: Santa Rosa Platform (TS) . FMC8T0

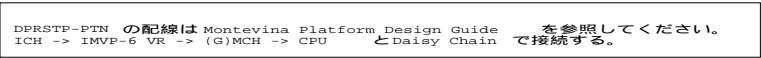
DESIGNED BY T.OCHIAI	TITLE FG6IN1	FUNCTION DDR2 TERMINATION	SH.NO. 148	PAGE NO. 031	REV.MARK 00	DRAWING.NO. 360065815
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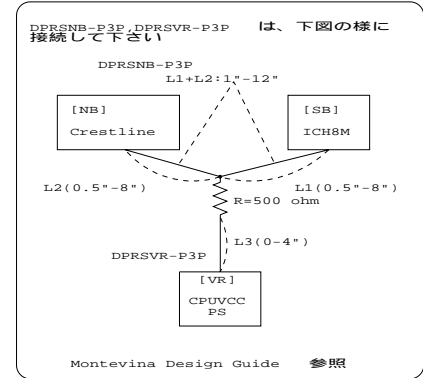
2008.05.05 19:11

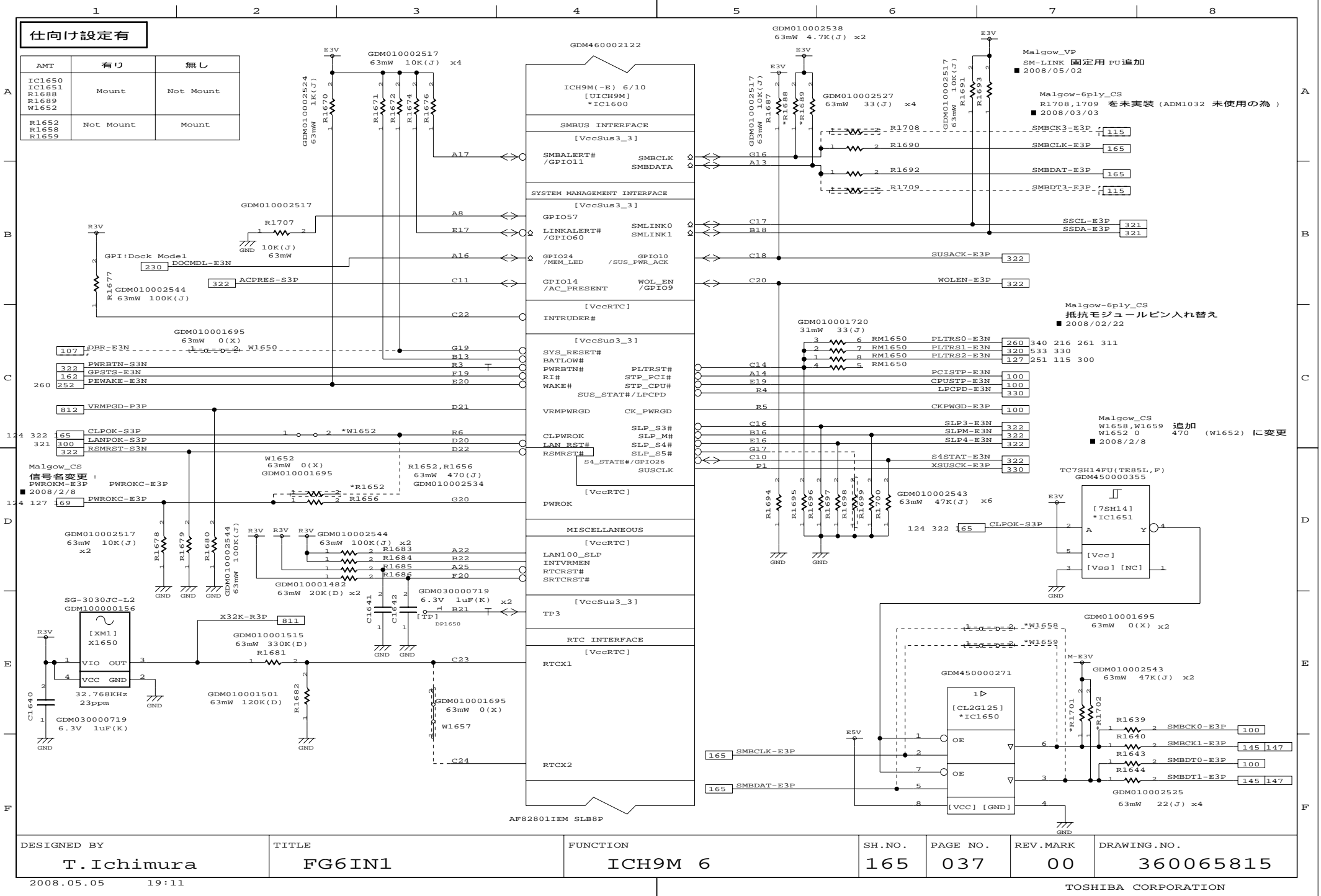
TOSHIBA CORPORATION

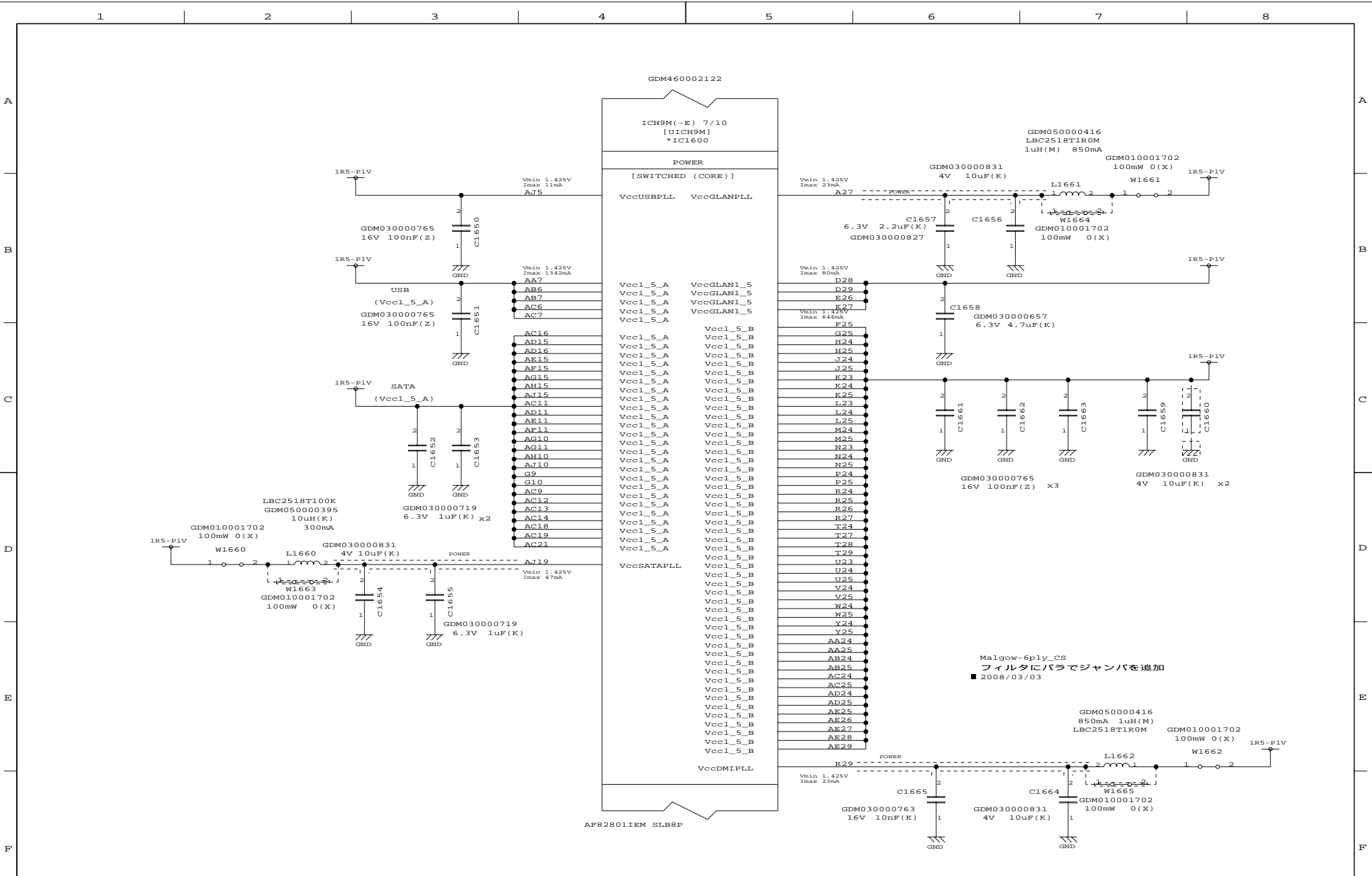




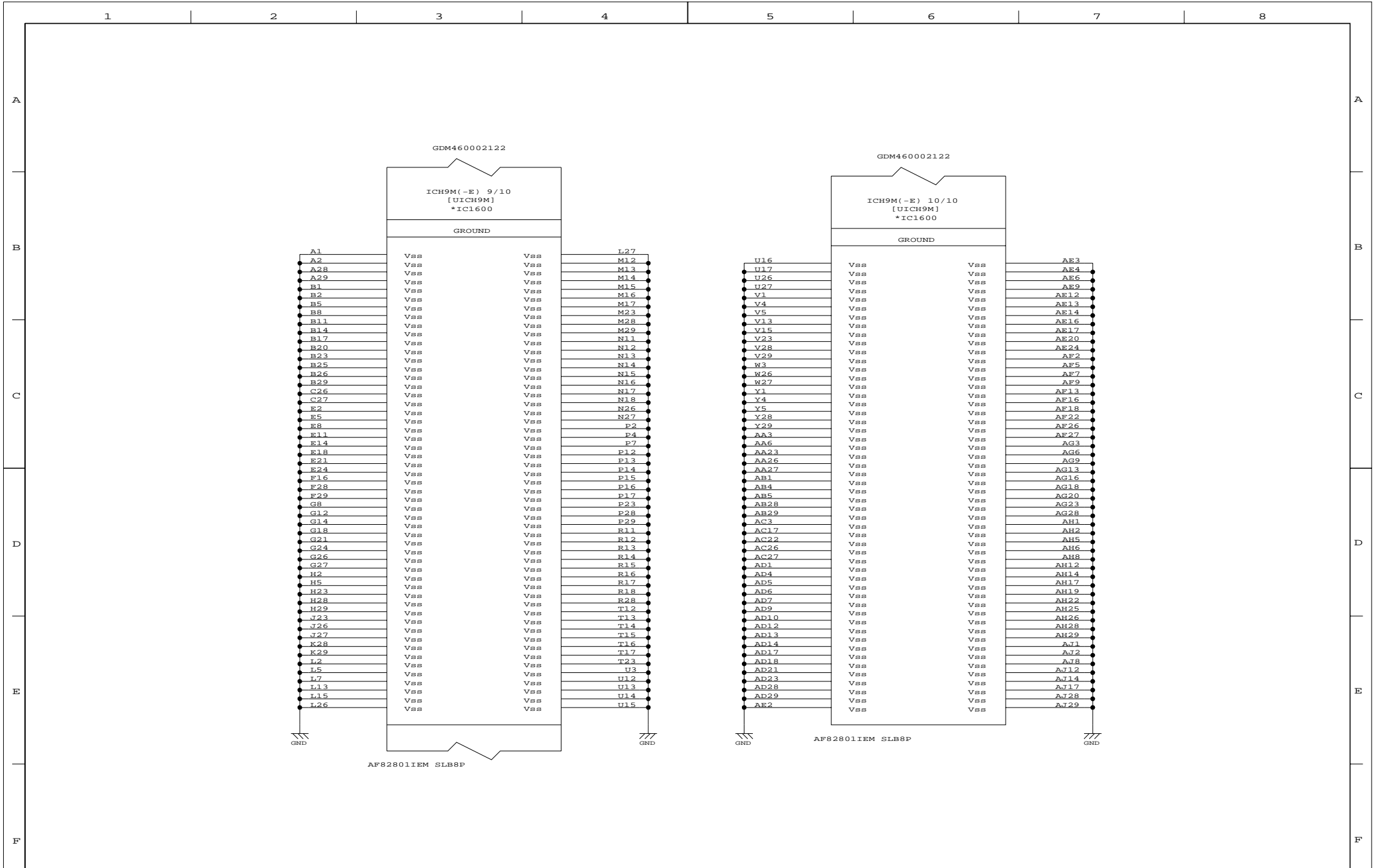




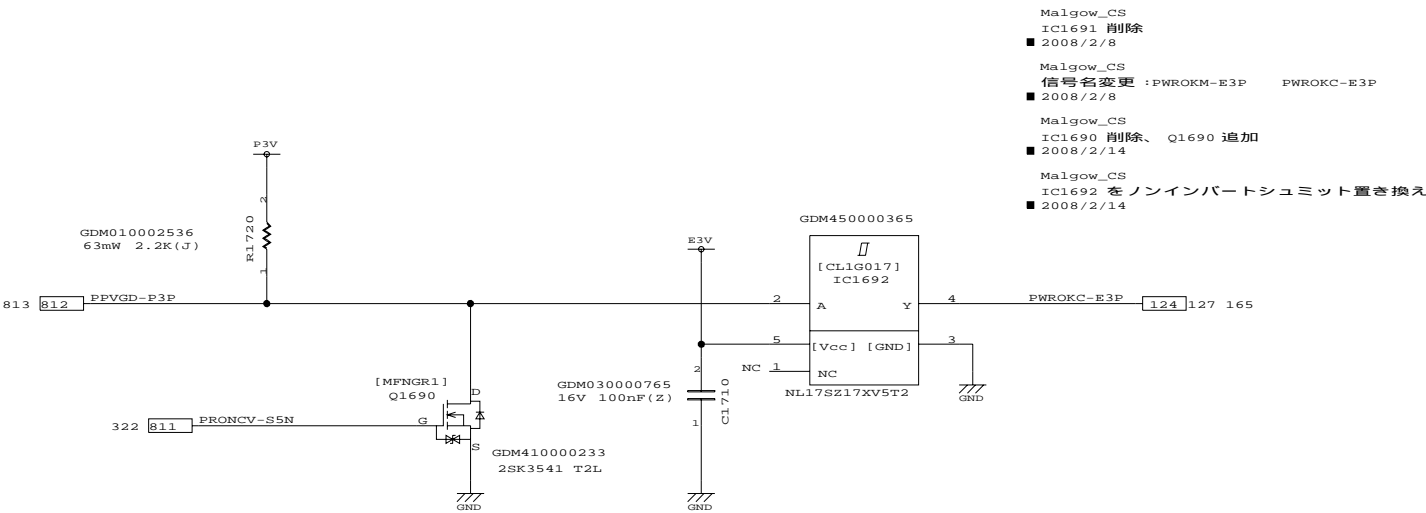








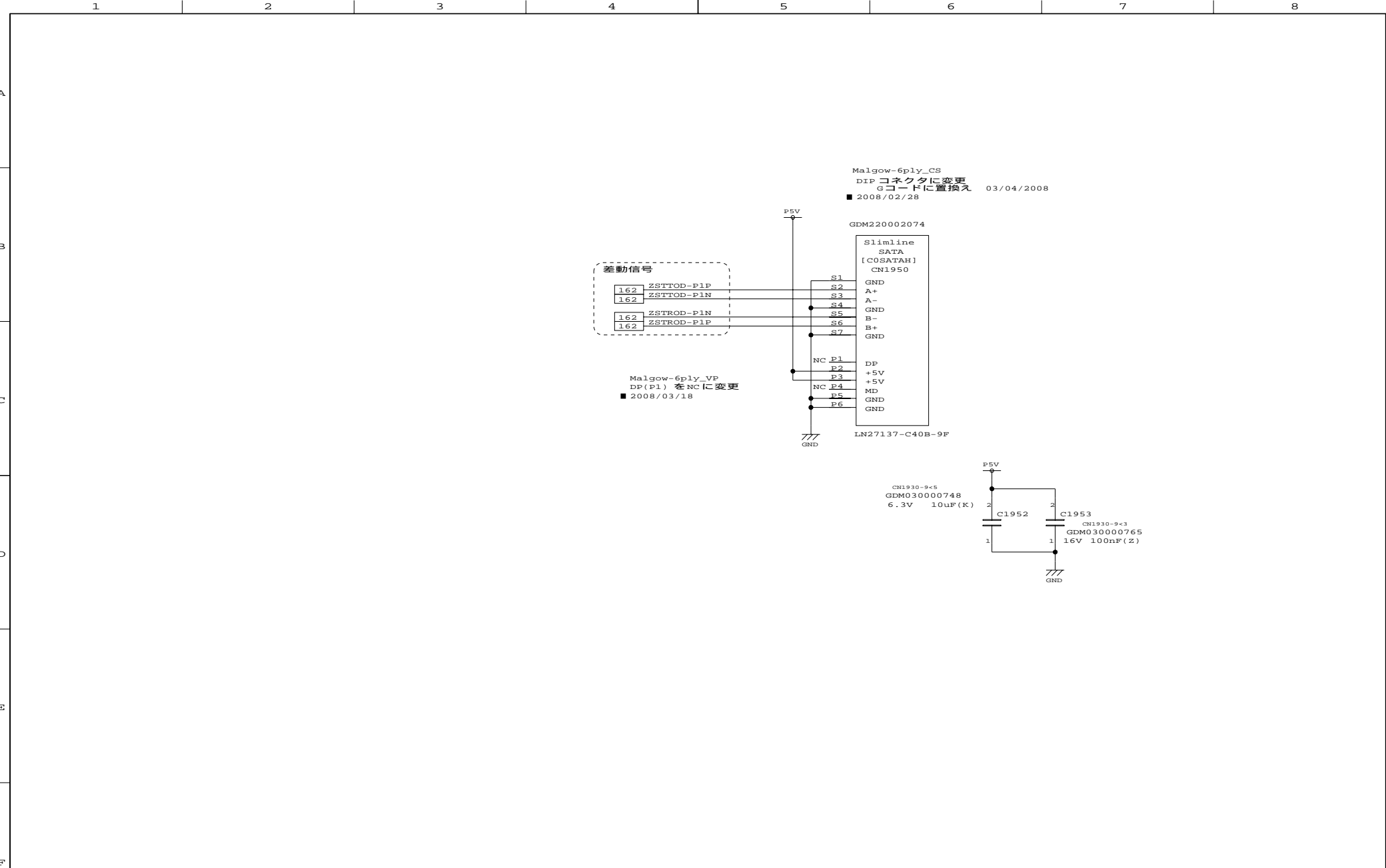
DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
T.Ichimura	FG6IN1	ICH9M 9	168	040	00	360065815
2008.05.05	19:11					TOSHIBA CORPORATION



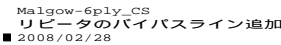
DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
T.OCHIAI	FG6IN1	IMVP-6+ POWER OK	169	041	00	360065815
2008.05.05	19:11	TOSHIBA CORPORATION				

[illegible]

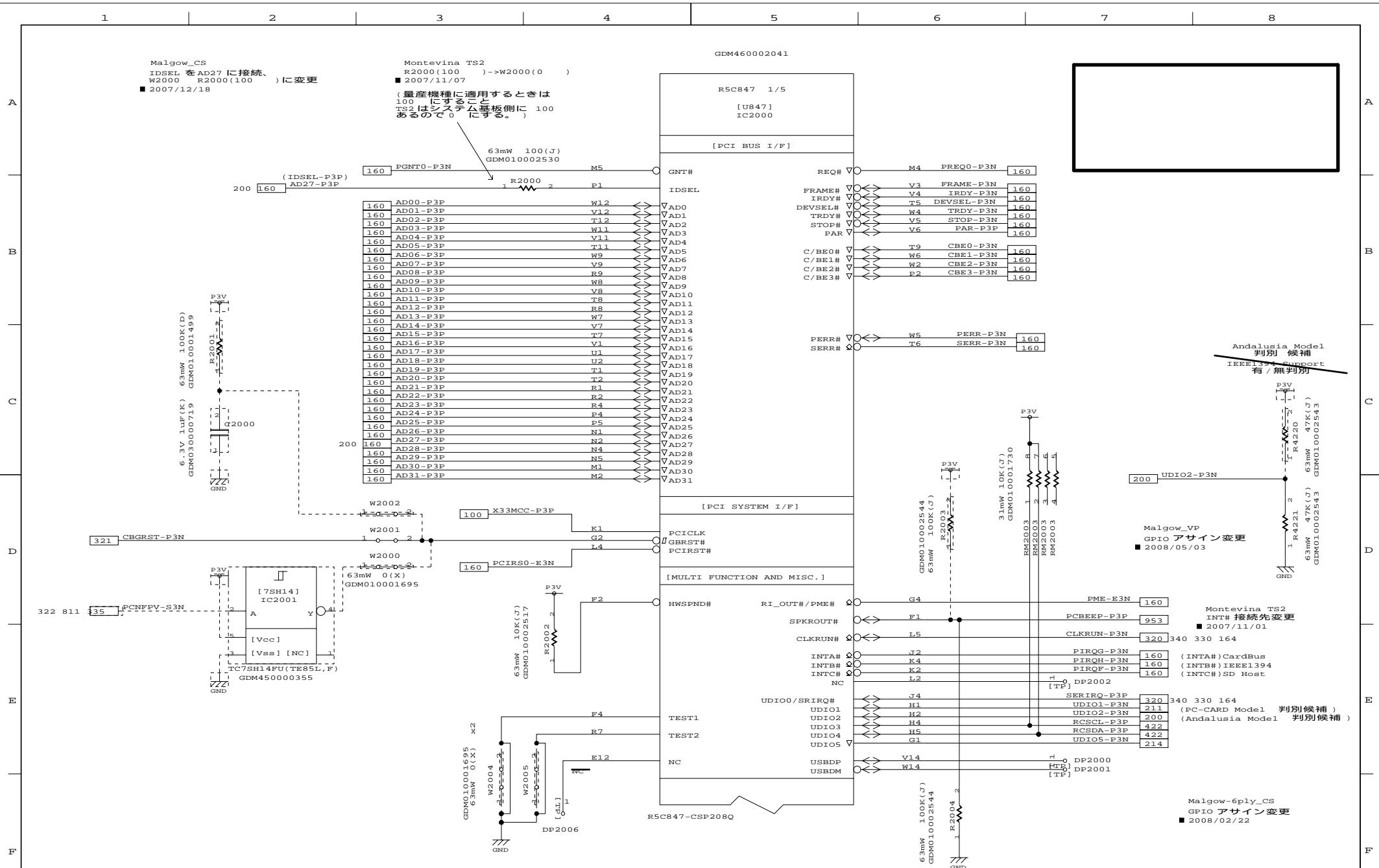
DESIGNED BY T.OCHIAI	TITLE FG6IN1	FUNCTION SATA HDD I/F	SH.NO. 190	PAGE NO. 042	REV.MARK 00	DRAWING.NO. 360065815
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DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
T.Ichimura	FG6IN1	SATA ODD I/F	195	043	00	360065815
2008.05.05	19:11					TOSHIBA CORPORATION

TOSHIBA CORPORATION





DESIGNED BY 2006/8/21

T. Ichimura

TITLE

FG6IN1

FUNCTION

CARD CONT 1

SH.NO.

200

PAGE NO.

046

REV.MARK

00

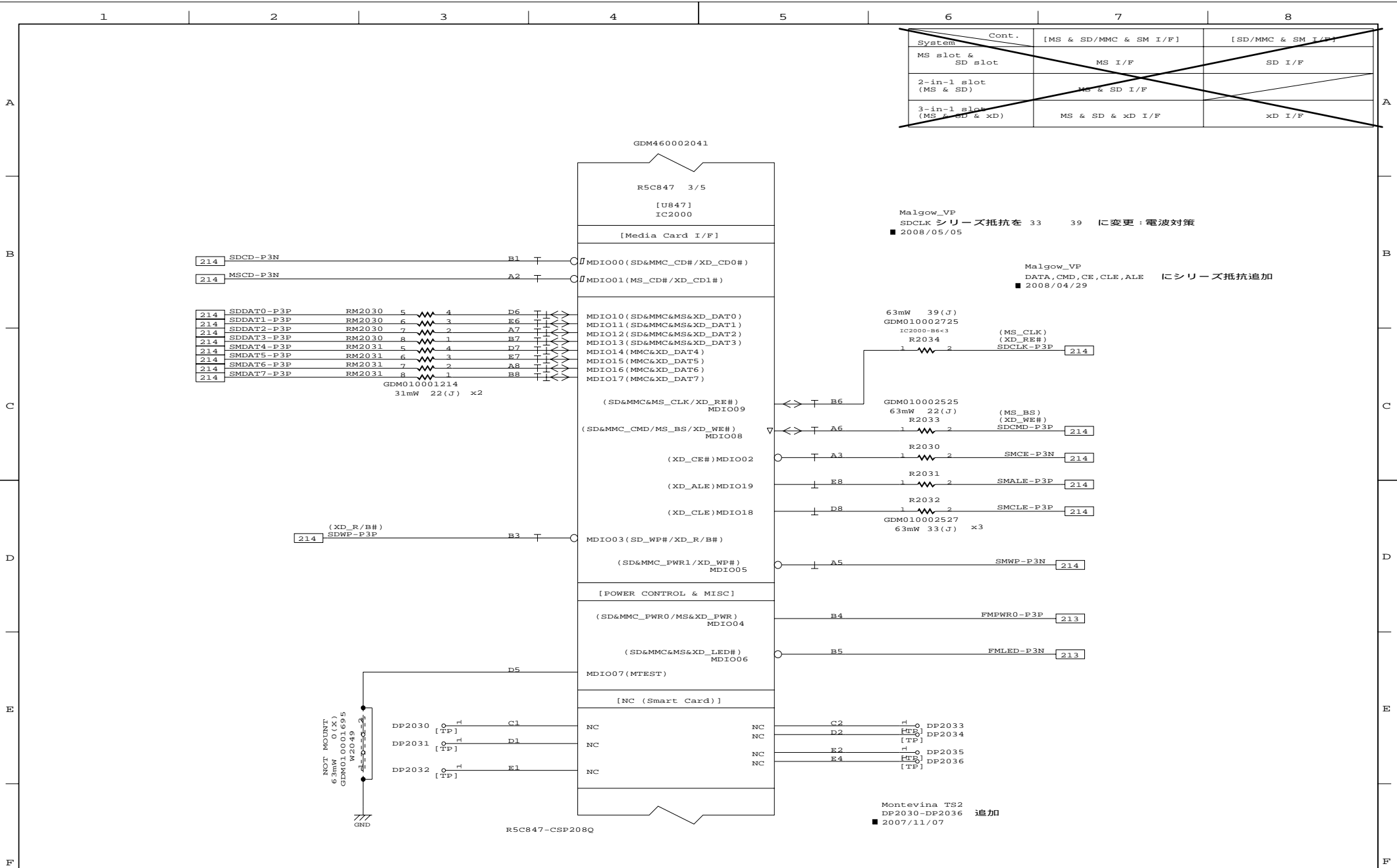
DRAWING.NO.

360065815

2008.05.05 19:11

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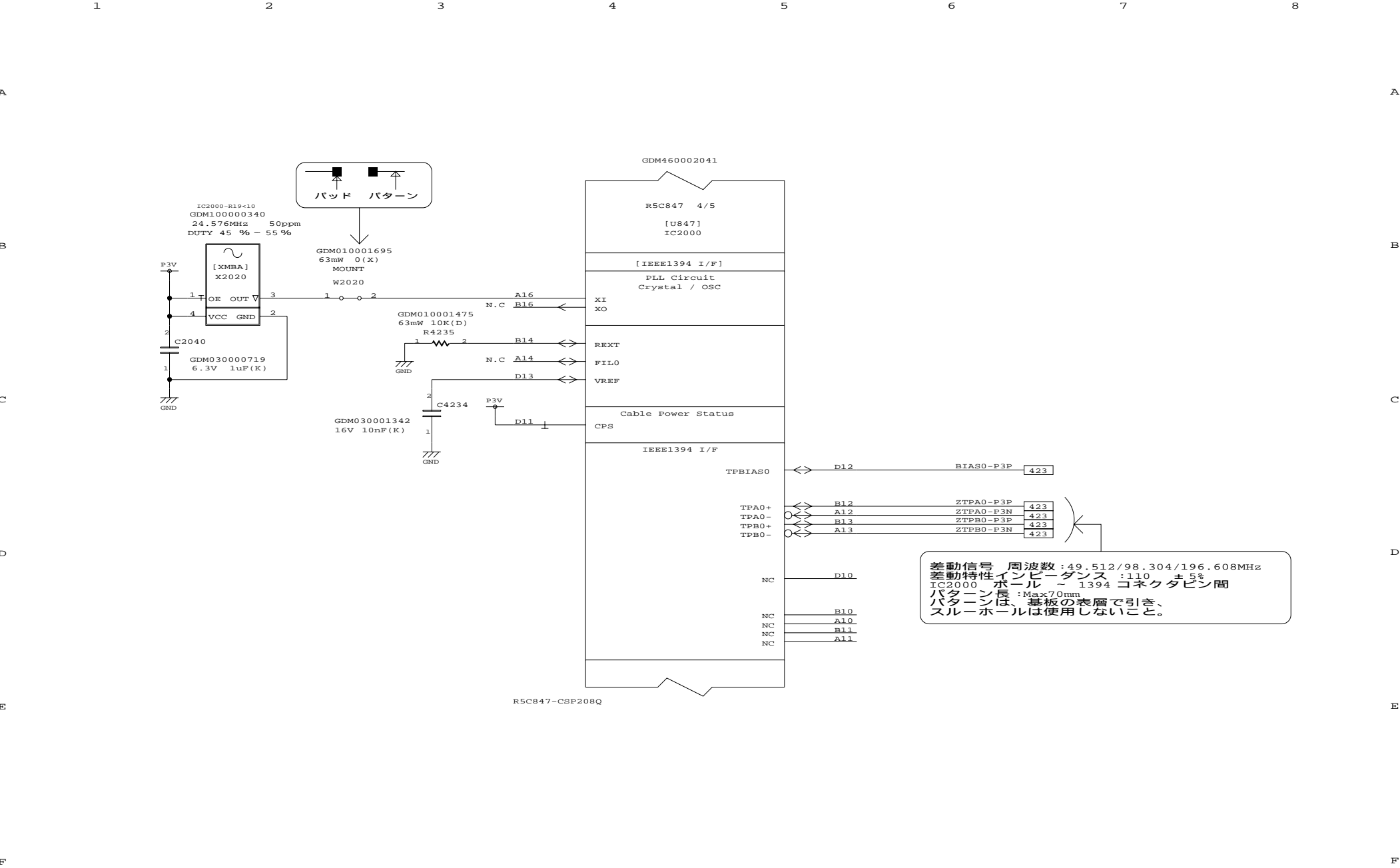
TOSHIBA CORPORATION

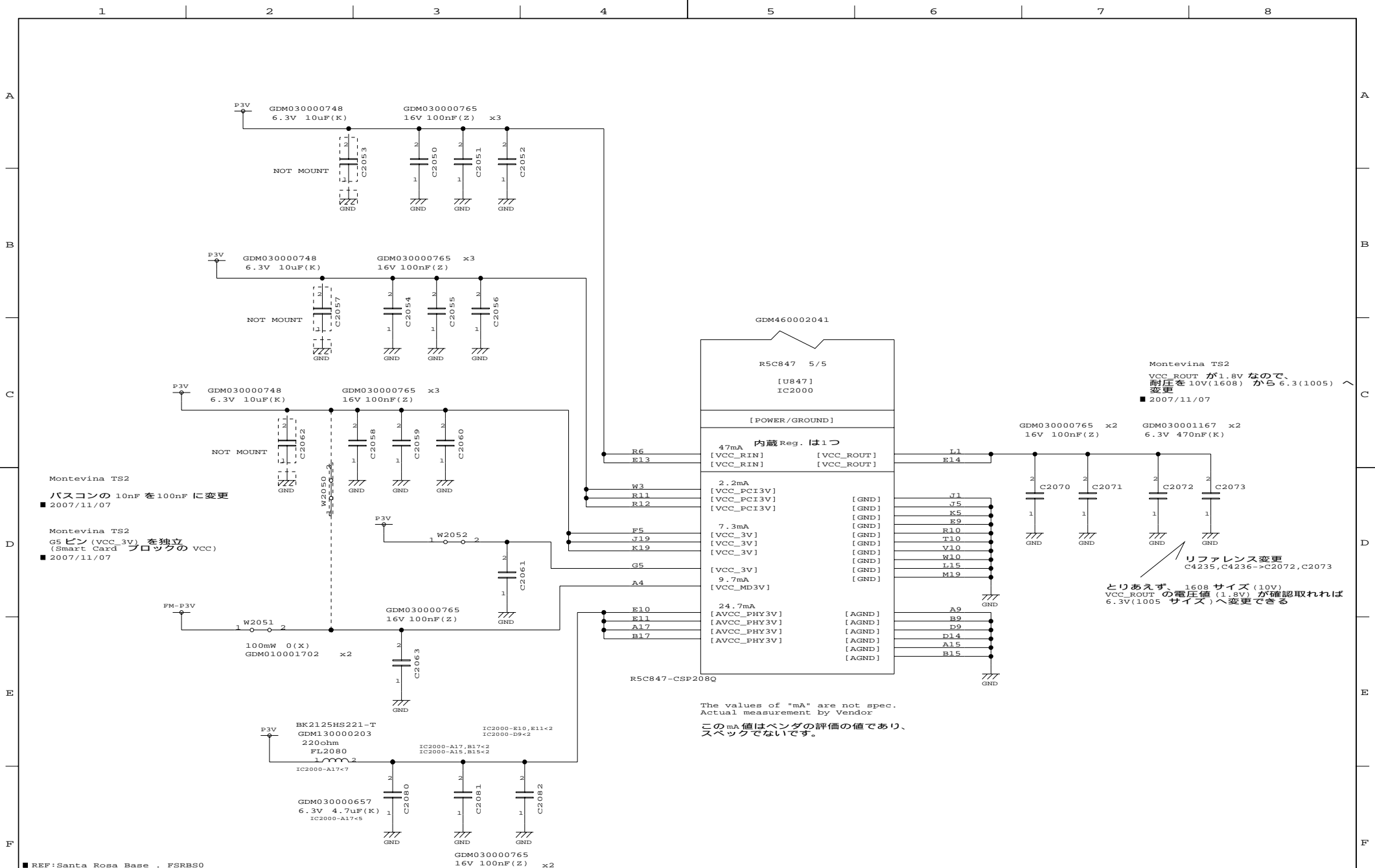


REF: Santa Rosa Base . FSRBS0

DESIGNED BY	2006/8/30	TITLE	2006/8/30	FUNCTION	2006/8/30	SH.NO.	2006/8/30	PAGE NO.	2006/8/30	REV.MARK	2006/8/30	DRAWING.NO.	2006/8/30
T. Ichimura		FG6IN1		CARD CONT 4		203		048		00		360065815	

2008.05.05 19:11

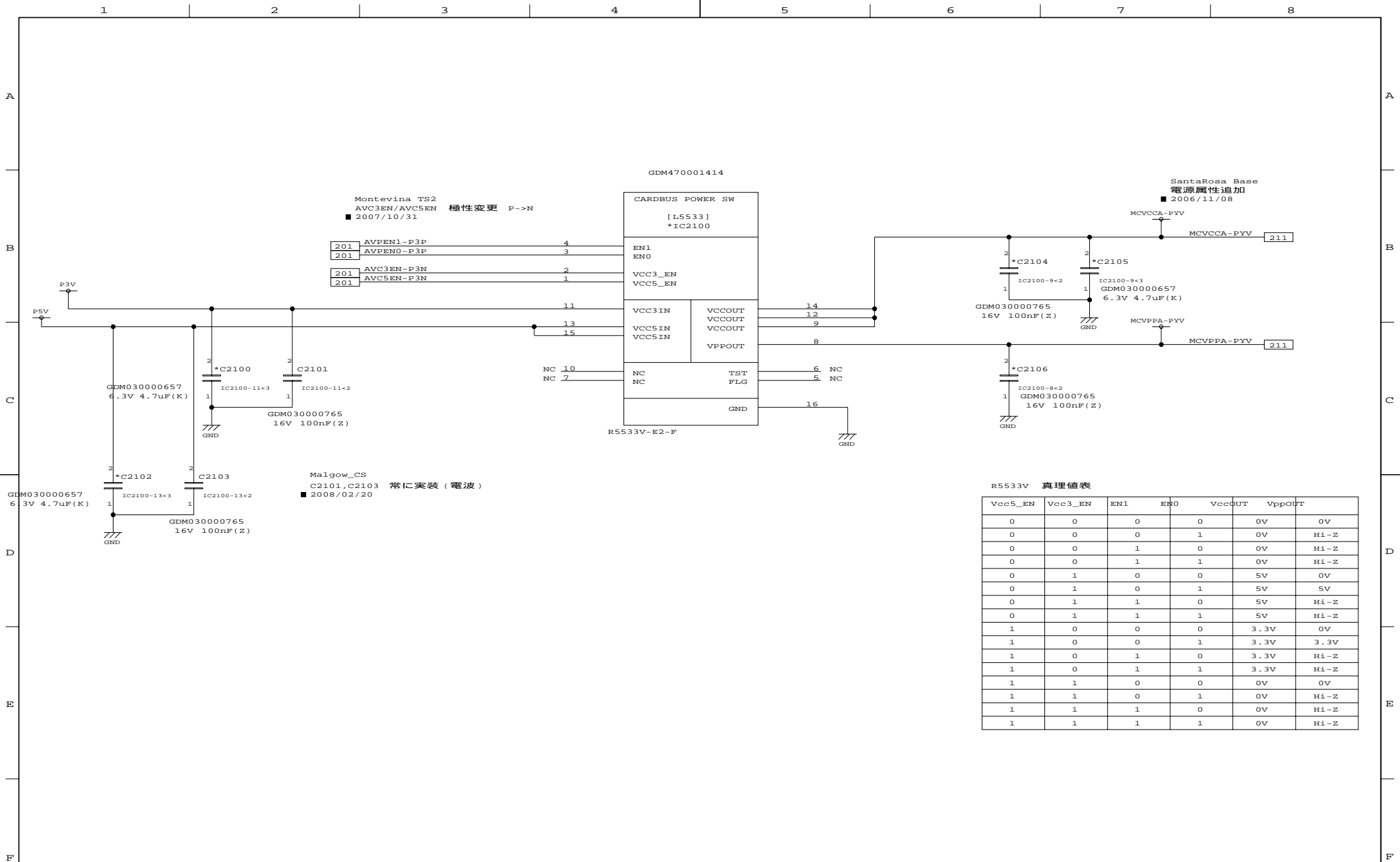




REF: Santa Rosa Base . FSRBS0

DESIGNED BY 2006/8/28
T. IchimuraTITLE
FG6IN1FUNCTION
CARD CONT 6SH.NO.
205PAGE NO.
050REV.MARK
00DRAWING.NO.
360065815

2008.05.05 19:11



■ REF: Santa Rosa Base . FSRBS0

DESIGNED BY 2006/8/28

T. Ichimura

TITLE

FG6IN1

FUNCTION

PC-Card POWER

SH.NO.

210

PAGE NO.

051

REV.MARK

00

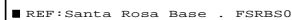
DRAWING.NO.

360065815

2008.05.05 19:11

TOSHIBA CORPORATION

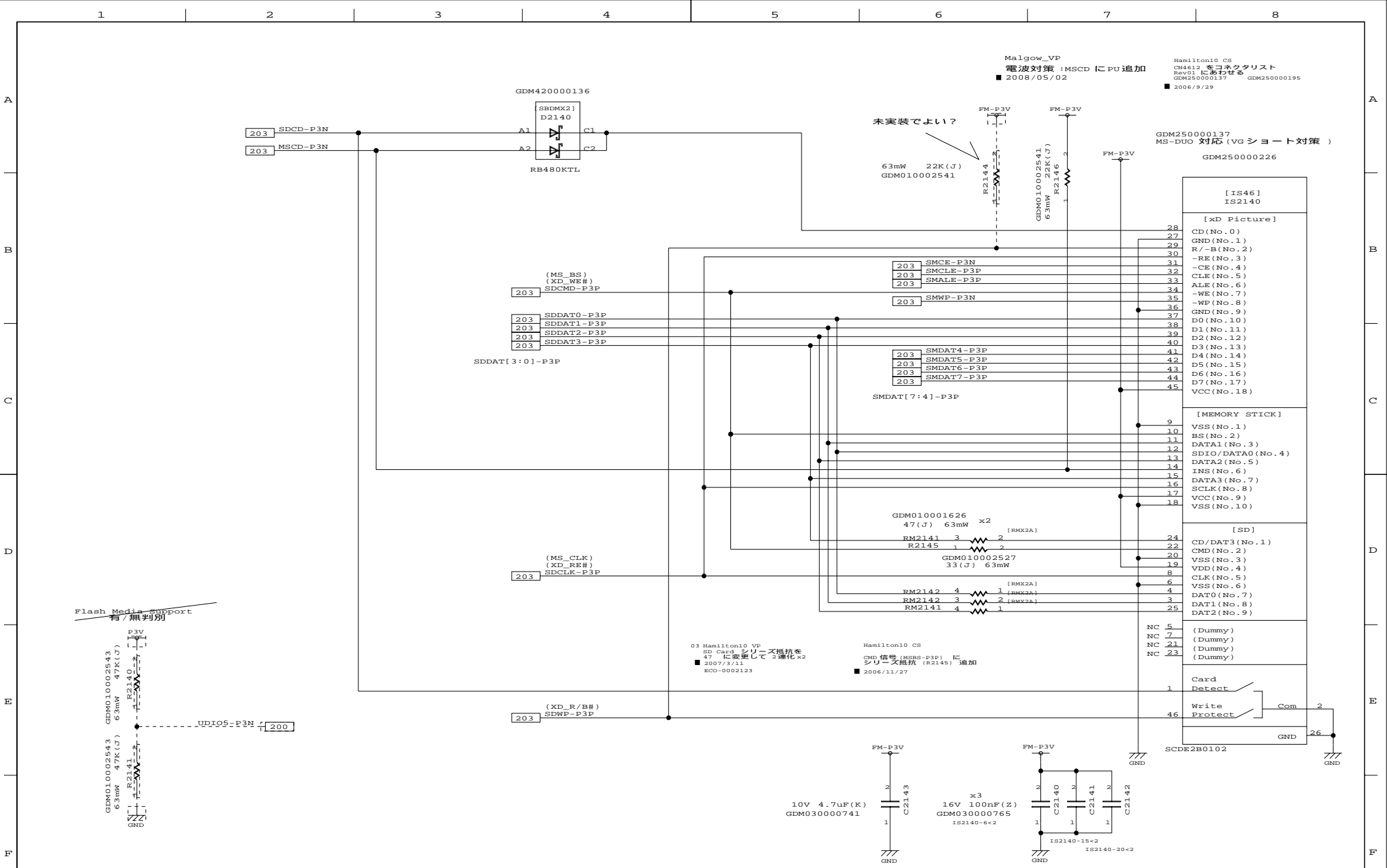
TS2 評価結果次第で大きい方に戻す可能性有り



DRAWING . NO .

360065815

TOSHIBA CORPORATION



DESIGNED BY 2006/8/28		TITLE		FUNCTION		SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
T.Ichimura		FG6IN1		MEDIA BRIDGE I/F		214	054	00	360065815
2008.05.05 19:11								TOSHIBA CORPORATION	

Integrated Hub downstream ports

Port		Device removable
1	DP_DP/DN	Depend on MODE0/SC_LED
2	Smart Card reader	Non-removable
3	EGATE_DP/DN	Removable
4	Contactless Smart Card Reader	Non-removable

MODE0	Port 1
H	Non-removable
L	Removable

Port 1 未使用時は L に設定すること

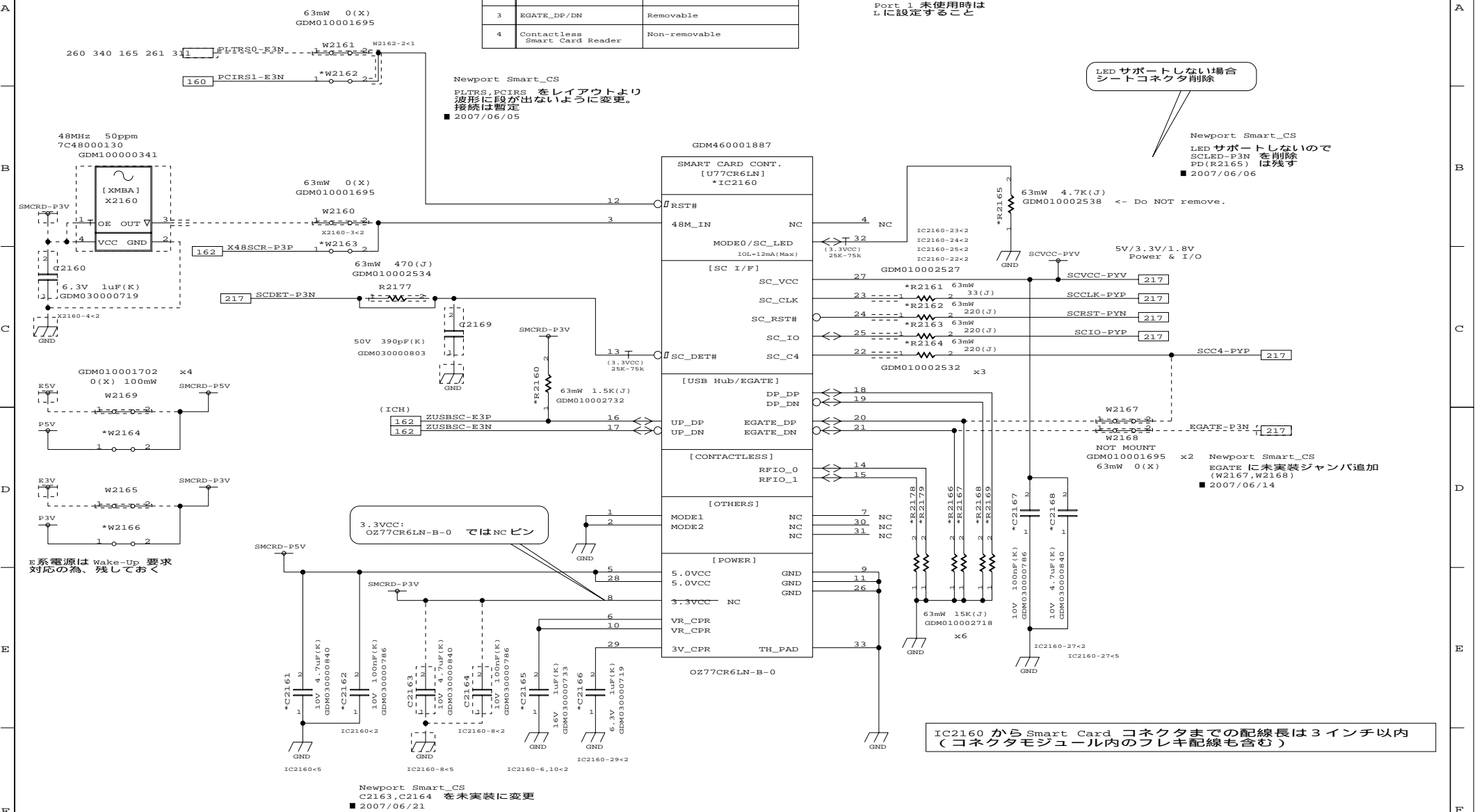
Newport Smart_CS

PLTRS, PCIRS をレイアウトより
波形に段が出ないように変更。
接続は暫定
■ 2007/06/05

LED サポートしない場合
シートコネクタ削除

Newport Smart_CS

LED サポートしないので
SCLED-P3N を削除
PD(R2165) は残す
■ 2007/06/06



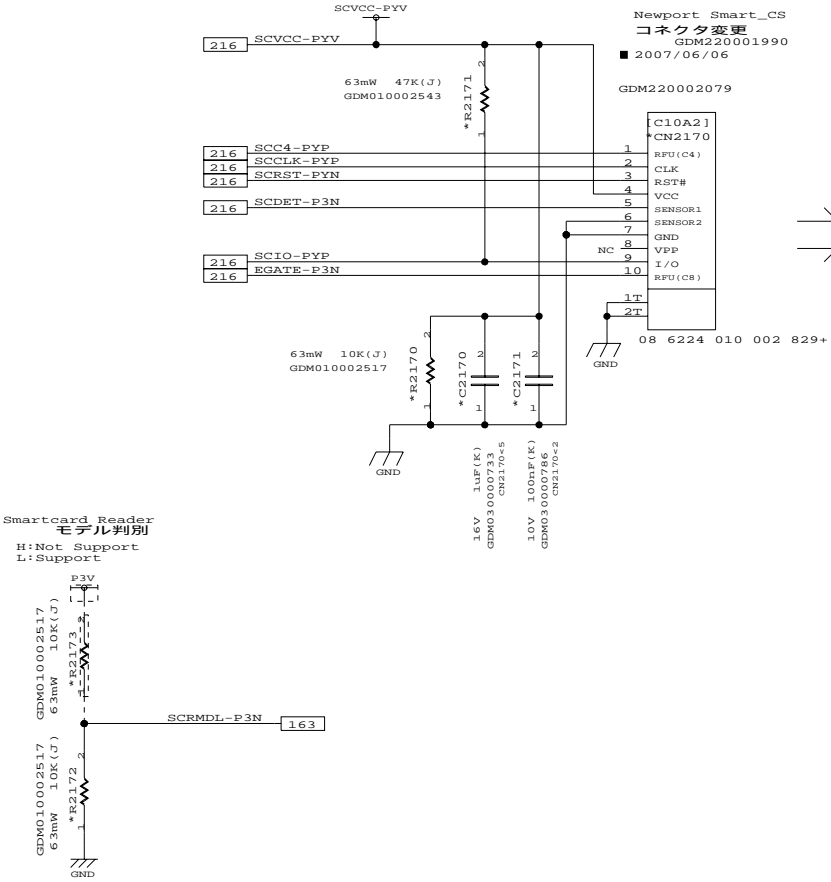
IC2160 から Smart Card コネクタまでの配線長は 3 インチ以内
(コネクタモジュール内のフレキシ配線も含む)

■ REF: NEW

DESIGNED BY	2008/05/05	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
T. Ichimura		FG6IN1	Smart Card Reader	216	055	00	360065815

2008.05.05 19:11

TOSHIBA CORPORATION



Smart Card
Socket

Malgow-6ply_VP
CN2170 を GDM220002079 に変更
(FFC FFC 変更対応)
■ 2008/03/24

Smart Card Socket のコンタクトのアサイン

Contact No.	Assignment	Contact No.	Assignment
C1	VCC	C5	GND
C2	RST	C6	VPP
C3	CLK	C7	I/O
C4	AUX1	C8	AUX2

AUX1 is used by function code (FCB) for type 2 synchronous cards (ISO/IEC 7816-10).

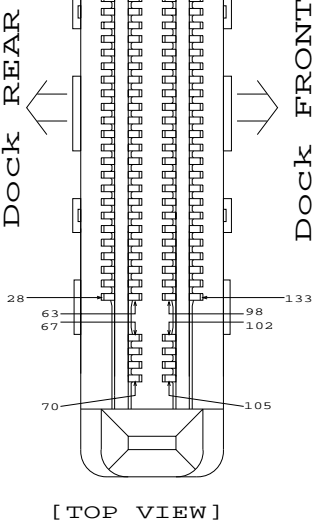
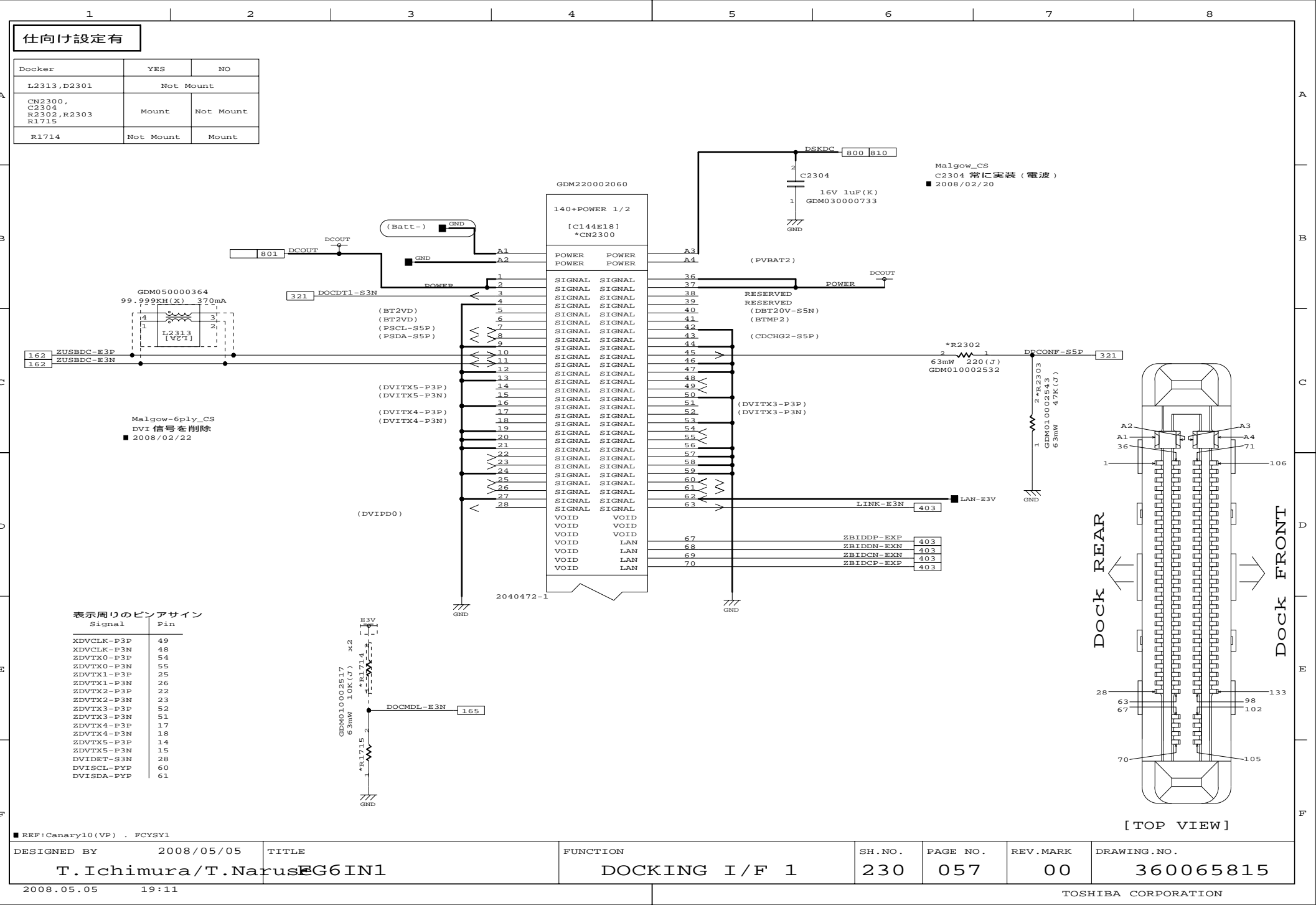
If an I/F device provides a USB I/F, VCC shall be used by VBUS, AUX1 by D+ and AUX2 by D-.

The use of AUX1 and AUX2 by the USB I/F does not preclude the use of these contacts by other protocols or other I/F devices defined in this International Standard series.

Such use is reserved by ISO/IEC JTC1/SC17.

ISO/IEC 7816-2 AMENDMENT 1 (2004-06-01) より

DESIGNED BY	2008/05/05	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
T.Ichimura		FG6IN1	Smart Card I/F	217	056	00	360065815



仕向け設定有

Docker	YES	NO
CN2300 C2310 R2314-R2317	Mount	Not Mount

	GM W/ Dock	GM/GL W/o Dock	PM W/ Dock
R2310-R2312 FL2310-FL2312 IC2311	Mount Mount NotMount	NotMount Mount NotMount	NotMount Mount Mount

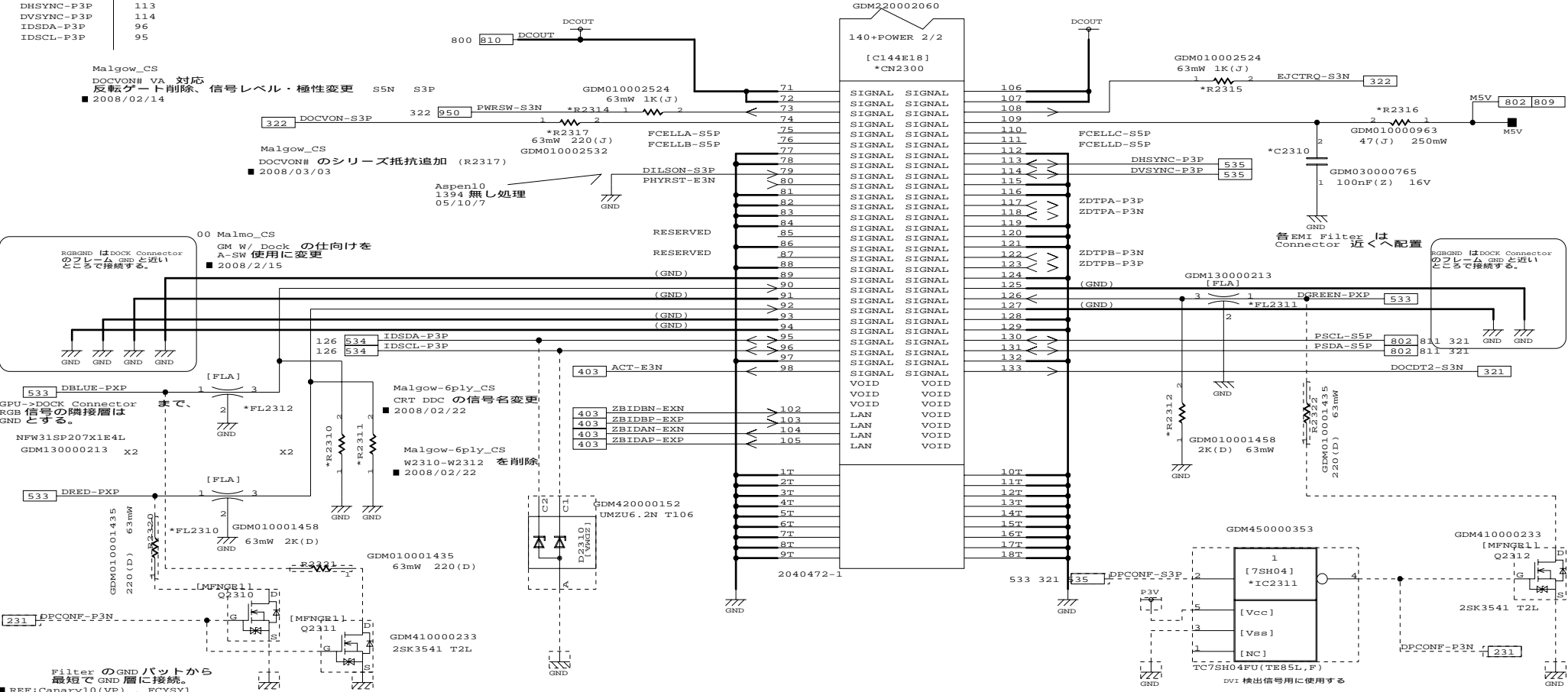
Aspen10
コネクタ GDM コードに変更
05/10/7

Canary10_CS:
コネクタ変更 GDM220001699 GDM220001756 06/2/23
コネクタ変更 GDM220001756 GDM220001699 06/2/28

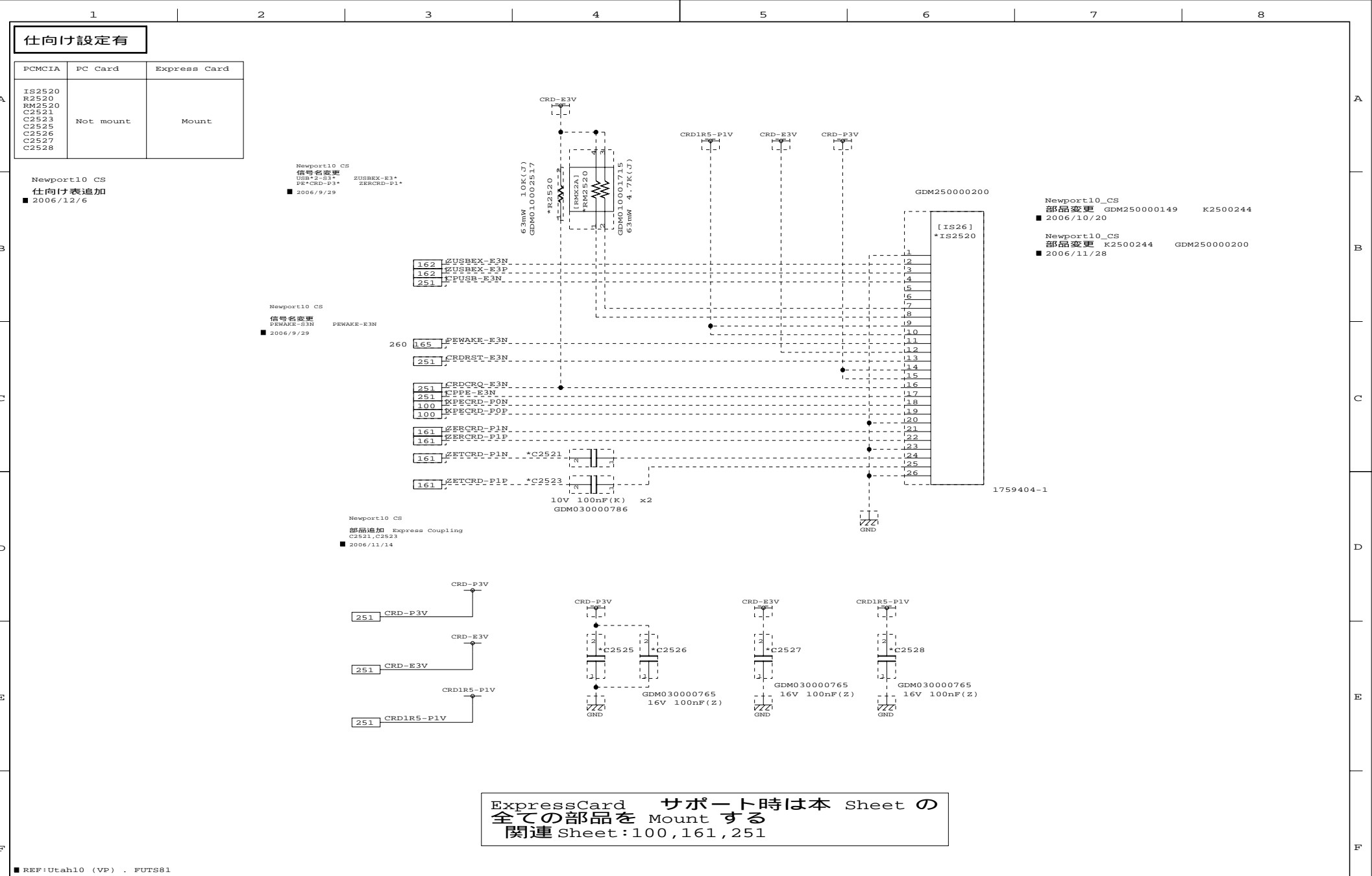
[02] Canary10_VP:
コネクタ変更 GDM220001699 GDM220001756
(ピンスルー対応) (TEMCO-0001096) 06/4/6

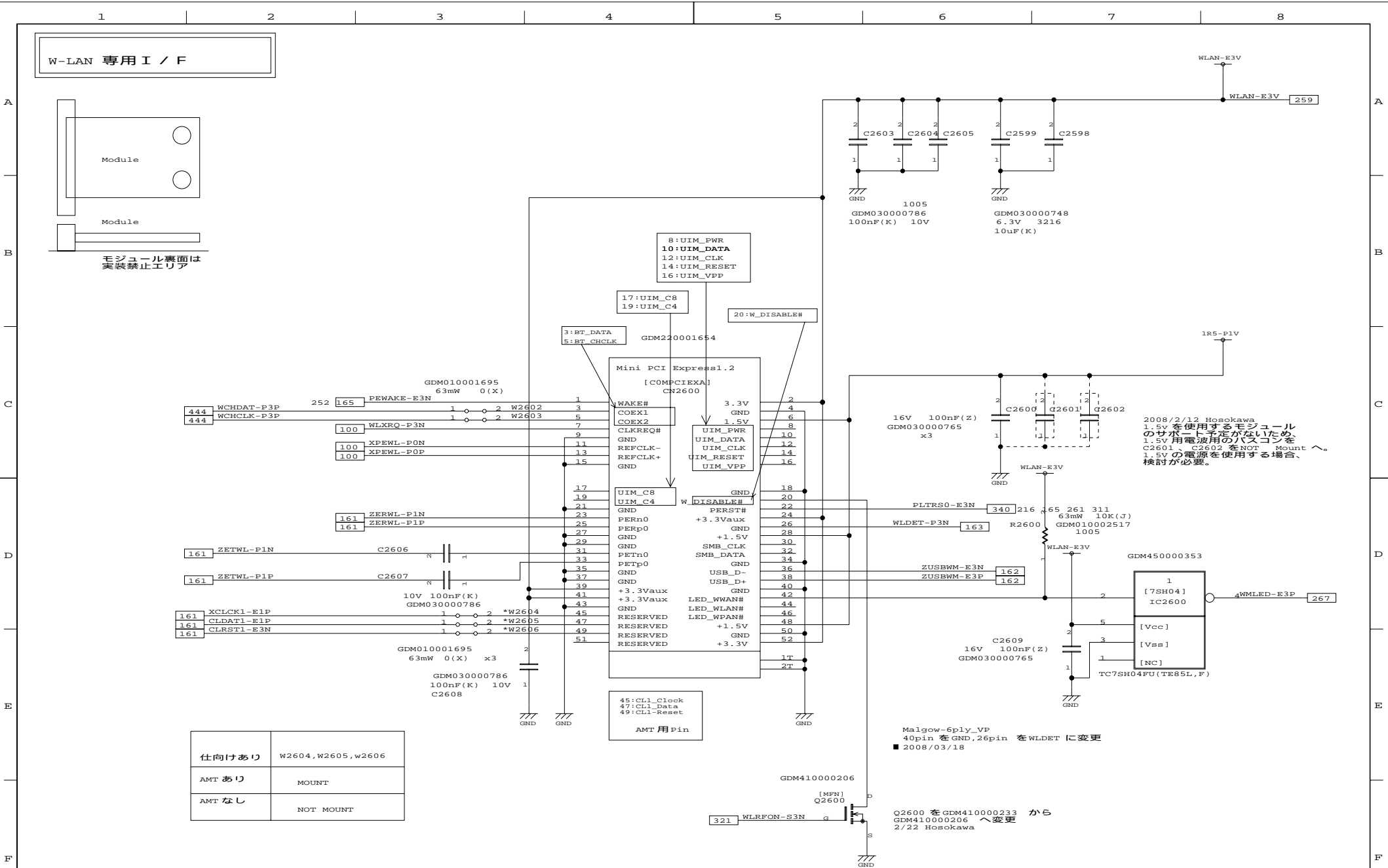
表示周りのピンサイン

Signal	pin
DRED-PXP	92
DBLUE-PXP	90
DGREEN-PXP	126
DHSYNC-P3P	113
DVSYNC-P3P	114
IDSDA-P3P	96
IDSCL-P3P	95



DESIGNED BY	2008/3/3	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
T. Ichimura/T. Naruse		EG6IN1	DOCKING I/F 2	231	058	00	360065815

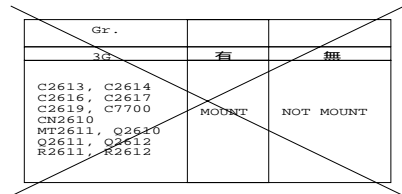




3G/Robson	YES	NO
R2610	Mount	Mount
W2614	Not Mount	Not Mount
上記部品以外全て	Mount	Not Mount

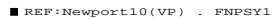
TOSHIBA CONFIDENTIAL

P系にスイッチ追加。
2005/10/05 五十嵐



(PEWAKE-E3N)

追加



TITLE

FUNCTION

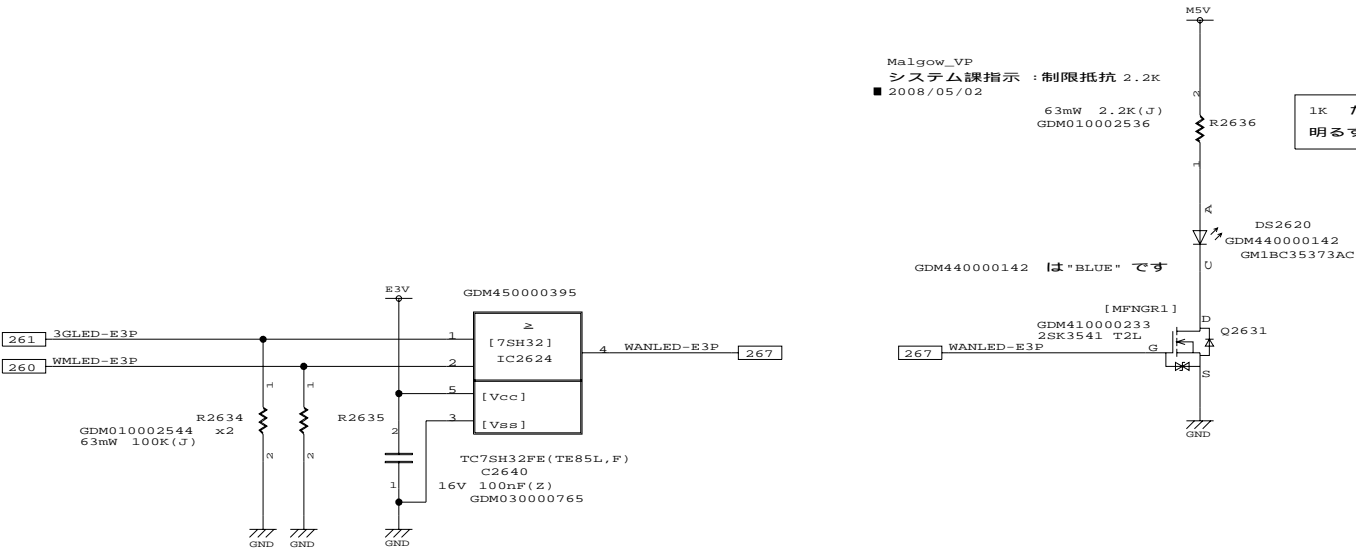
SH . NO .

PAGE NO.

	REV. MARK
--	-----------

DRAWING . NO .

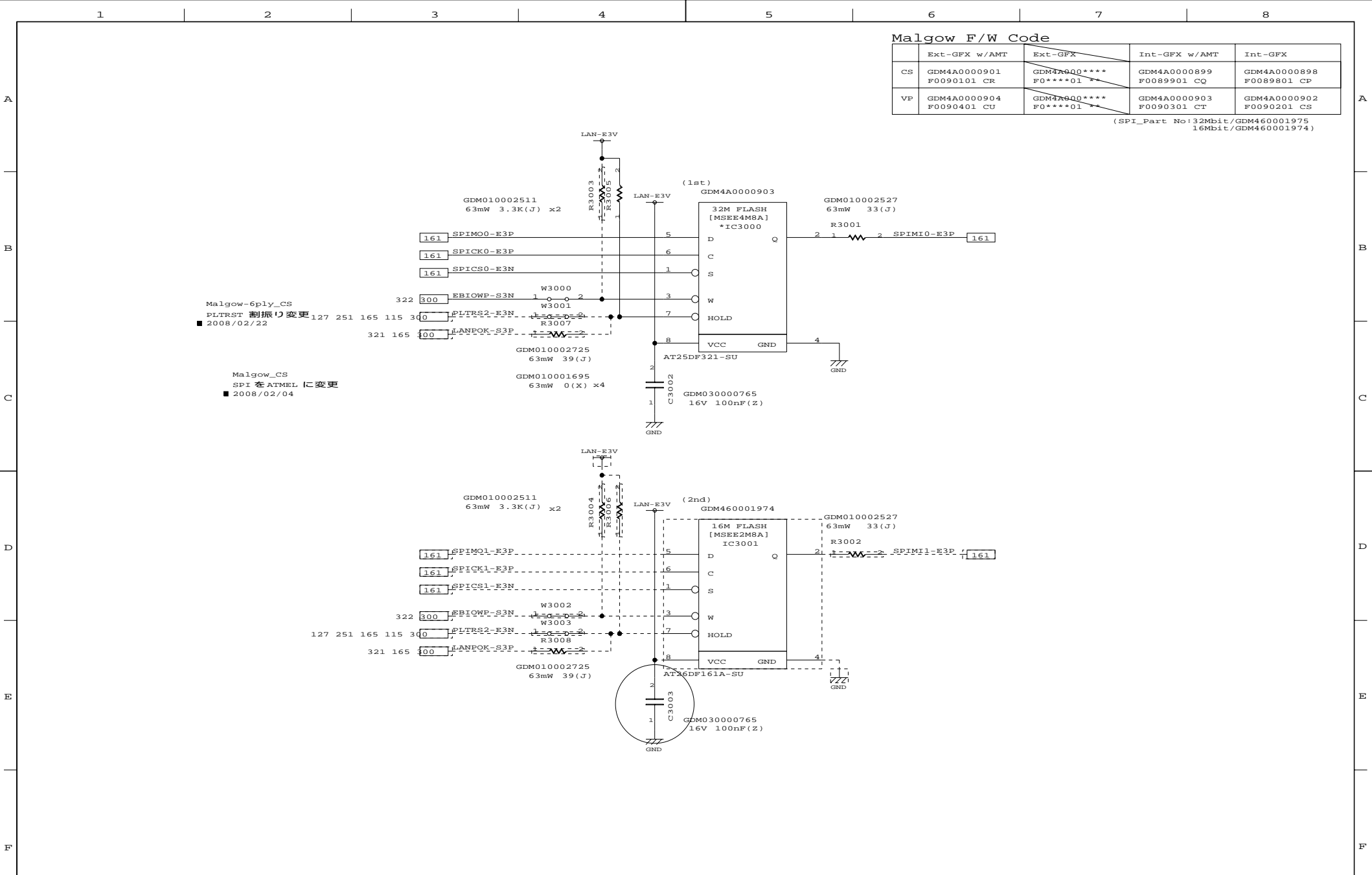
360065815



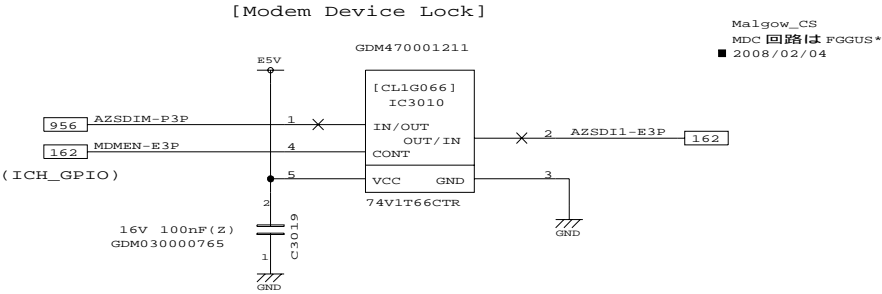
Ref:Wengen CS

DESIGNED BY	2008/05/05	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
H.Gushiken		FG6IN1	3G(WWAN) LED	267	064	00	360065815

2008.05.05 19:11



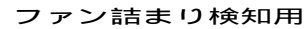
DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
T.Ichimura	FG6IN1	FLASH ROM	300	065	00	360065815



DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
T.Ichimura	FG6IN1	MDC Device Lock	301	066	00	360065815
2008.05.05	19:11					TOSHIBA CORPORATION

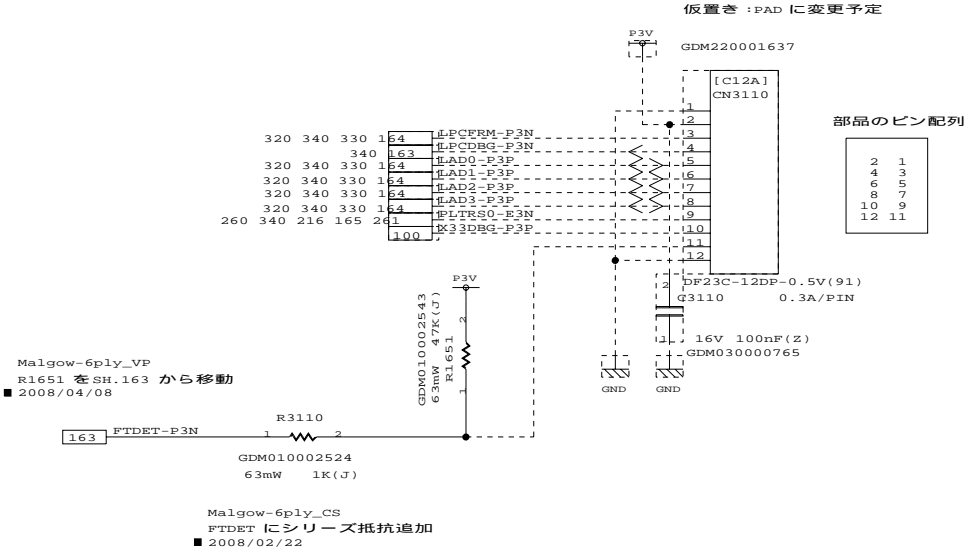
TOSHIBA CONFIDENTIAL

常温測定用



REF: New

TOSHIBA CORPORATION



■ REF:New

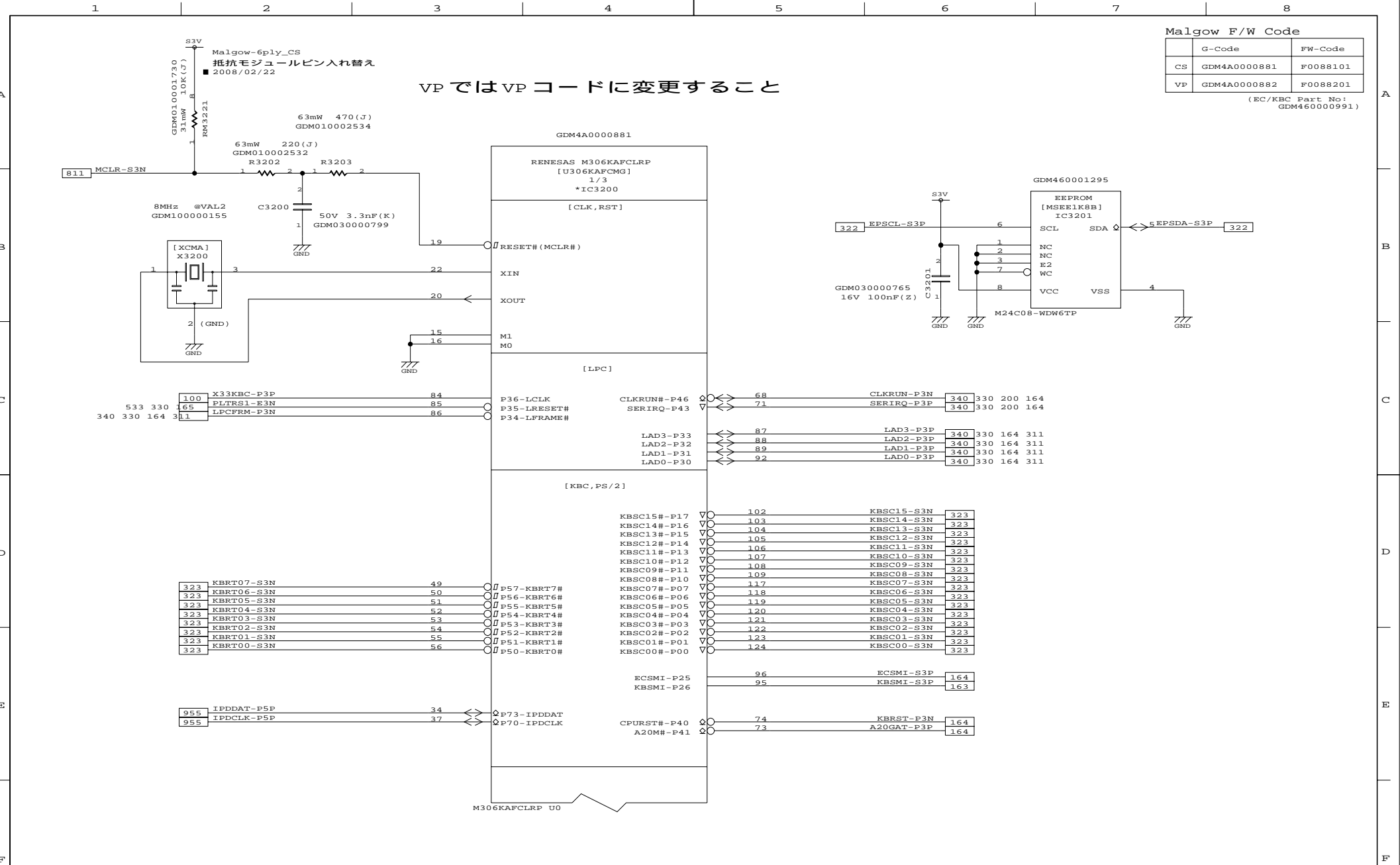
DESIGNED BY	2008/05/05	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
T.Iwaki/T.Ichimura		FG6IN1	F/T PJ	311	068	00	360065815

VP ではなく VP コードに変更すること

Malgow F/W Code

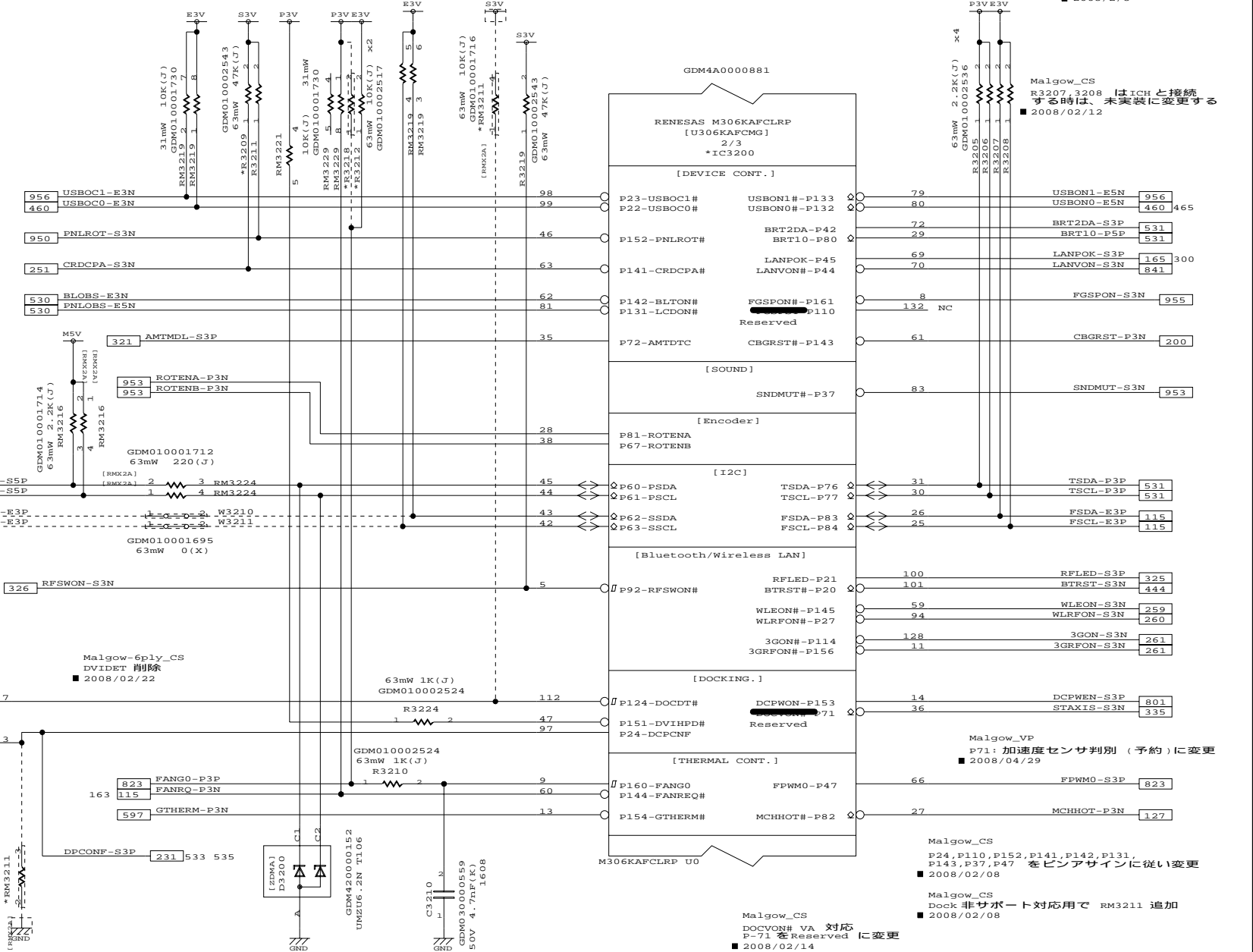
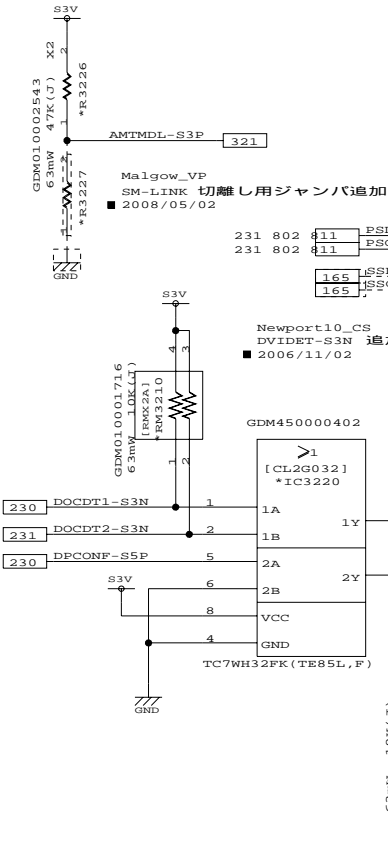
	G-Code	FW-Code
CS	GDM4A0000881	F0088101
VP	GDM4A0000882	F0088201

(EC/KBC Part No:
GDM46000991)



仕向け設定有

AMT	有り	無し
R3212	Mount	Not Mount
R3226		
R3218	Not Mount	Mount
R3227		
PCMCIA	PC Card	Express Card
R3209	Mount	Not Mount
Docker	有り	無し
IC3220	Mount	Not Mount
RM3210		
RM3211	Not Mount	Mount



DESIGNED BY 2008/05/05

K.Taki/T.Ichimura

TITLE

FG6IN1

FUNCTION

EC/KBC 2

SH.NO.

321

PAGE NO.

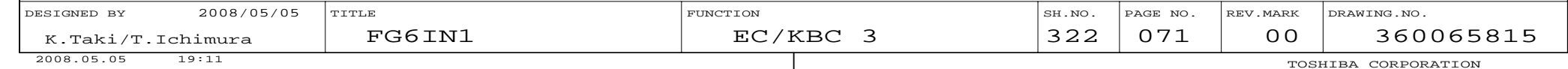
070

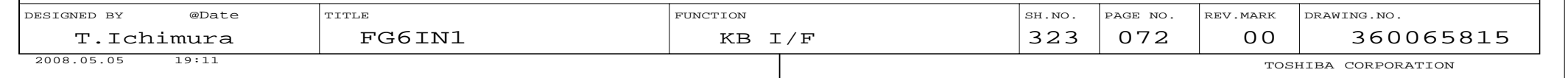
REV.MARK

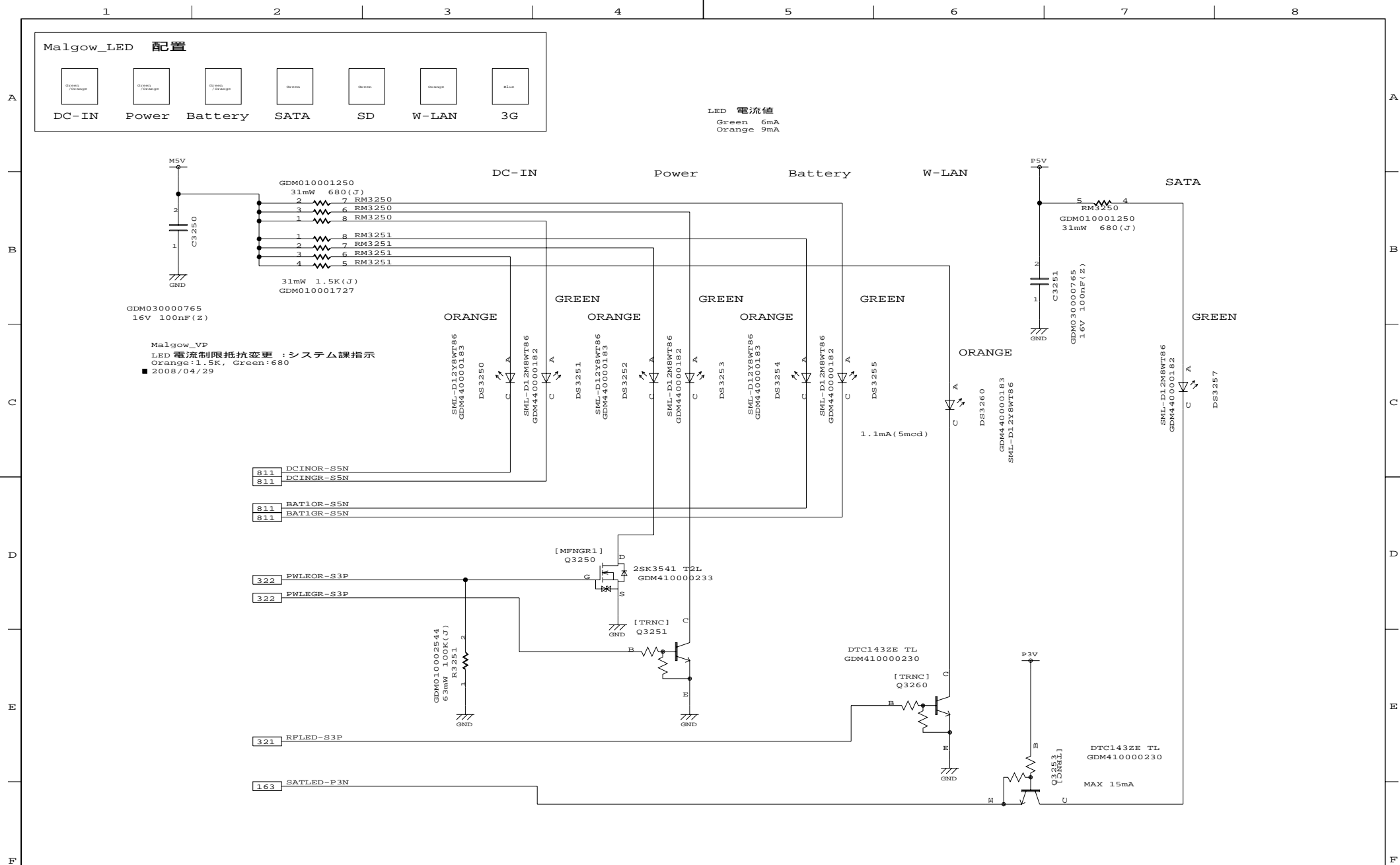
00

DRAWING.NO.

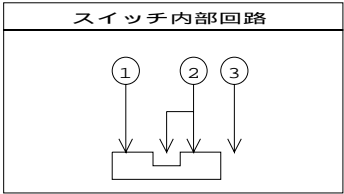
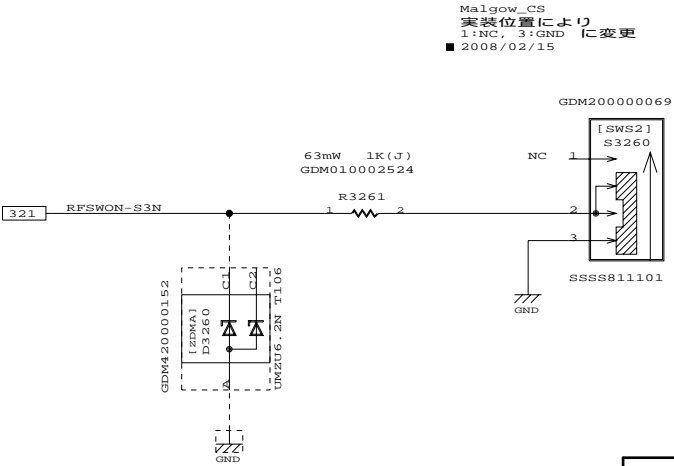
360065815



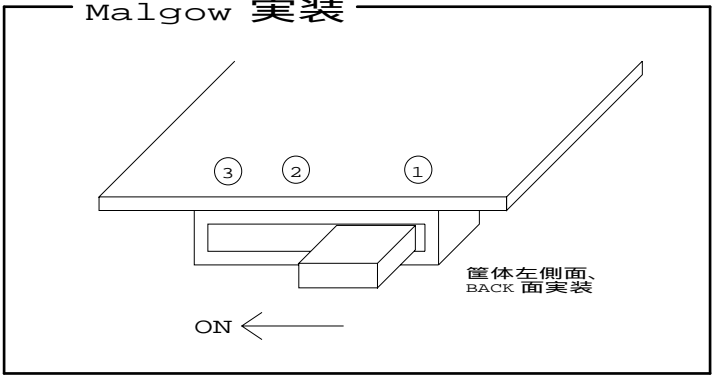




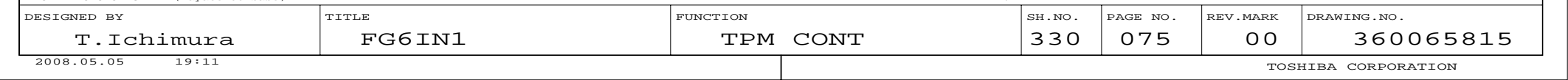
DESIGNED BY T. Ichimura	TITLE FG6IN1	FUNCTION LED	SH.NO. 325	PAGE NO. 073	REV.MARK 00	DRAWING.NO. 360065815
----------------------------	-----------------	-----------------	---------------	-----------------	----------------	--------------------------

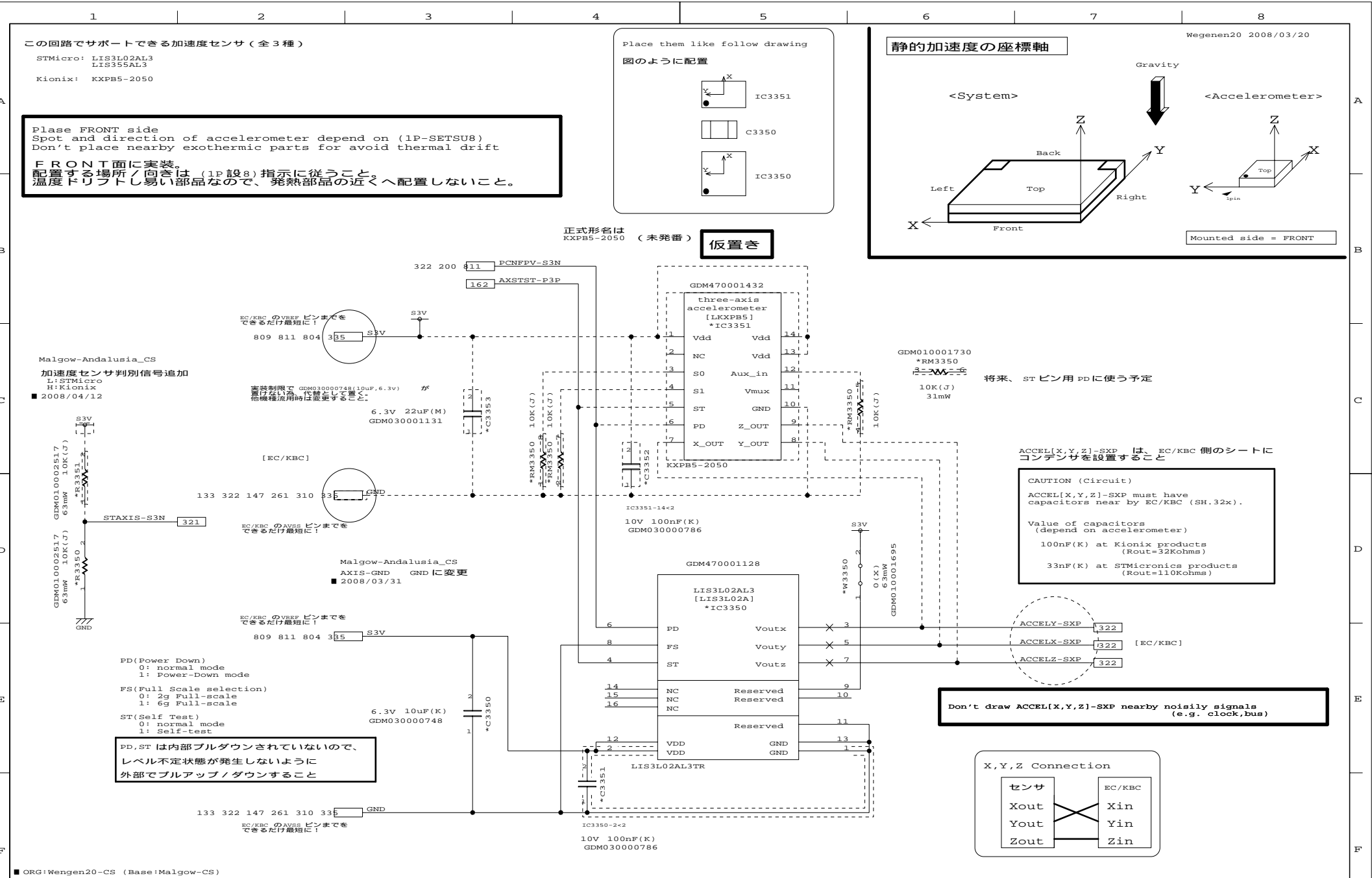


接点の切換えタイミング：規定なし
(Non shorting ではない)

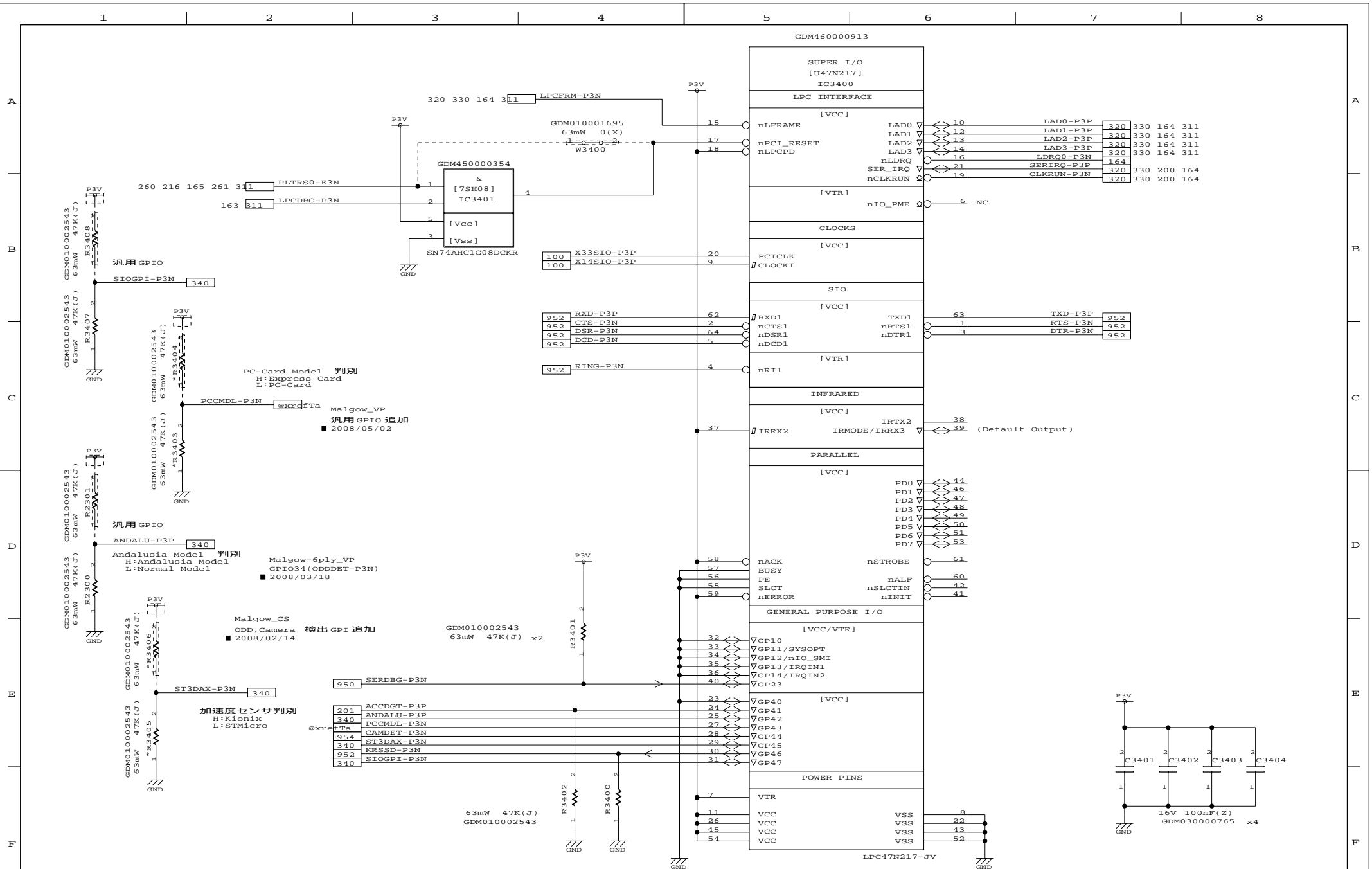


DESIGNED BY T.Ichimura		TITLE FG6IN1	FUNCTION WIRELESS SW,LED	SH.NO. 326	PAGE NO. 074	REV.MARK 00	DRAWING.NO. 360065815
2008.05.05 19:11		TOSHIBA CORPORATION					

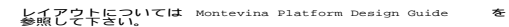


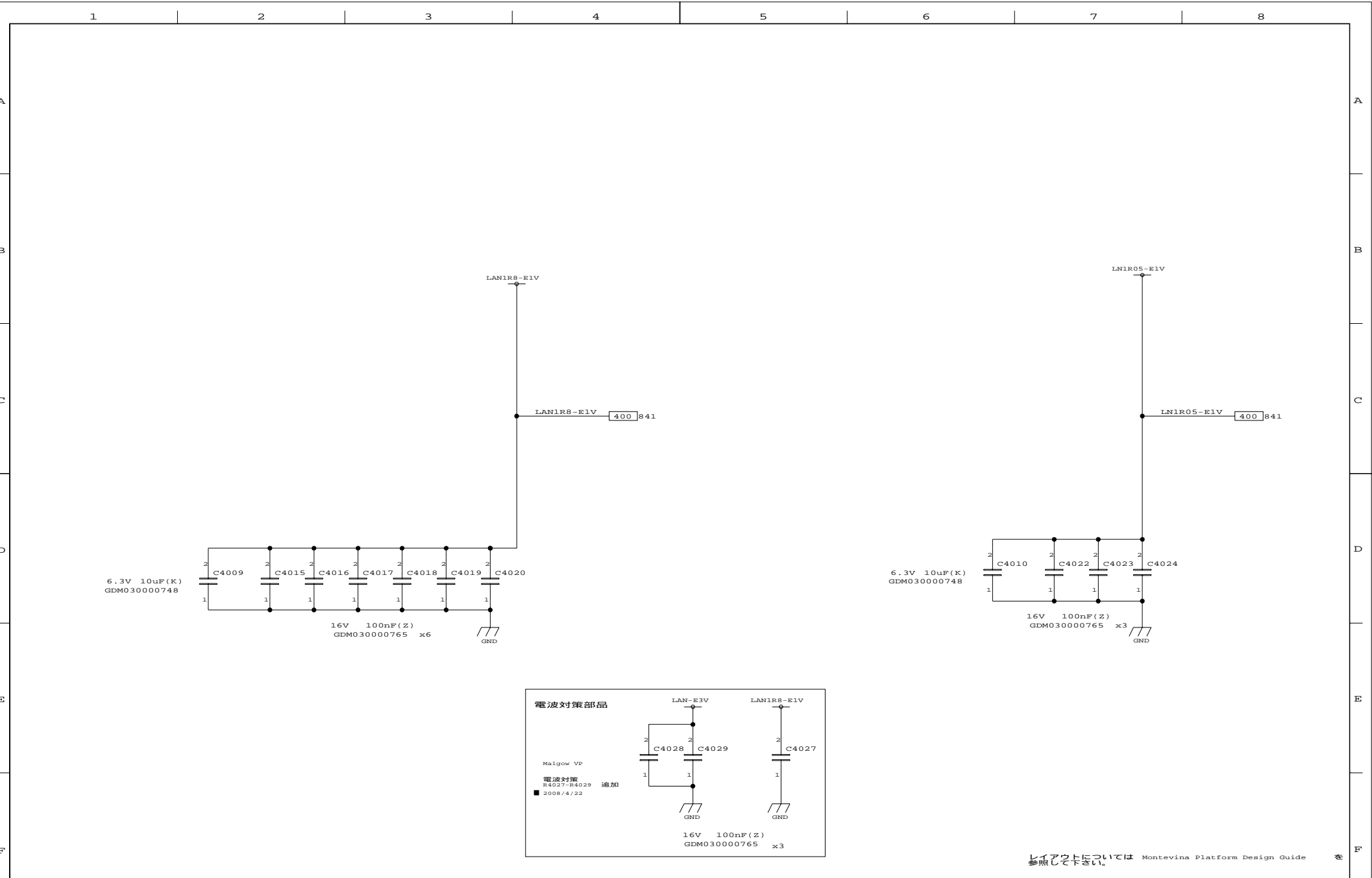


DESIGNED BY	2008/05/05	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
T.Ichimura		FG6IN1	ACCELEROMETER (FRONT)	335	076	00	360065815
2008.05.05	19:11	TOSHIBA CONFIDENTIAL					TOSHIBA CORPORATION



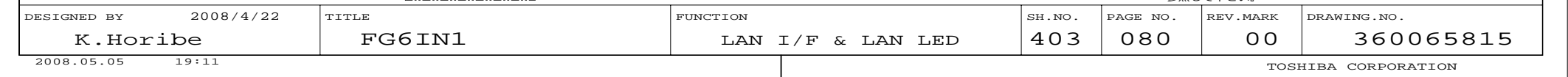
DRAWING.NO.
360065815

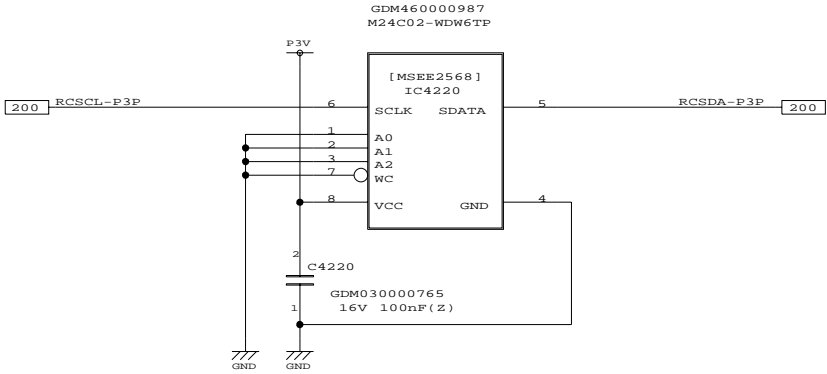




レイアウトについては Montevina Platform Design Guide を参照して下さい。

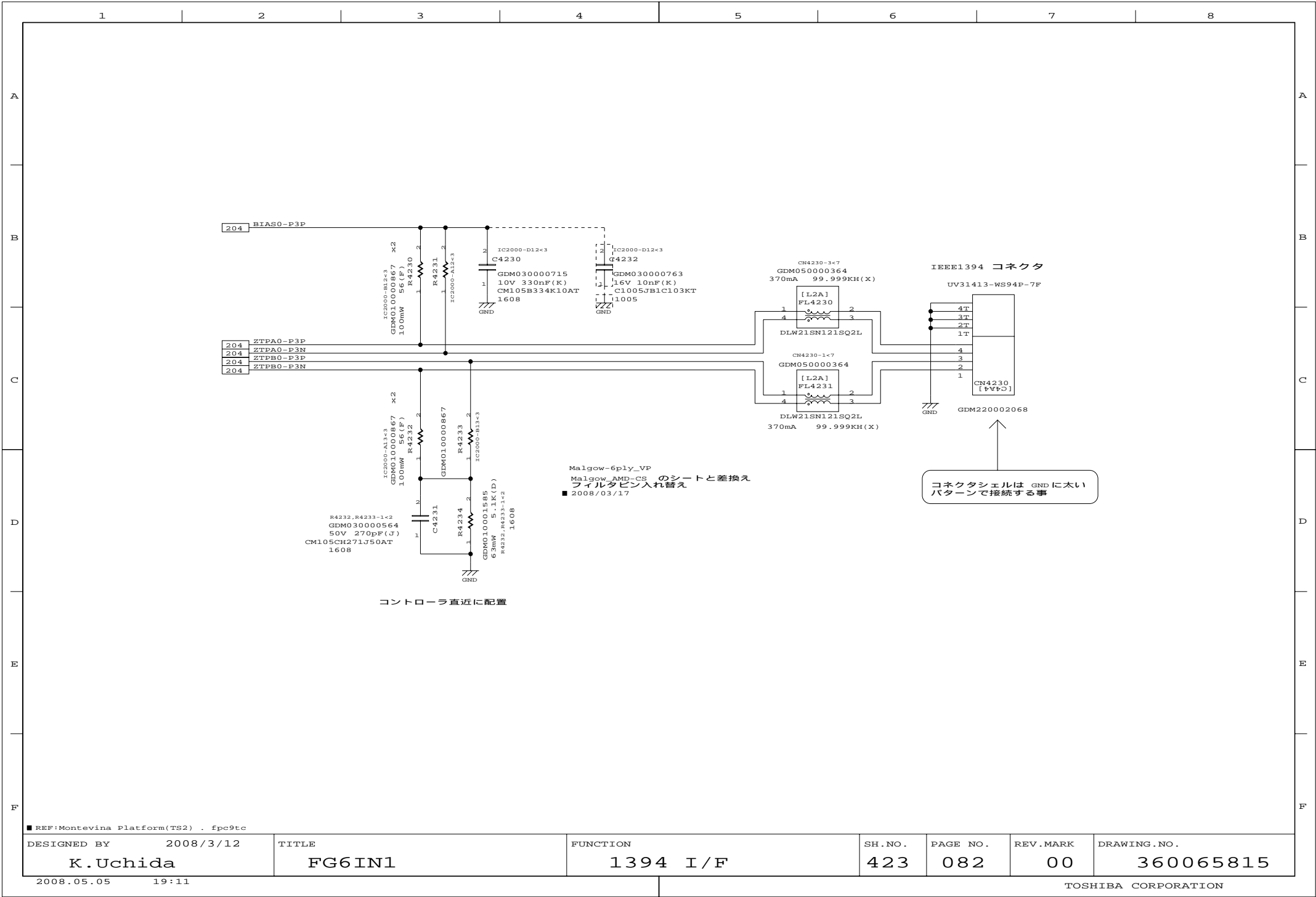
DESIGNED BY	2008/05/05	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
K.Horibe		FG6IN1	LAN Power	401	079	00	360065815
2008.05.05	19:11	TOSHIBA CORPORATION					





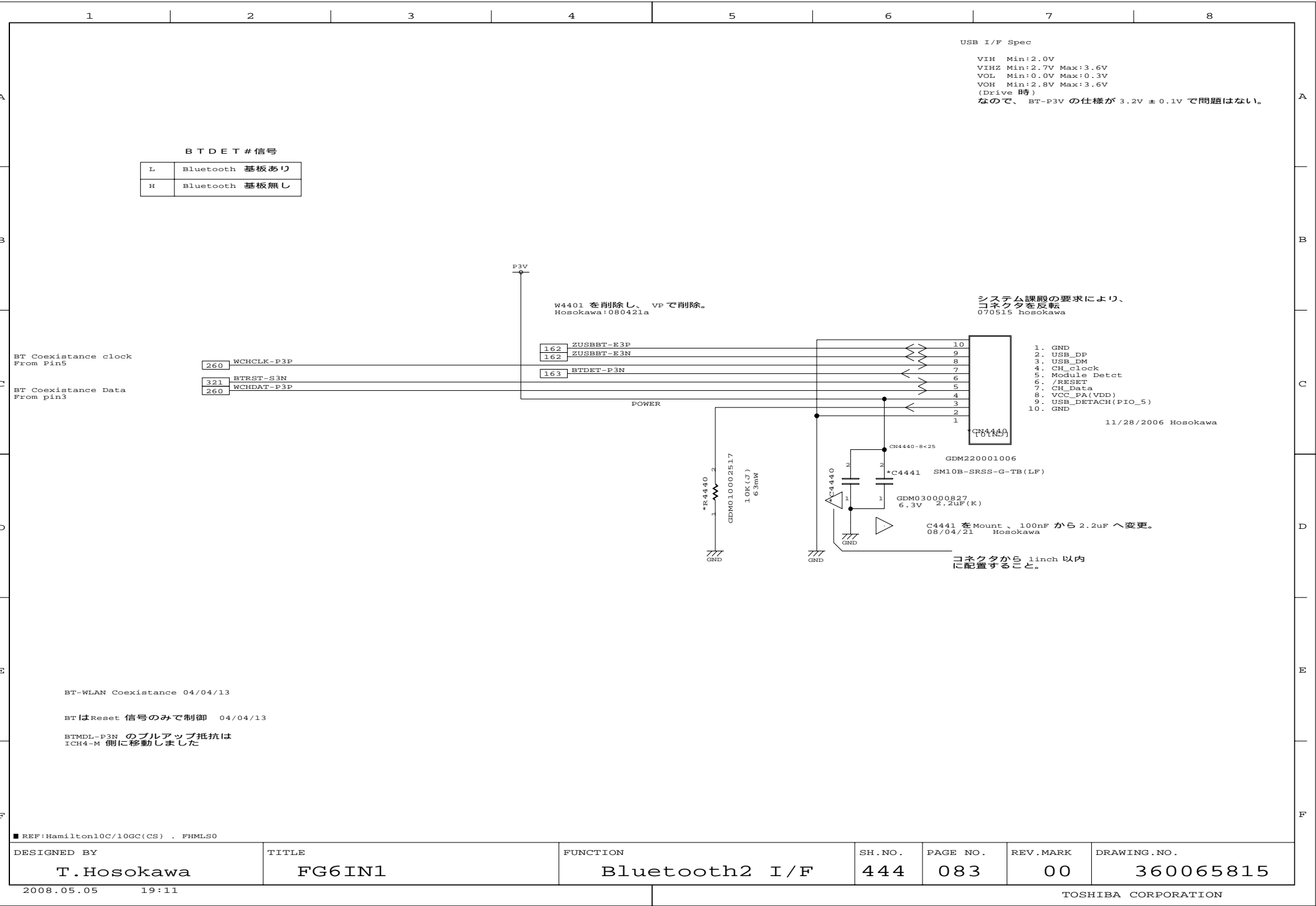
Malgow_VP
R4220, R4221 を SH.200 に移動
■ 2008/05/03

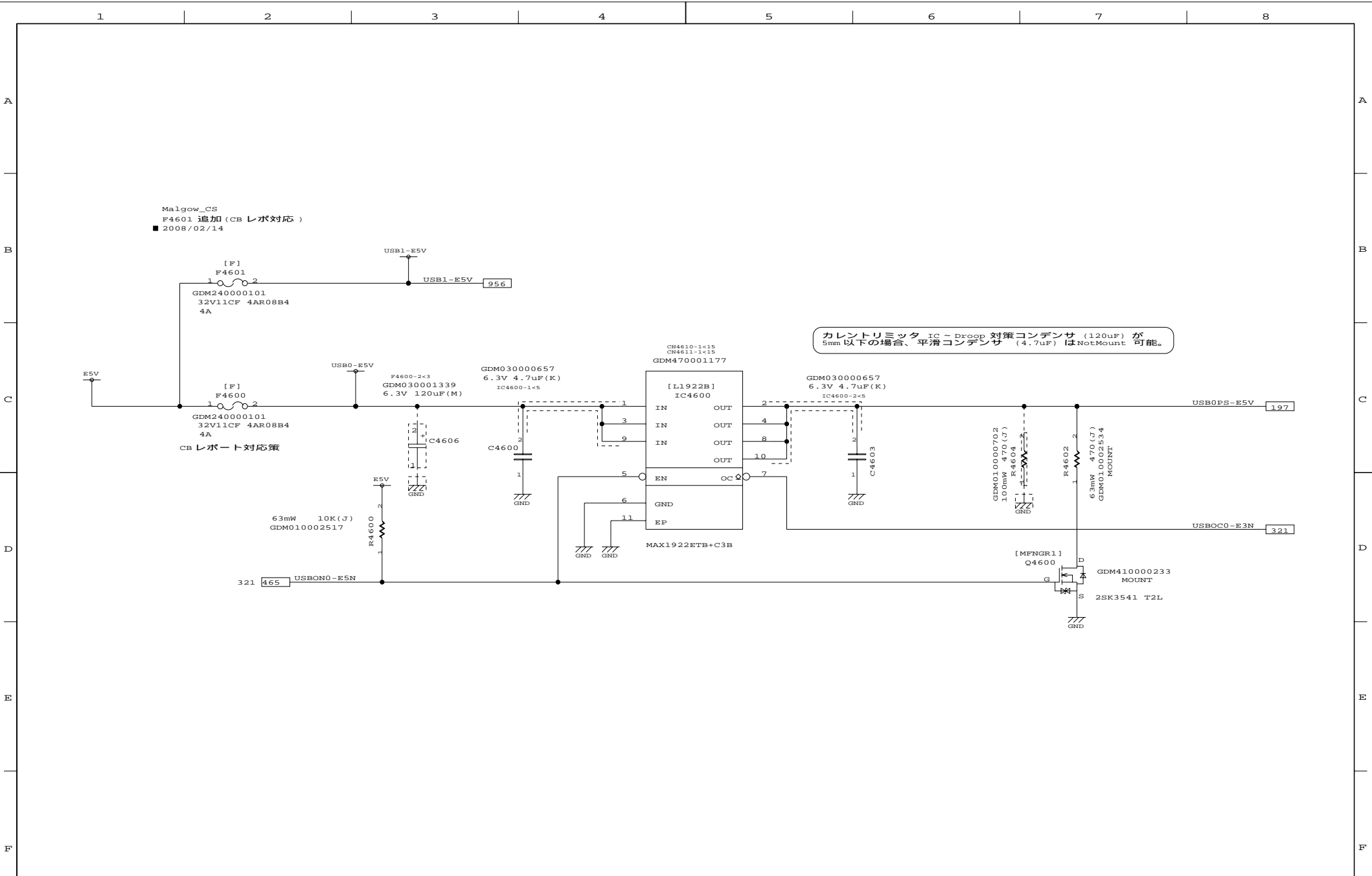
DESIGNED BY	2007/10/9	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
K.Uchida		FG6IN1	1394 EEPROM	422	081	00	360065815
2008.05.05	19:11	TOSHIBA CORPORATION					



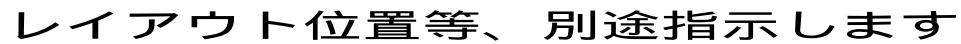
REF:Montevina Platform(TS2) . fpc9tc

DESIGNED BY	2008/3/12	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
K.Uchida		FG6IN1	1394 I/F	423	082	00	360065815
2008.05.05 19:11				TOSHIBA CORPORATION			



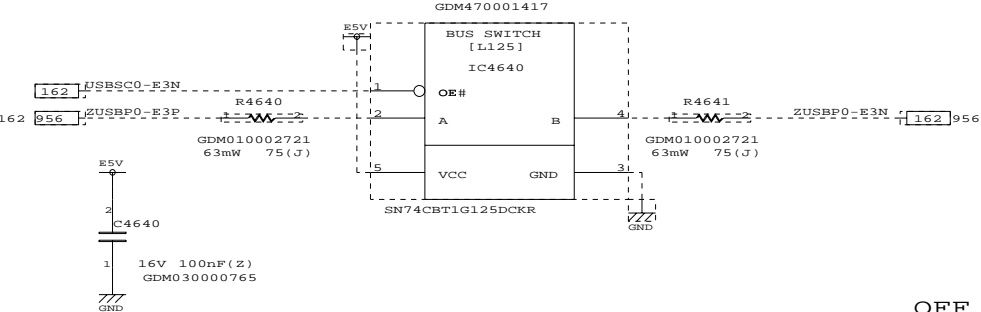


DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
T.Ichimura	FG6IN1	USB POWER	460	084	00	360065815
2008.05.05	19:11					TOSHIBA CORPORATION

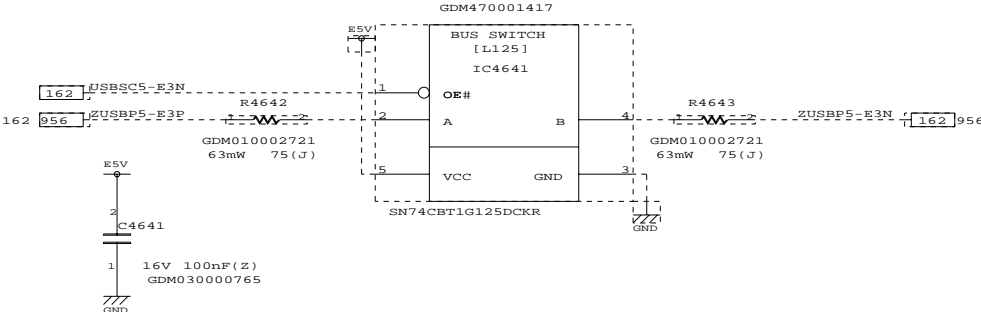


2008.05.05 19:11

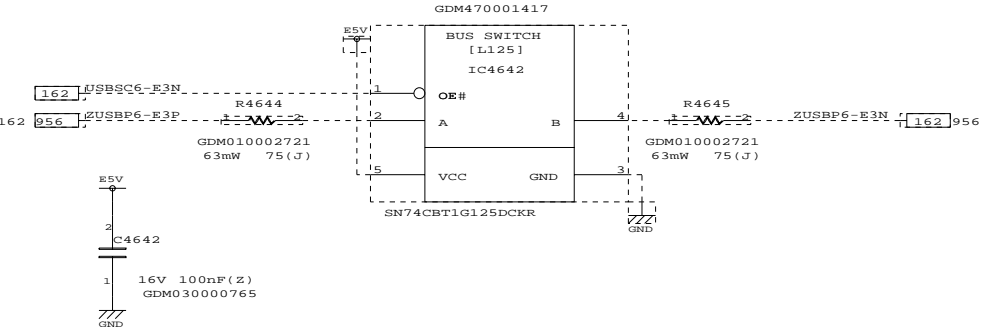
OFF 中給電回路
(Port0)



OFF 中給電回路
(Port5)



OFF 中給電回路
(Port6)

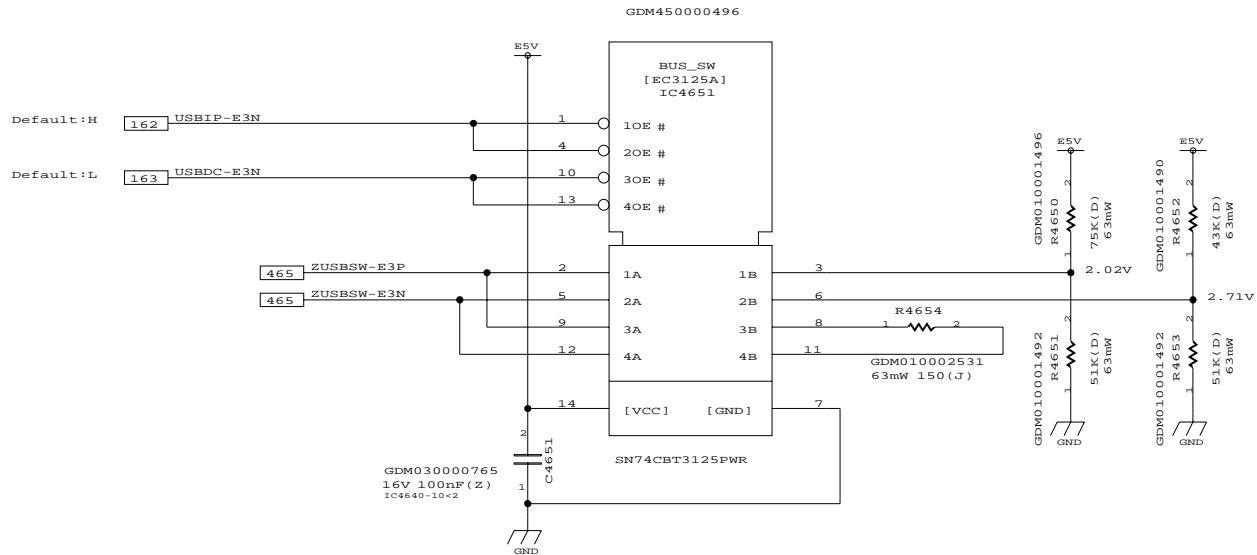
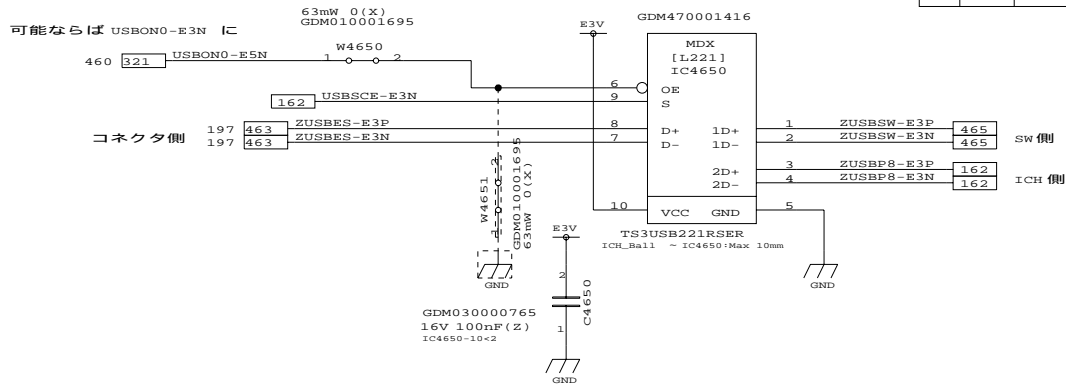


レイアウト位置等、別途指示します

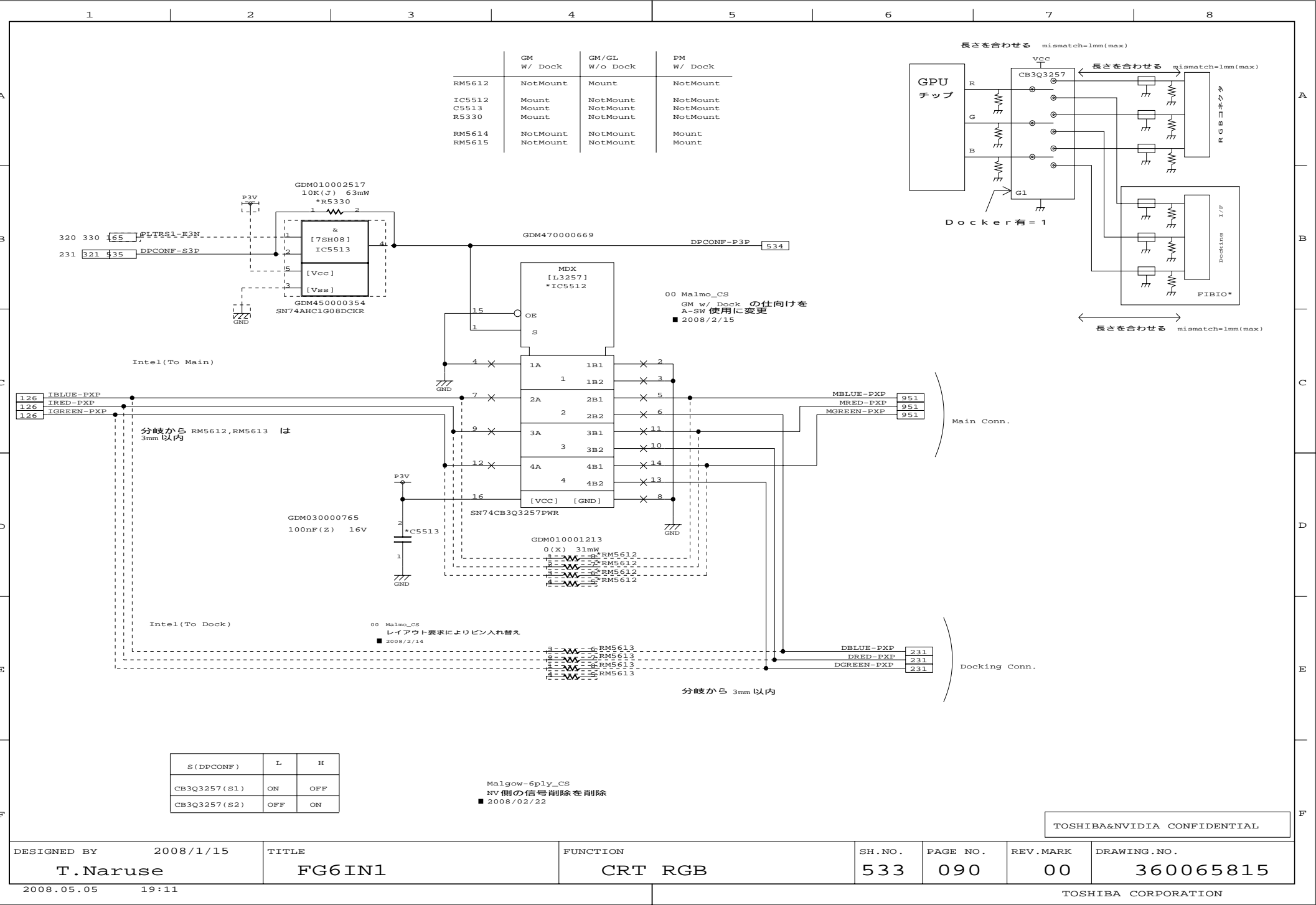
DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
K.Uchida	FG6IN1	USB CHARGER 2	464	086	00	360065815
2008.05.05	19:11					TOSHIBA CORPORATION

特定デバイス用 or Dedicated Charger

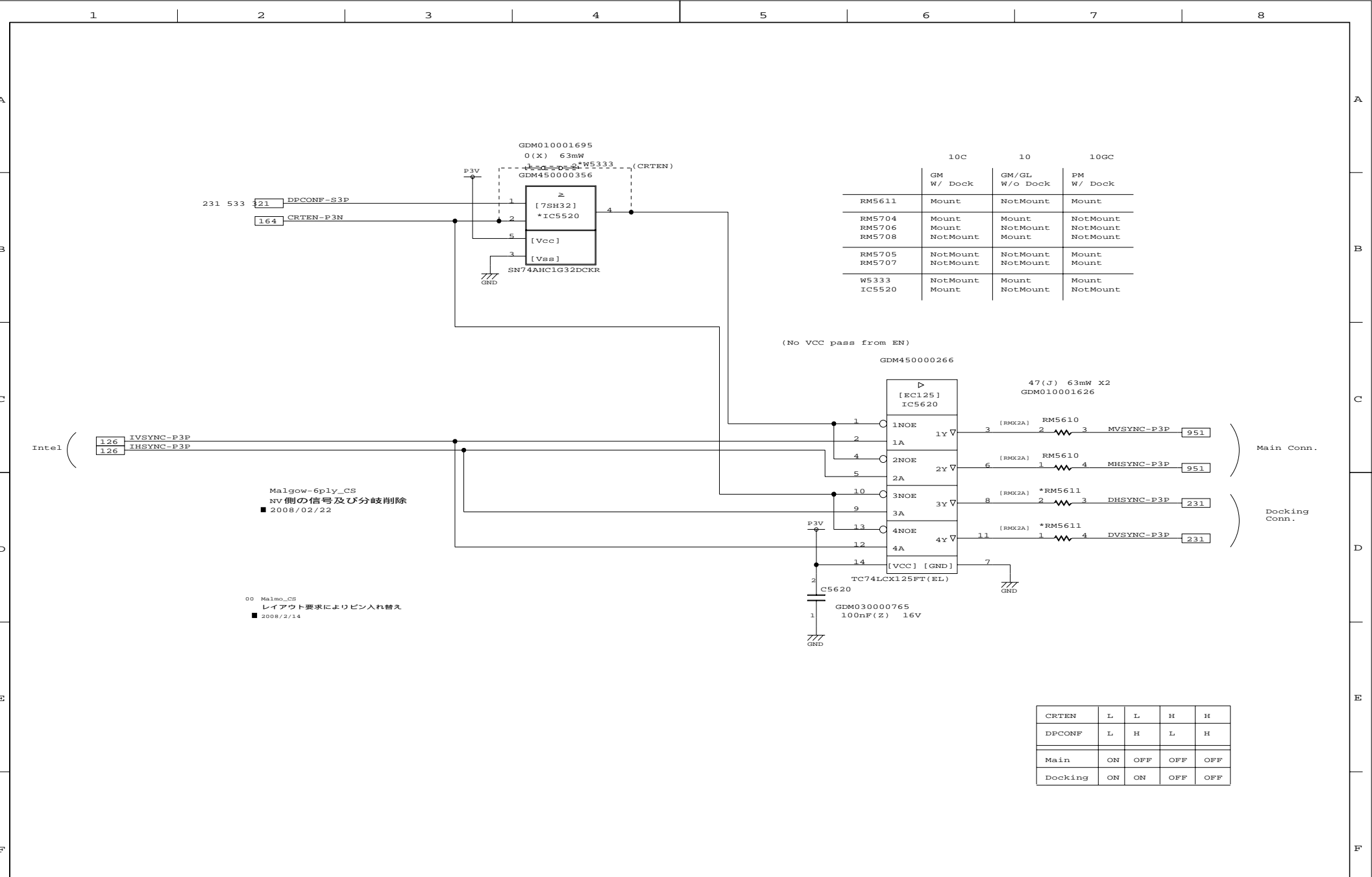
S	OE#	Function
X	H	Disconnect
L	L	D=1D
H	L	D=2D



DESIGNED BY K.Uchida	TITLE FG6IN1	FUNCTION USB CHARGER 3	SH.NO. 465	PAGE NO. 087	REV.MARK 00	DRAWING.NO. 360065815
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Main Conn.



12345678

仕向け設定有り

Malgow-6ply_CS
nVIDIA のLVDS ライン削除
■ 2008/02/22

Malgow-6ply_CS
分岐回路削除
■ 2008/02/22

GPUGM/GLPM

LVDS (RM5710-RM5711	Mount	NotMount
	RM5716-RM5717	Mount	NotMount
	RM5714-RM5715	NotMount	Mount
	RM5718-RM5719	NotMount	Mount

TOSHIBA&NVIDIA CONFIDENTIAL

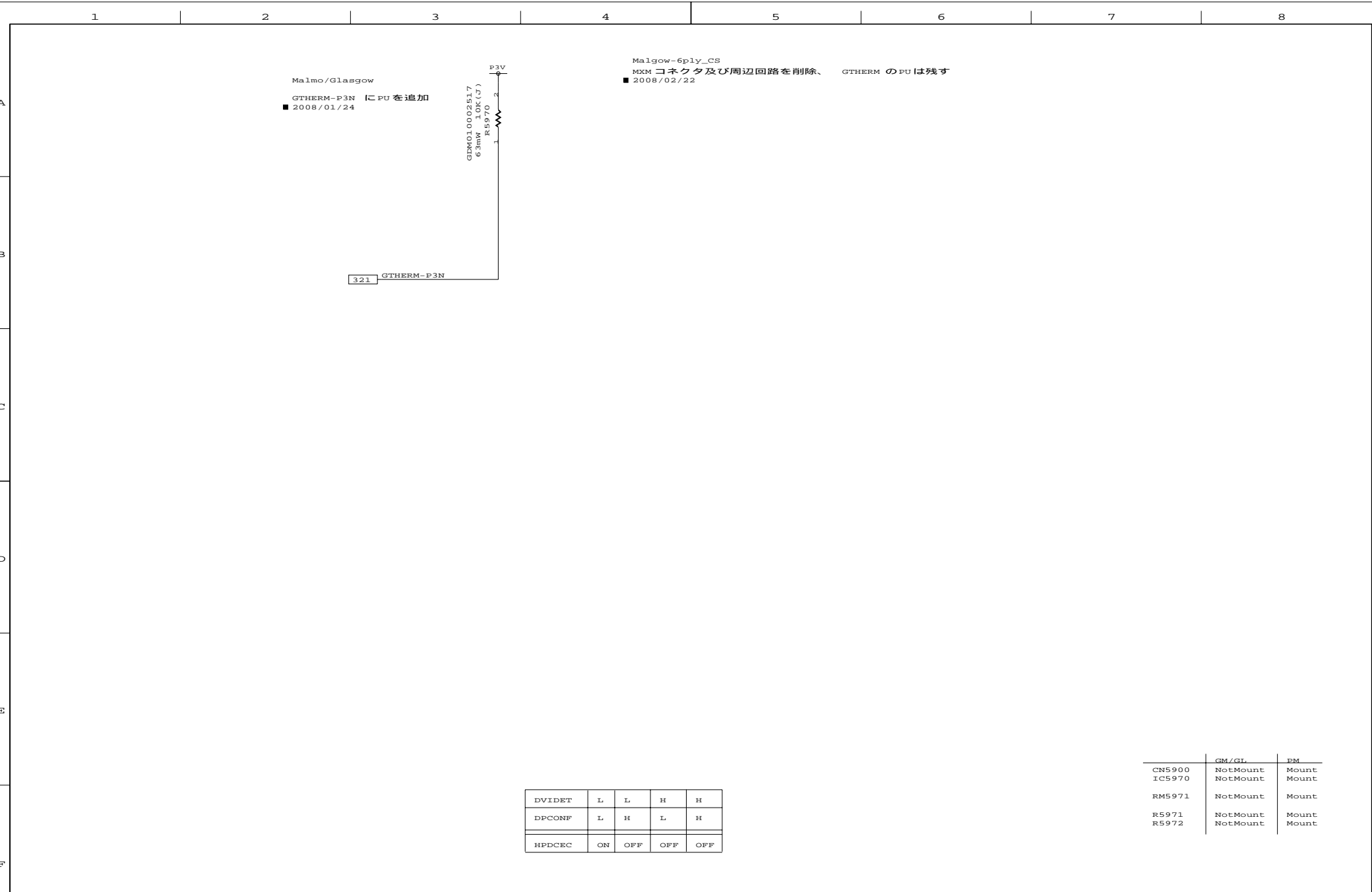
DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
T.Naruse	FG6IN1	LVDS Jumper	541	093	00	360065815

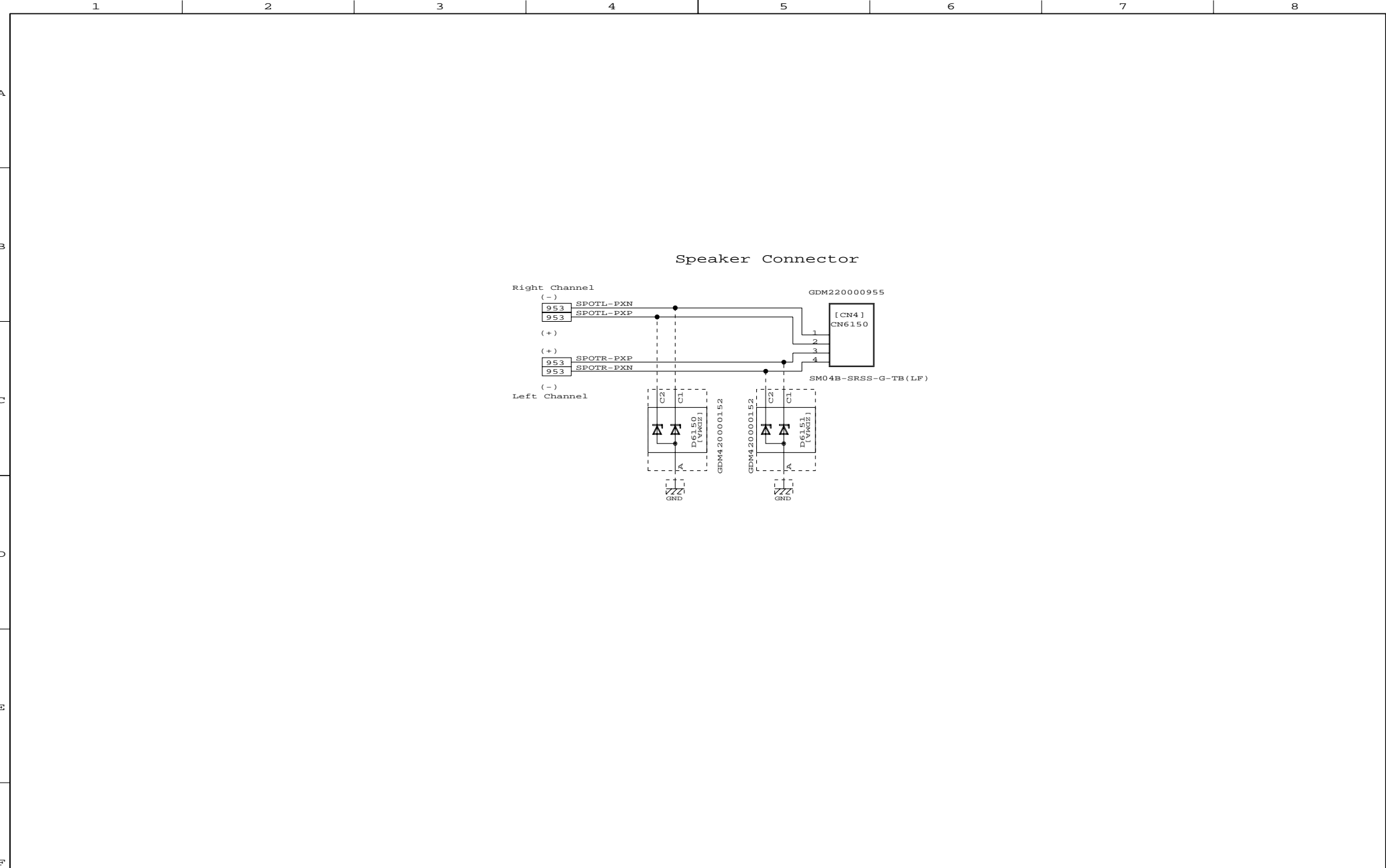
1	2	3	4	5	6	7	8
A							
B							
C							
D							
E							
F							

Malgow-6ply_CS
MXM コネクタを削除
■ 2008/02/22

	GM/GL	PM
CN5900	NotMount	Mount

DESIGNED BY H.Sonobe/T.Naruse	TITLE FG6IN1	FUNCTION GFX I/F 1	SH.NO. 596	PAGE NO. 094	REV.MARK 00	DRAWING.NO. 360065815
2008.05.05 19:11			TOSHIBA CORPORATION			

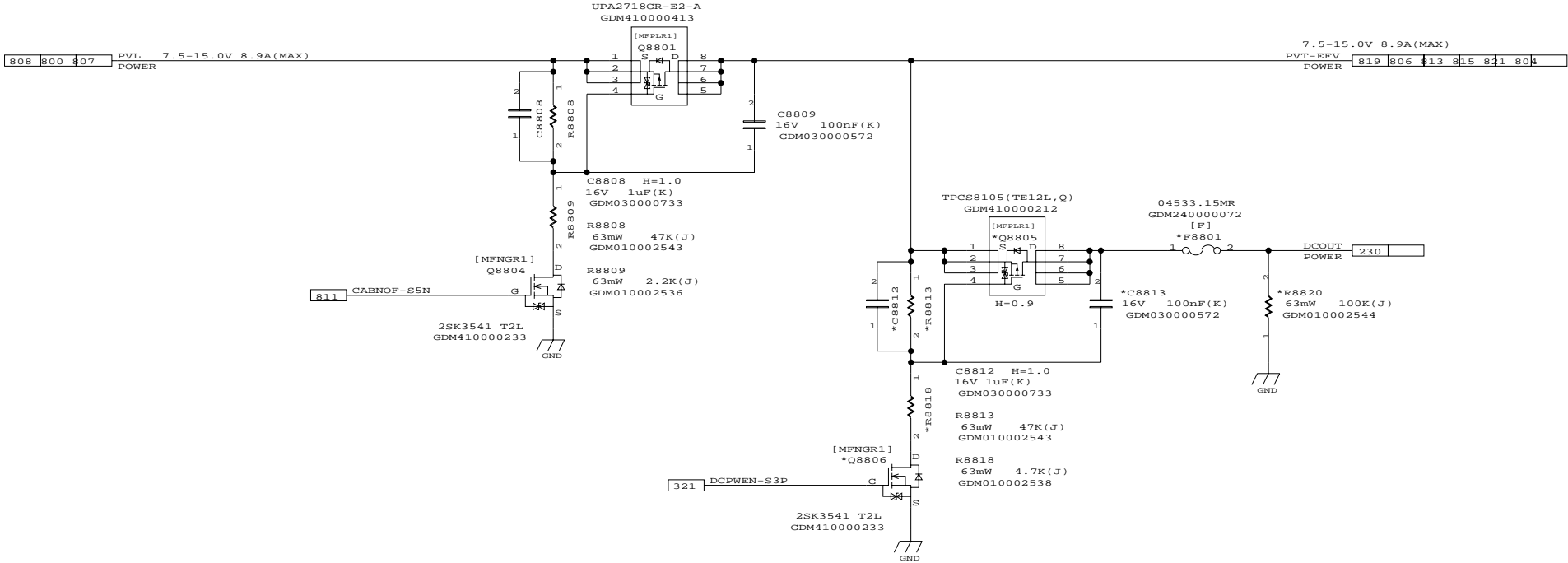




REV.05

DESIGNED BY	2007/5/24	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
M.Yamaguchi		FG6IN1	Speaker Conn.	615	096	00	360065815

仕向け設定あり



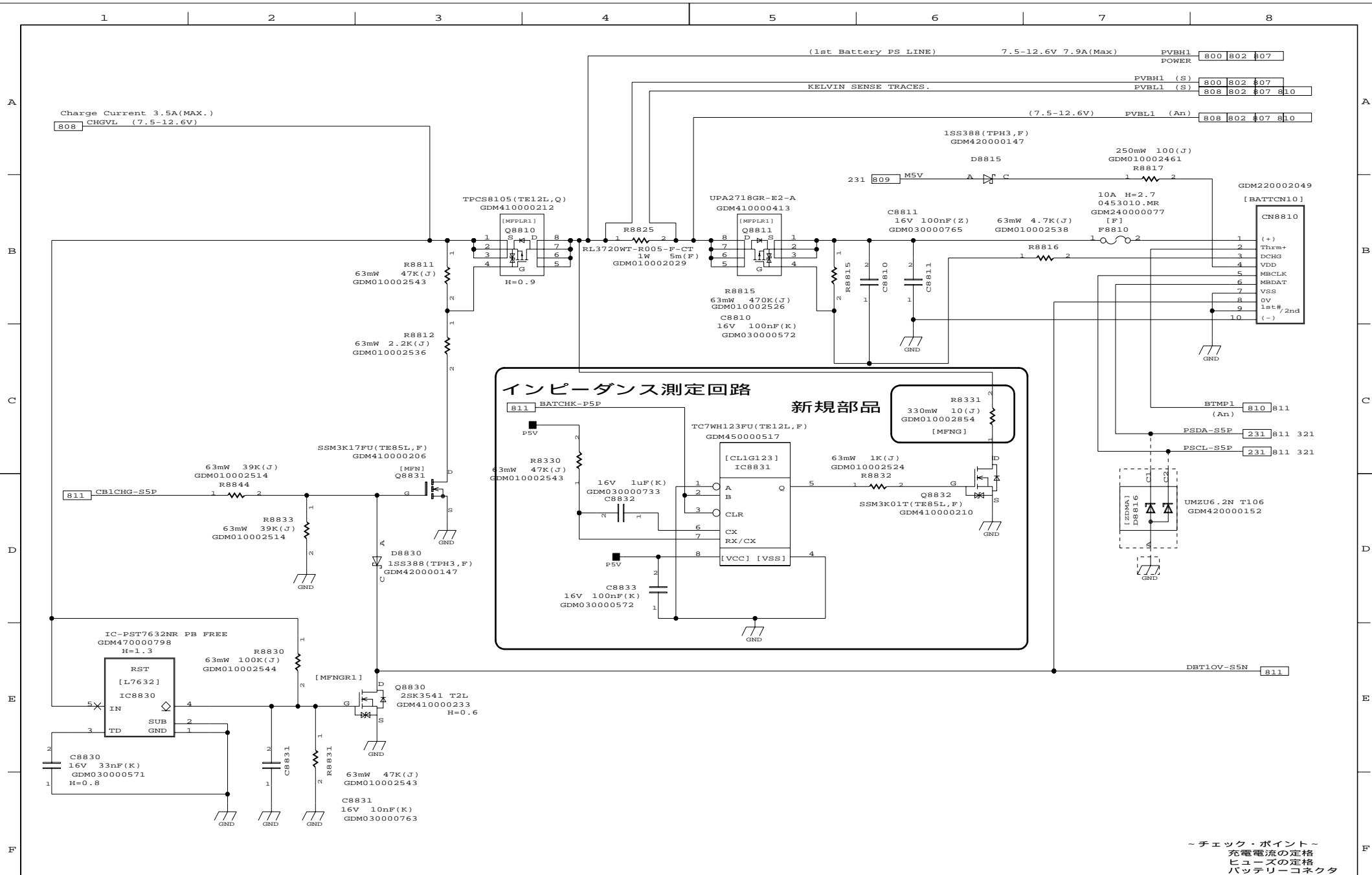
仕向け設定

Group	Docker サポート	Docker 非サポート
C8812,C8813,F8801,Q8805 Q8806,R8813,R8818,R8820	Mount	Not Mount

～チェック～
部品定格
FET
ドッキング出力回路

DESIGNED BY Y.Horie	2008/2/6	TITLE FG6IN1	FUNCTION [PS]PVT-SW	SH.NO. 801	PAGE NO. 099	REV.MARK 00	DRAWING.NO. 360065815
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2008.05.05 19:11



~チェック・ポイント~
充電電流の定格
ヒューズの定格
バッテリーコネクタ

DESIGNED BY 2008/2/6

Y.Horie

TITLE

FG6IN1

FUNCTION

[PS]1st Battery

SH.NO.

802

PAGE NO.

100

REV.MARK

00

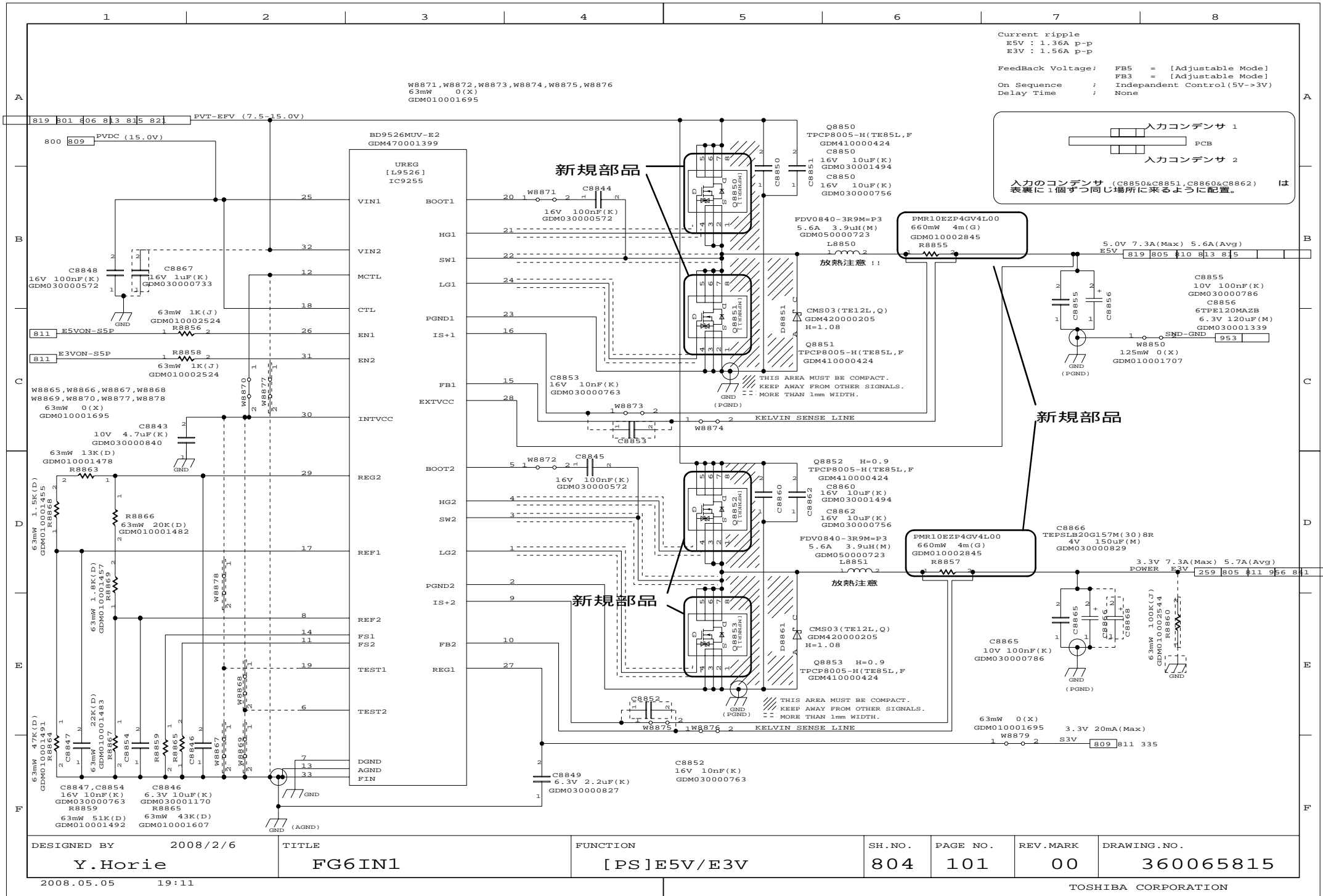
DRAWING.NO.

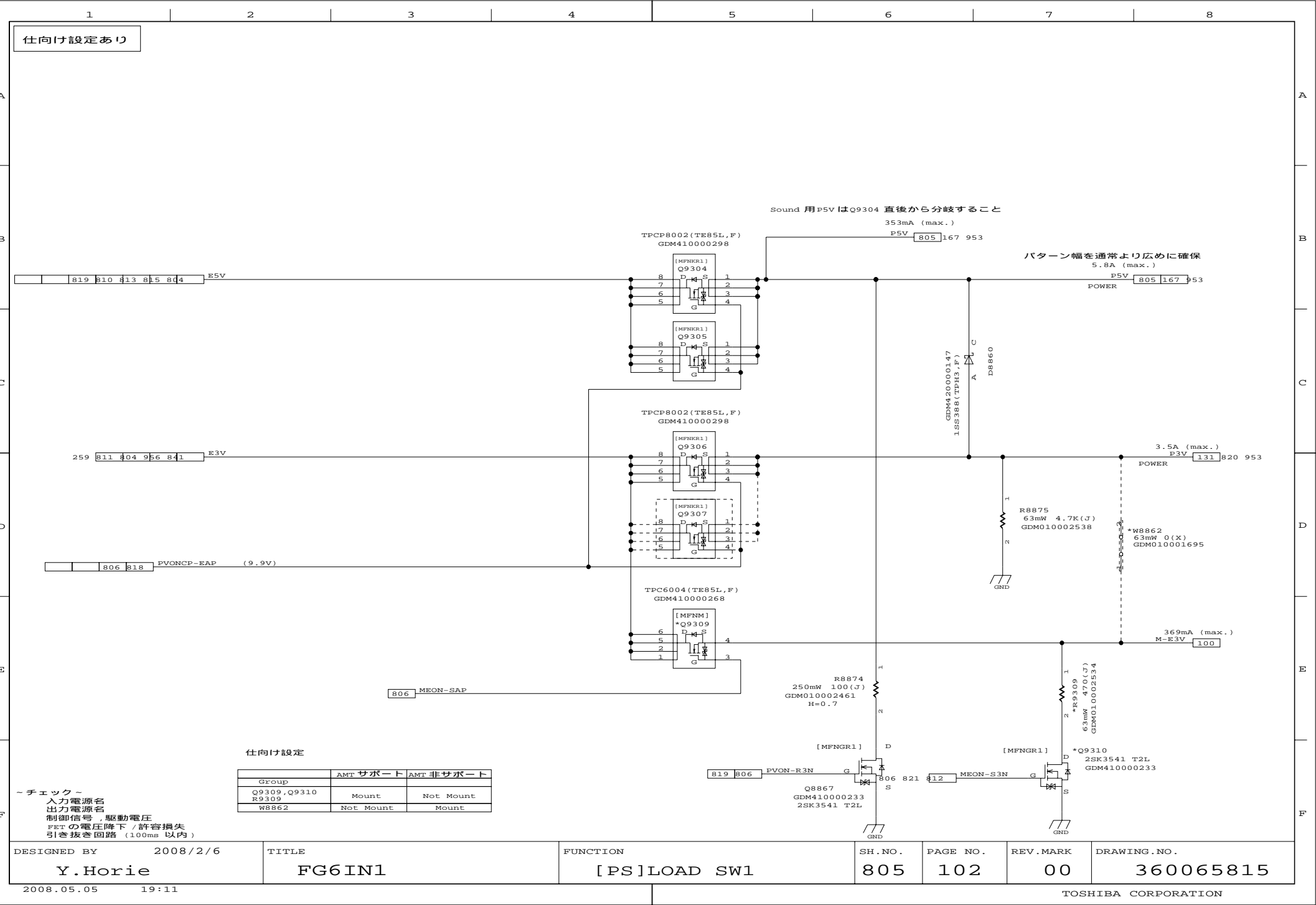
360065815

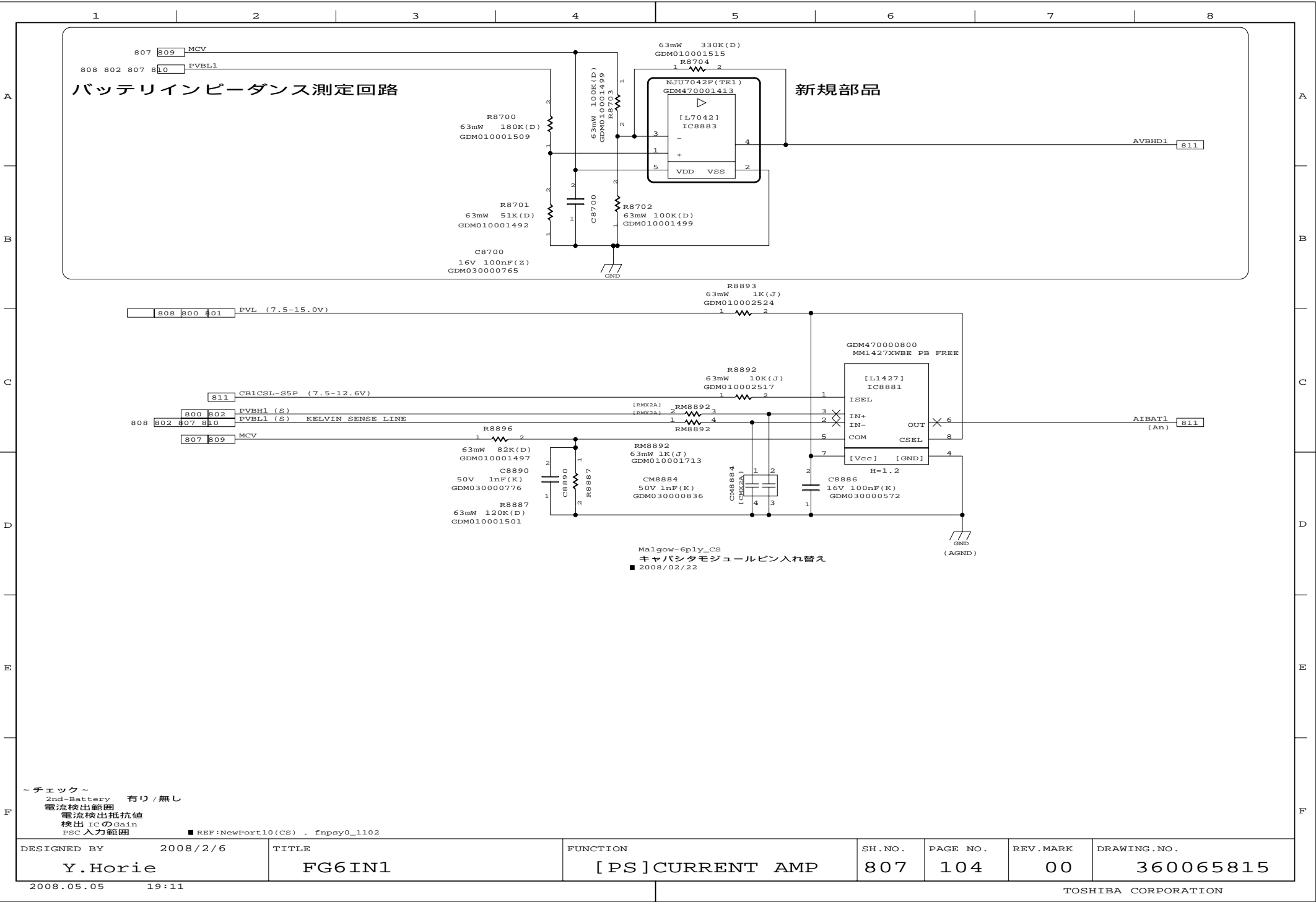
2008.05.05

19:11

TOSHIBA CORPORATION

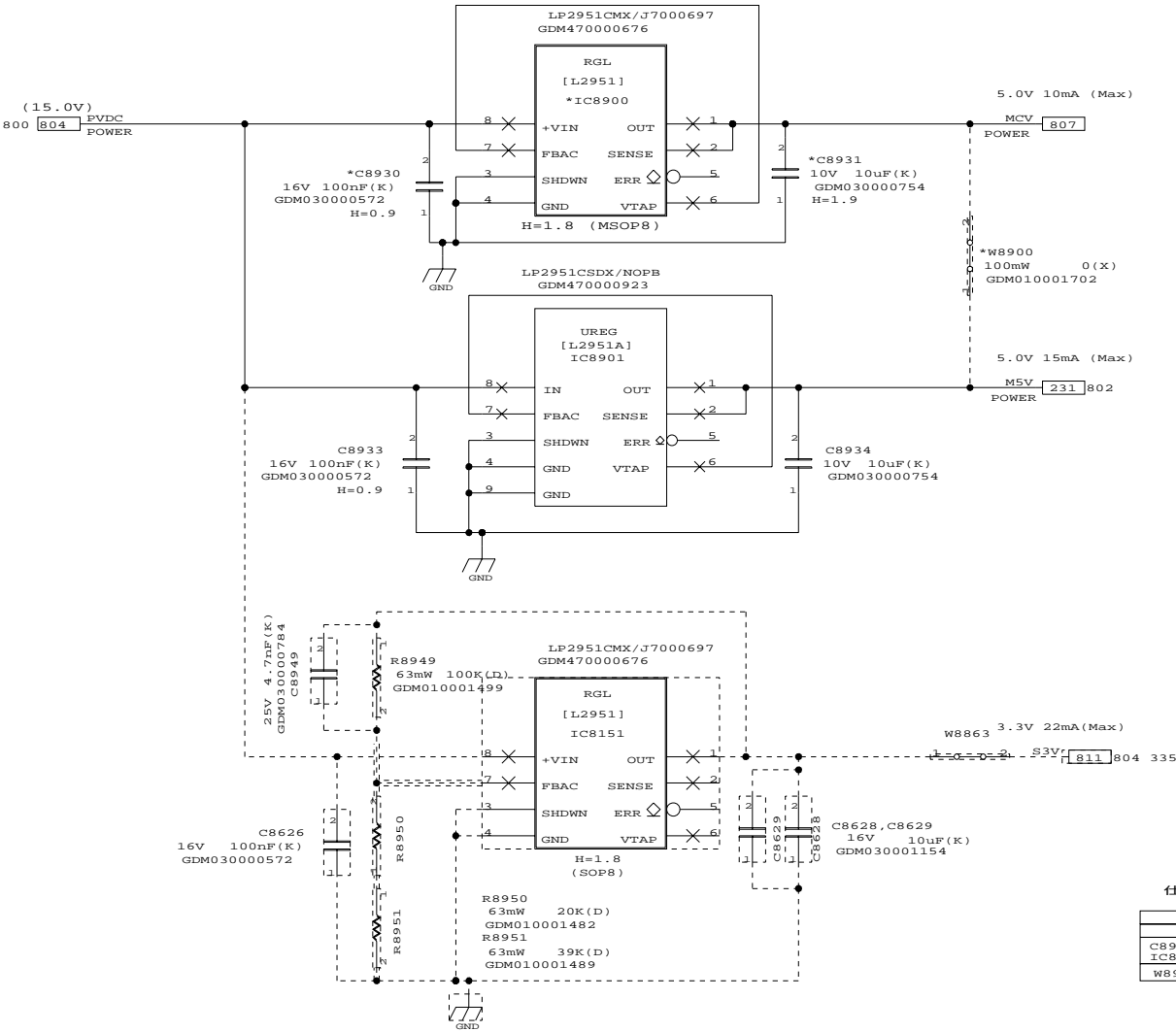






仕向け設定あり

M5V Supply current
LED : [10mA]
Thermal : [1mA × 2 =2mA]
IR Controller : [7.5mA]



仕向け設定

Group	Docker サポート	Docker 非サポート
C8930,C8931 IC8900	Mount	Not Mount
W8900	Not Mount	Mount

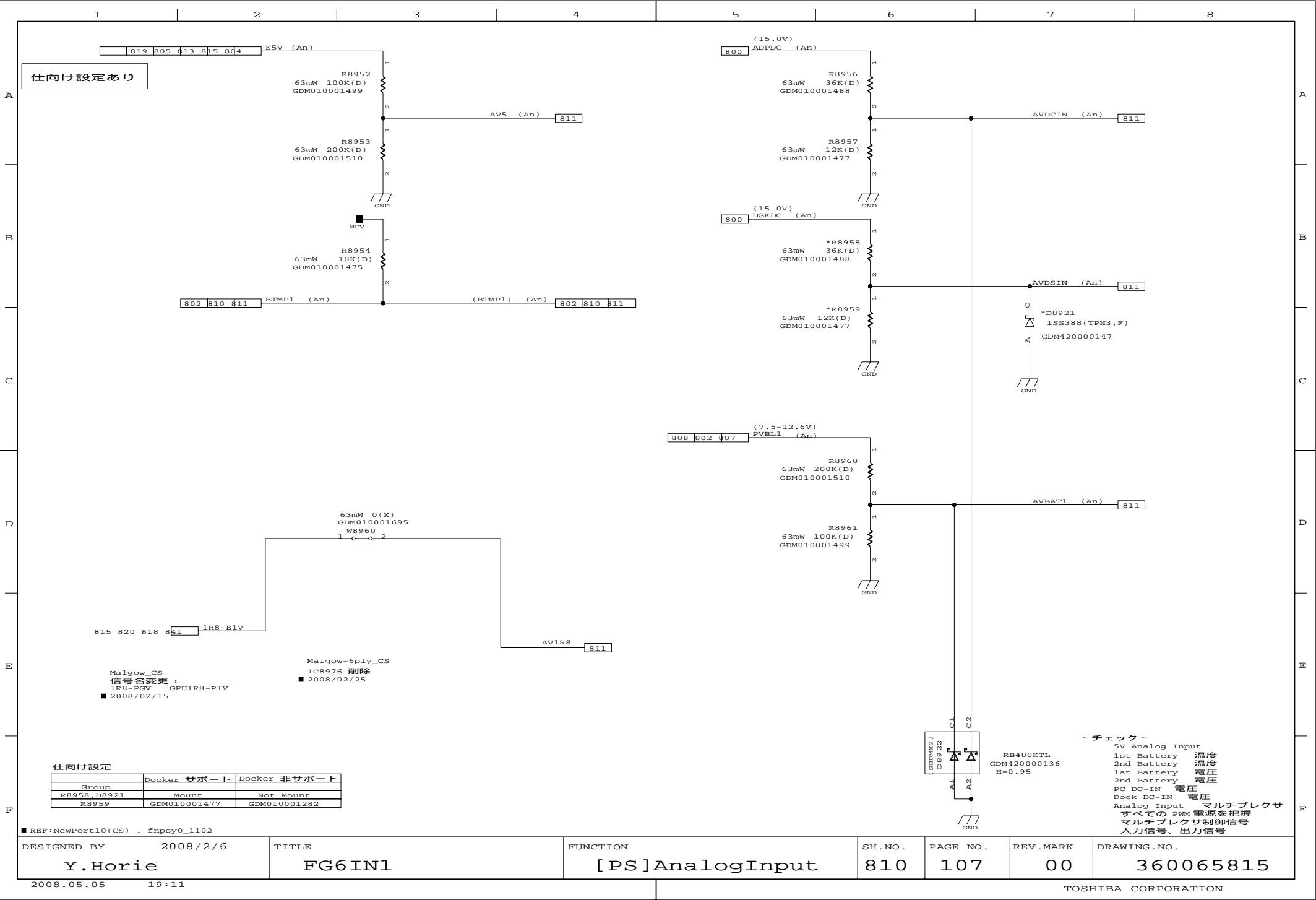
~チェック~
出力電圧
出力電流定格

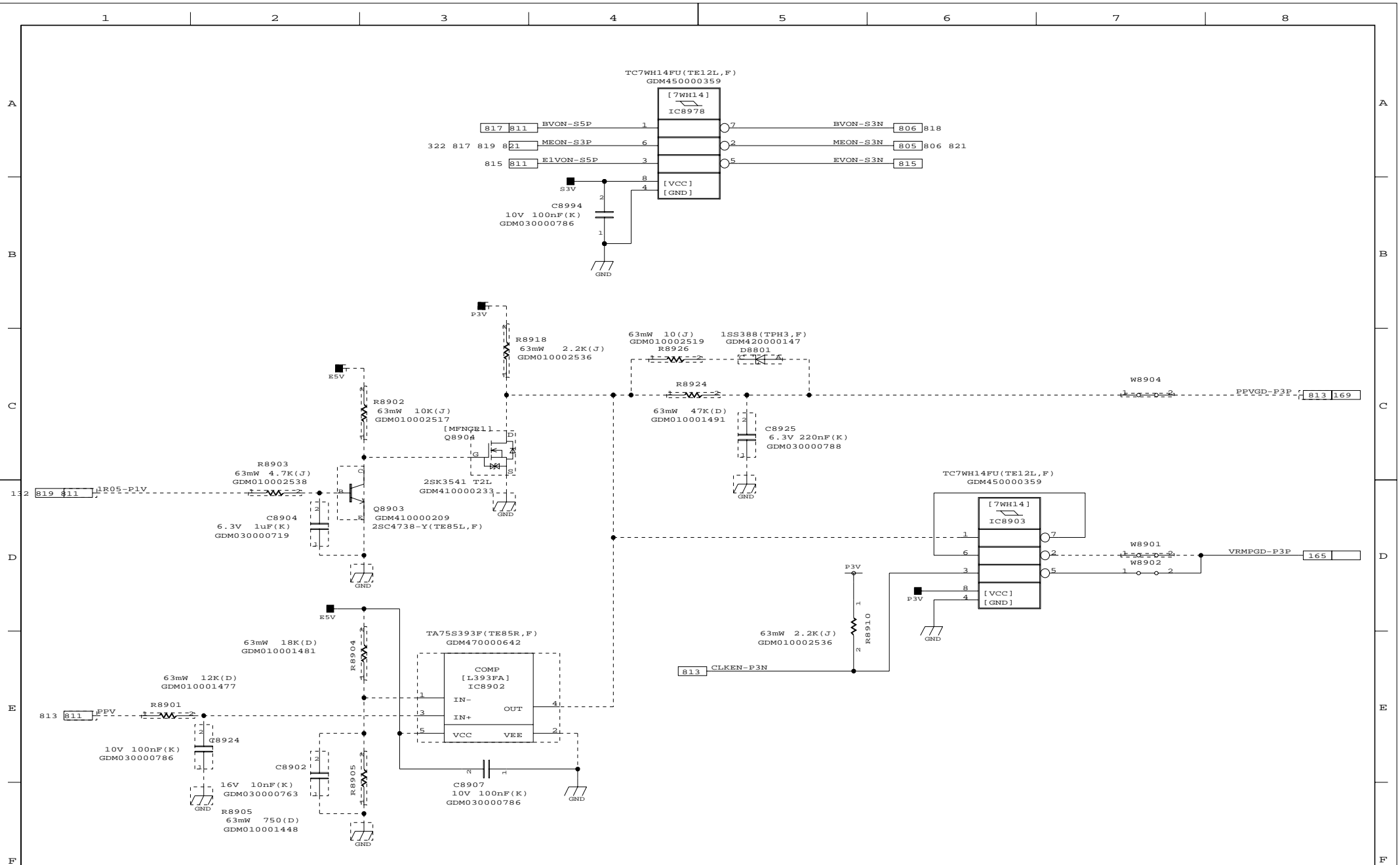
■ REF:NewPort10(CS) . fnpsy0_1102

DESIGNED BY Y.Horie	2008/2/6	TITLE FG6IN1	FUNCTION [PS]S5V/S3V	SH.NO. 809	PAGE NO. 106	REV.MARK 00	DRAWING.NO. 360065815
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2008.05.05 19:11

TOSHIBA CORPORATION





DESIGNED BY 2008/2/6

Y.Horie

2008.05.05 19:11

TITLE

FG6IN1

FUNCTION

[PS]PSC(2)

SH.NO.

812

PAGE NO.

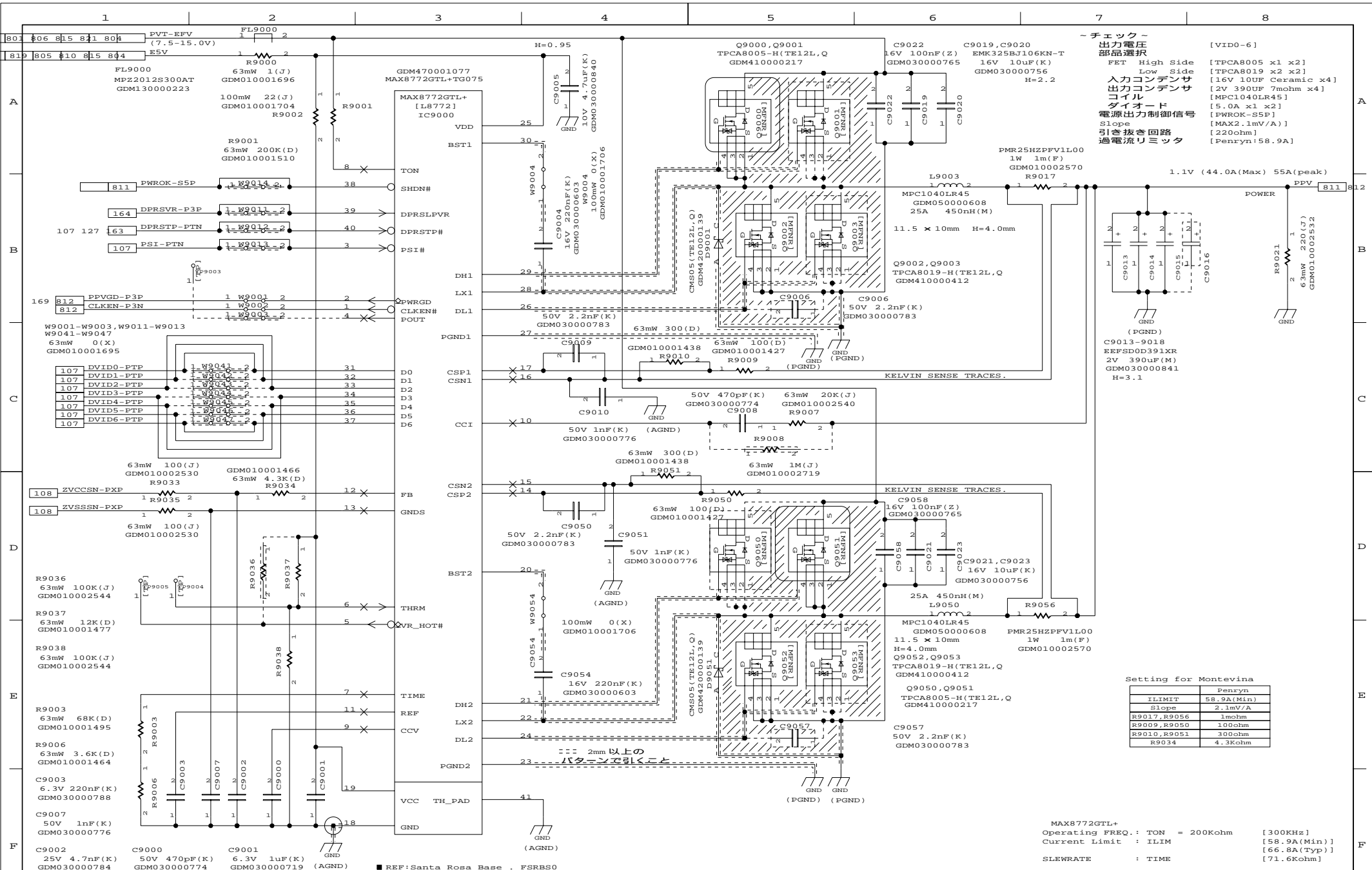
109

REV.MARK

00

DRAWING.NO.

360065815



DESIGNED BY 2008/2/6

Y.Horie

2008.05.05

19:11

TITLE

FG6IN1

FUNCTION

[PS]CPUVCC

SH.NO.

813

PAGE NO.

110

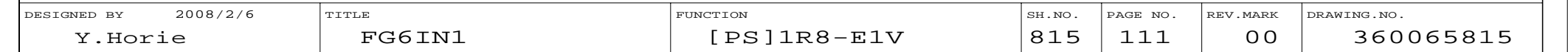
REV.MARK

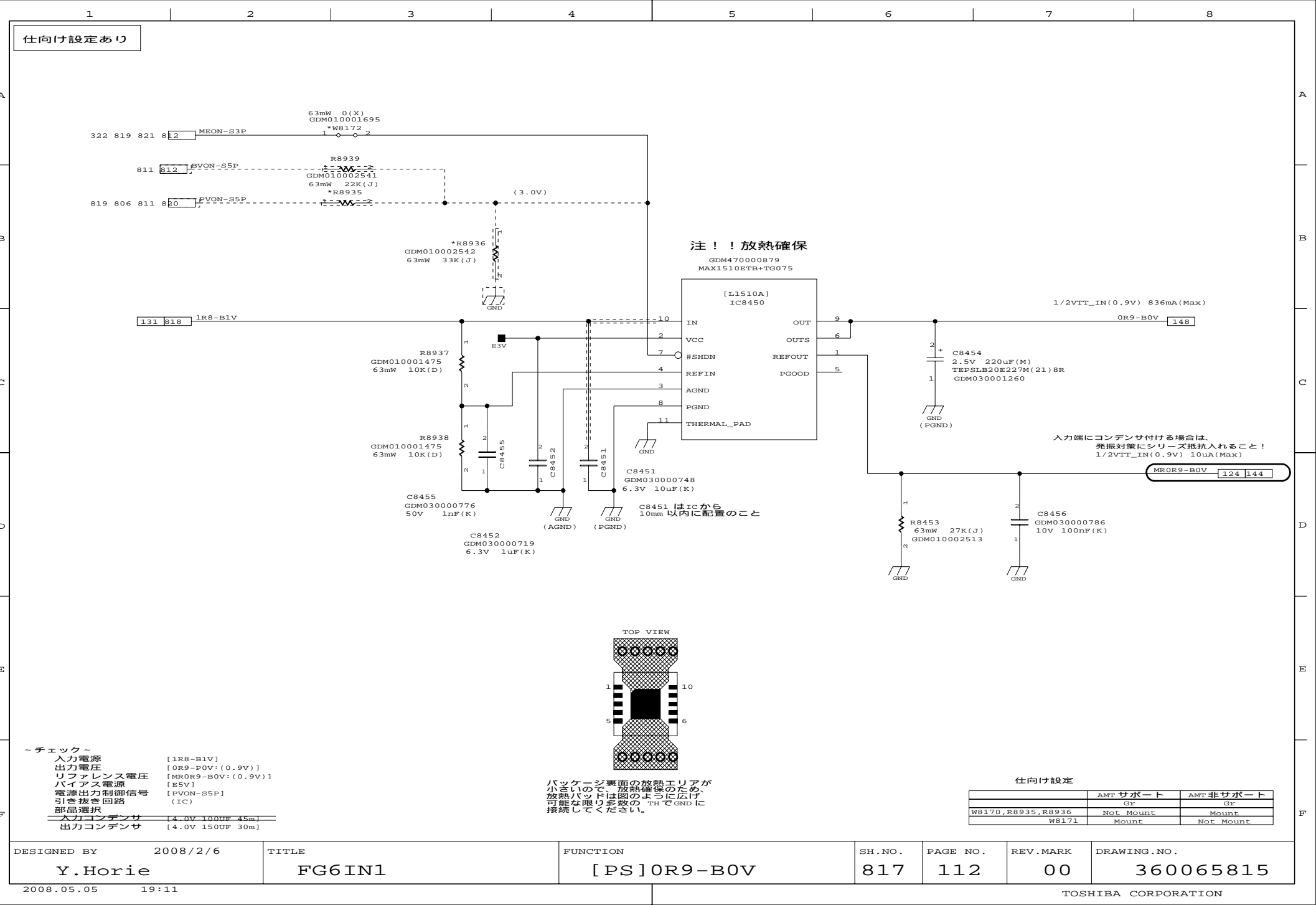
00

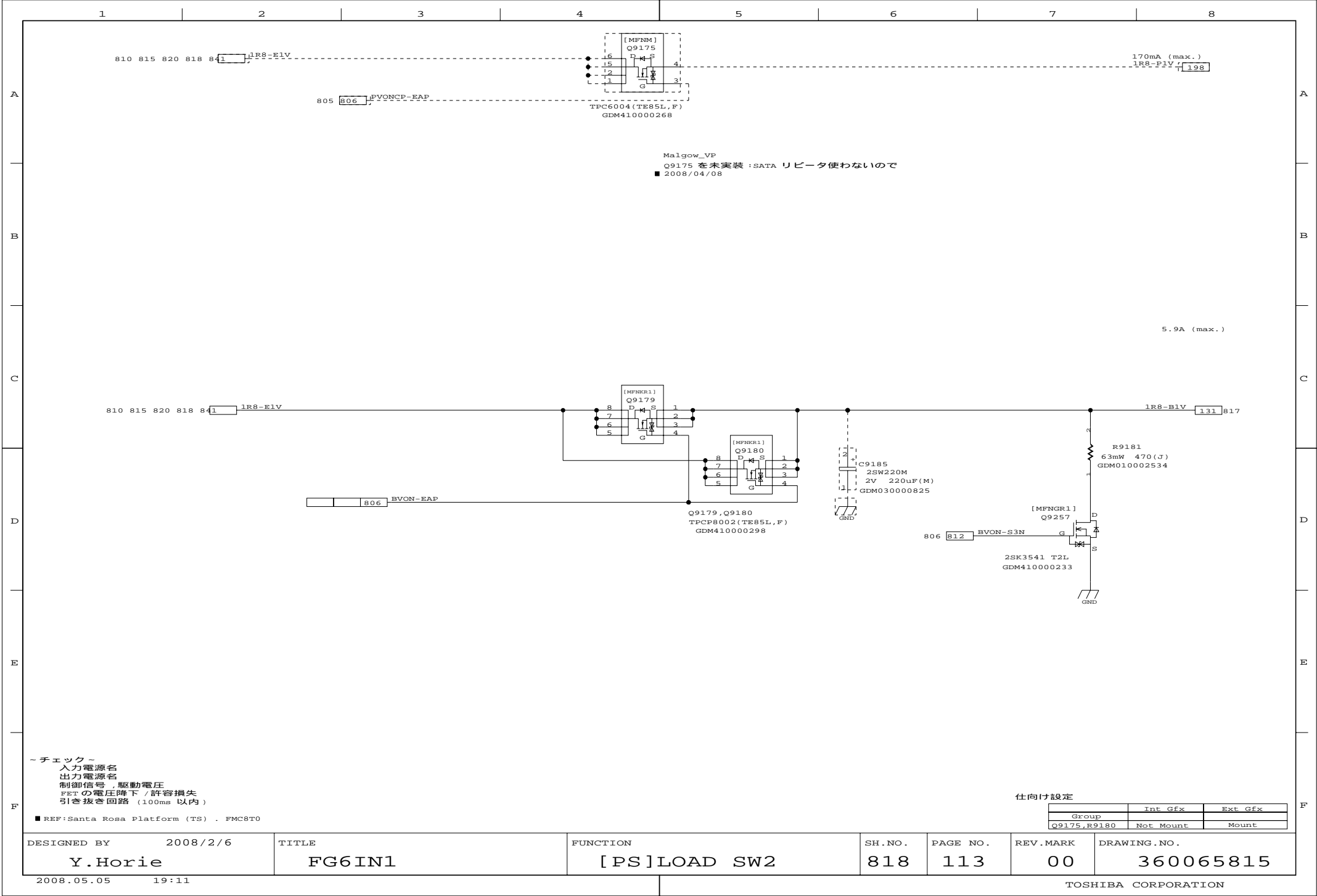
DRAWING.NO.

360065815

TOSHIBA CORPORATION





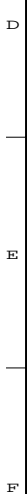


F

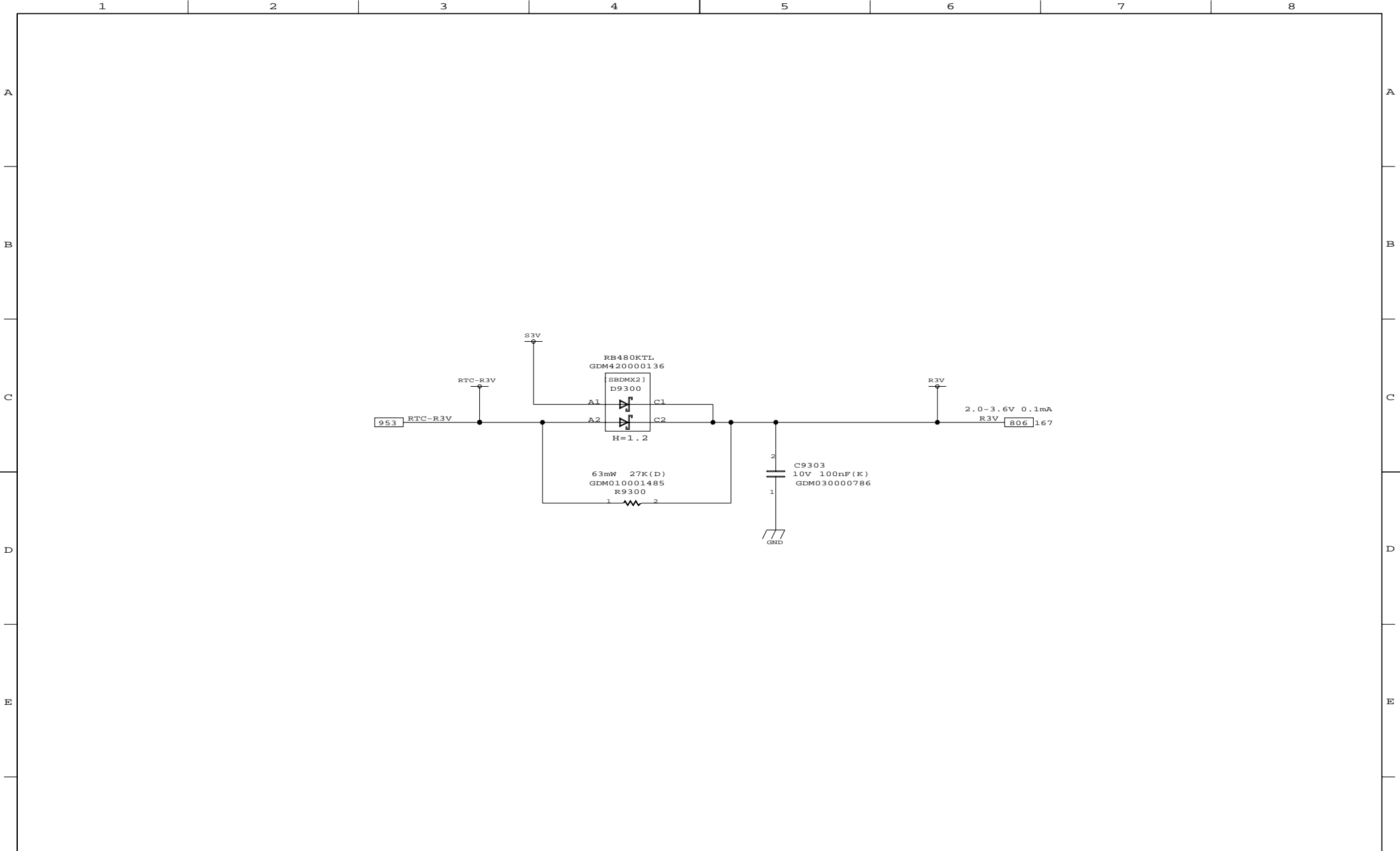


A vertical timeline diagram with a thick vertical line. On the left side, there are three labels: A, B, and C, each with a horizontal tick mark pointing to the line. On the right side, there are three labels: D, E, and F, each with a horizontal tick mark pointing to the line. A horizontal line segment connects the tick marks for C and D, spanning the vertical line.

利規部

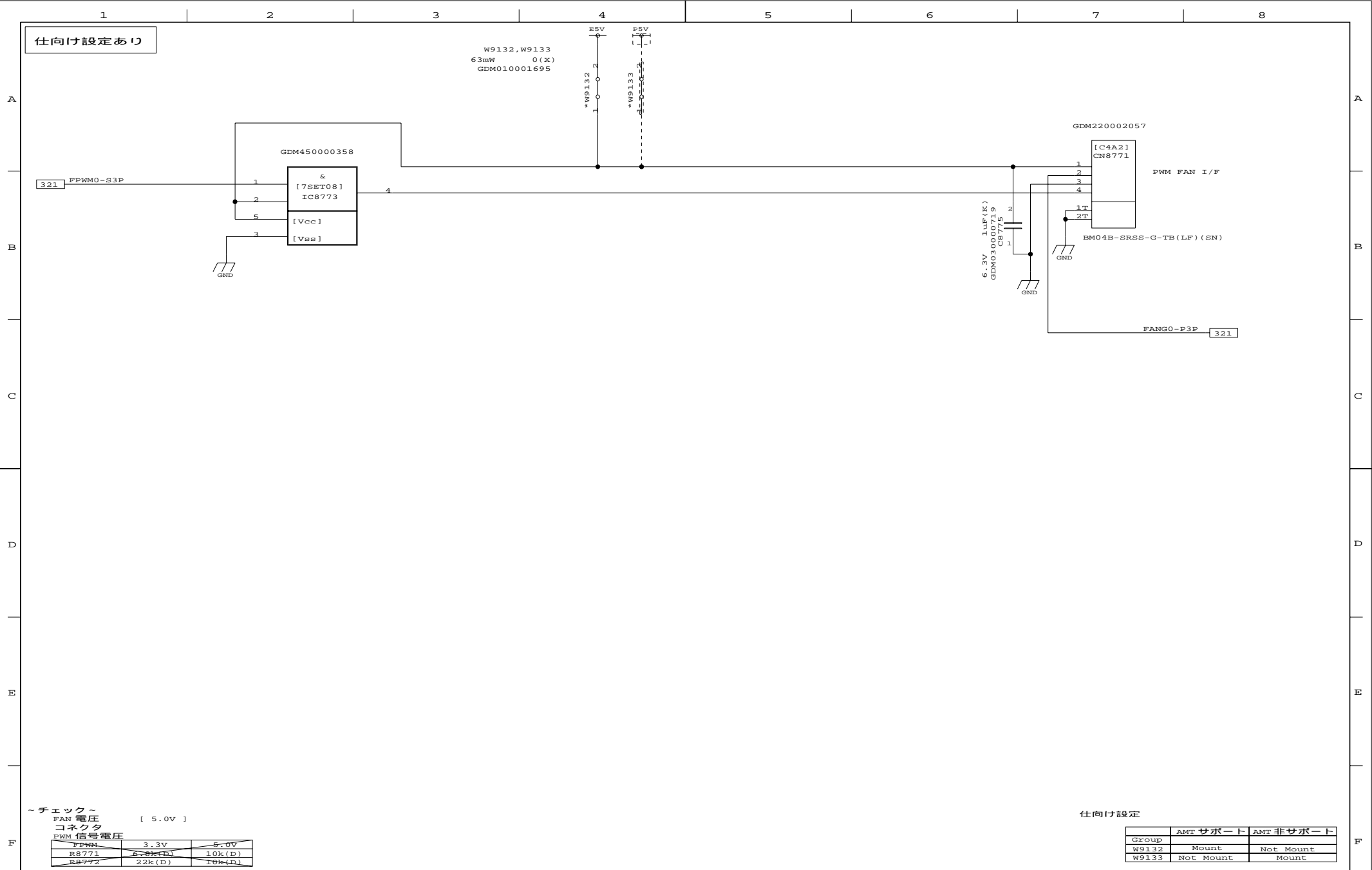


2008.05.05	19:11	TOSHIBA CORPORATION
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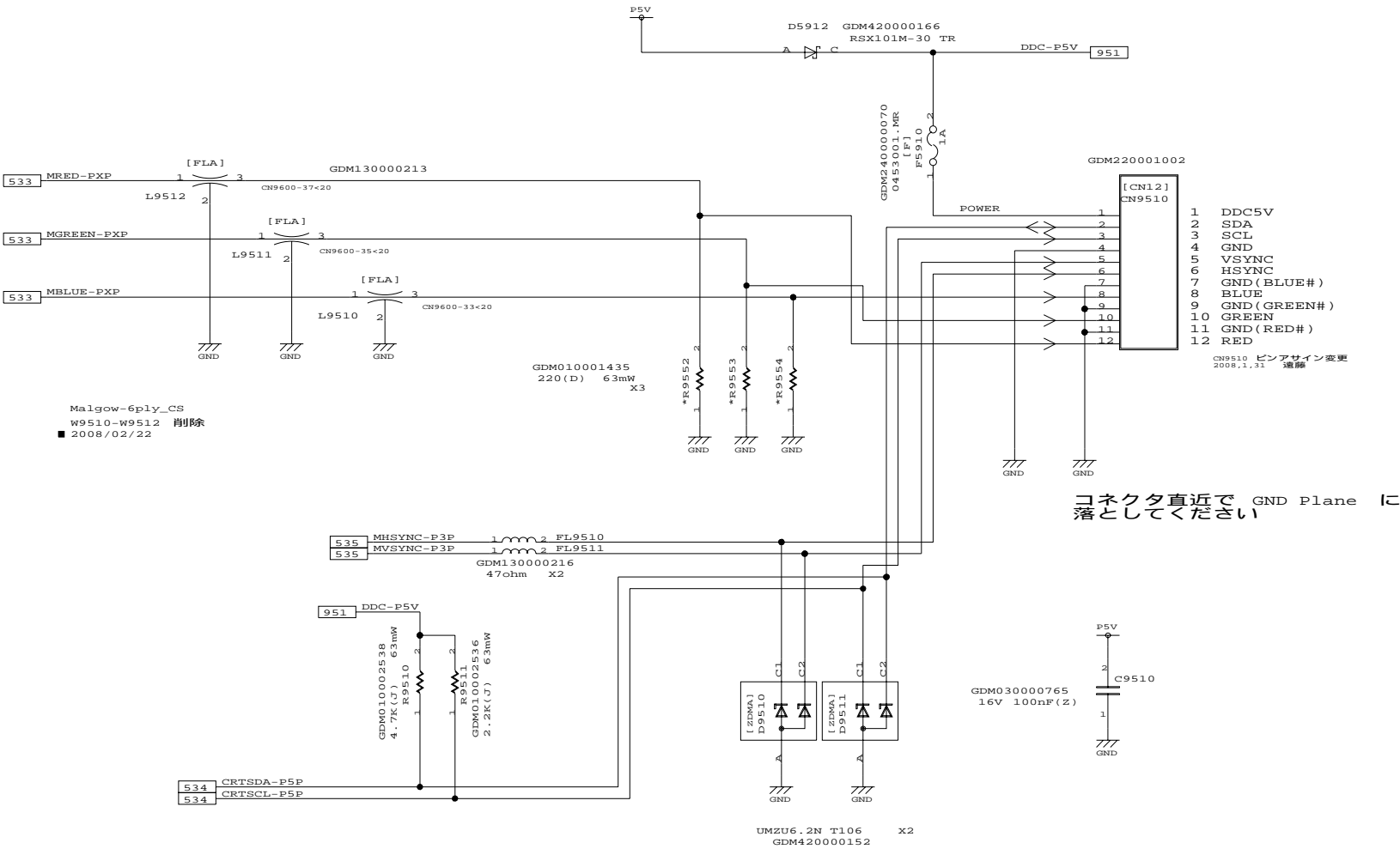
~チェック・ポイント~
RTC Battery コネクタ
使用 RTC Battery (Hi-MH)

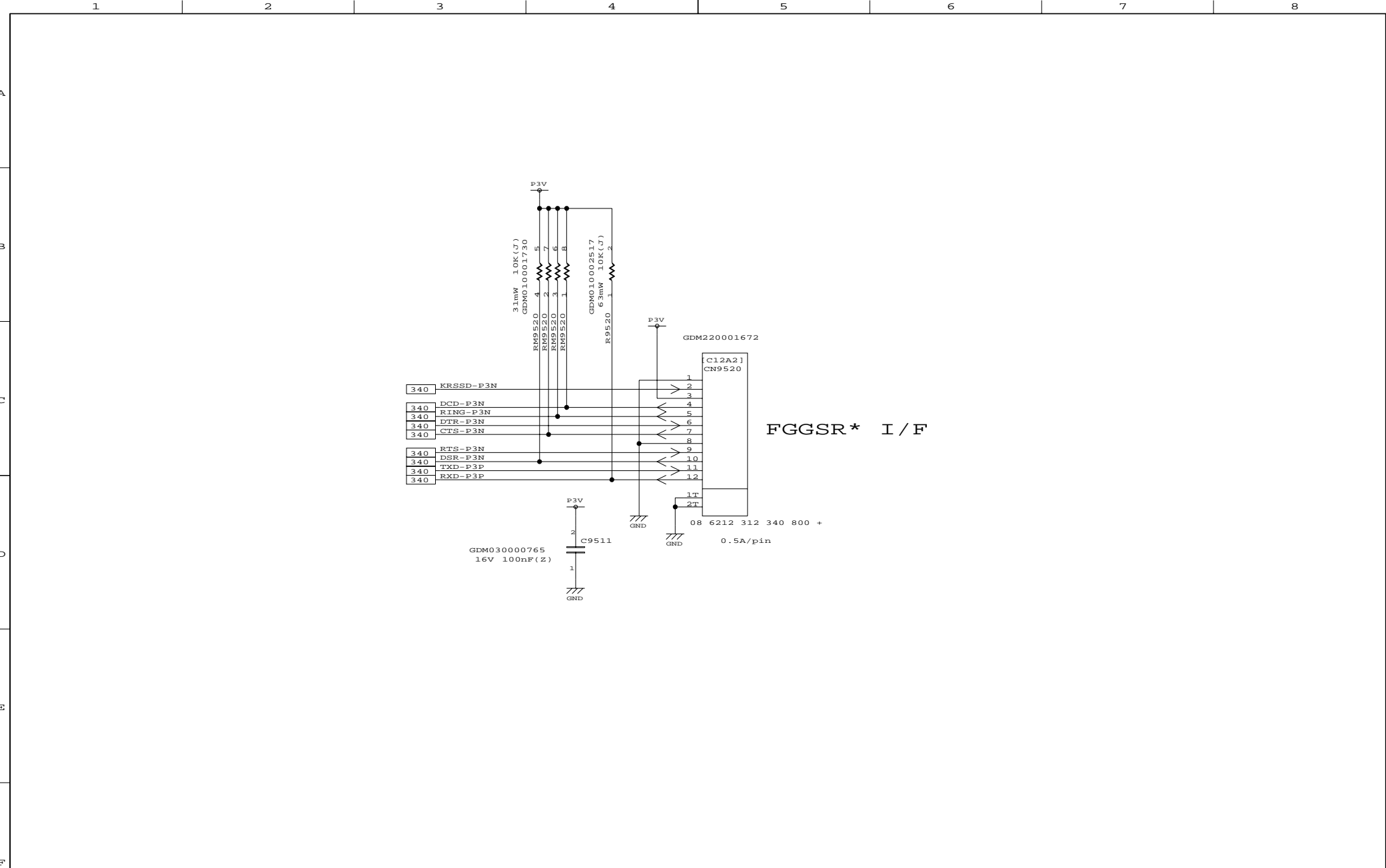
DESIGNED BY	2006/10/2	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
Y.Horie		FG6IN1	[PS]RTCVCC	822	117	00	360065815



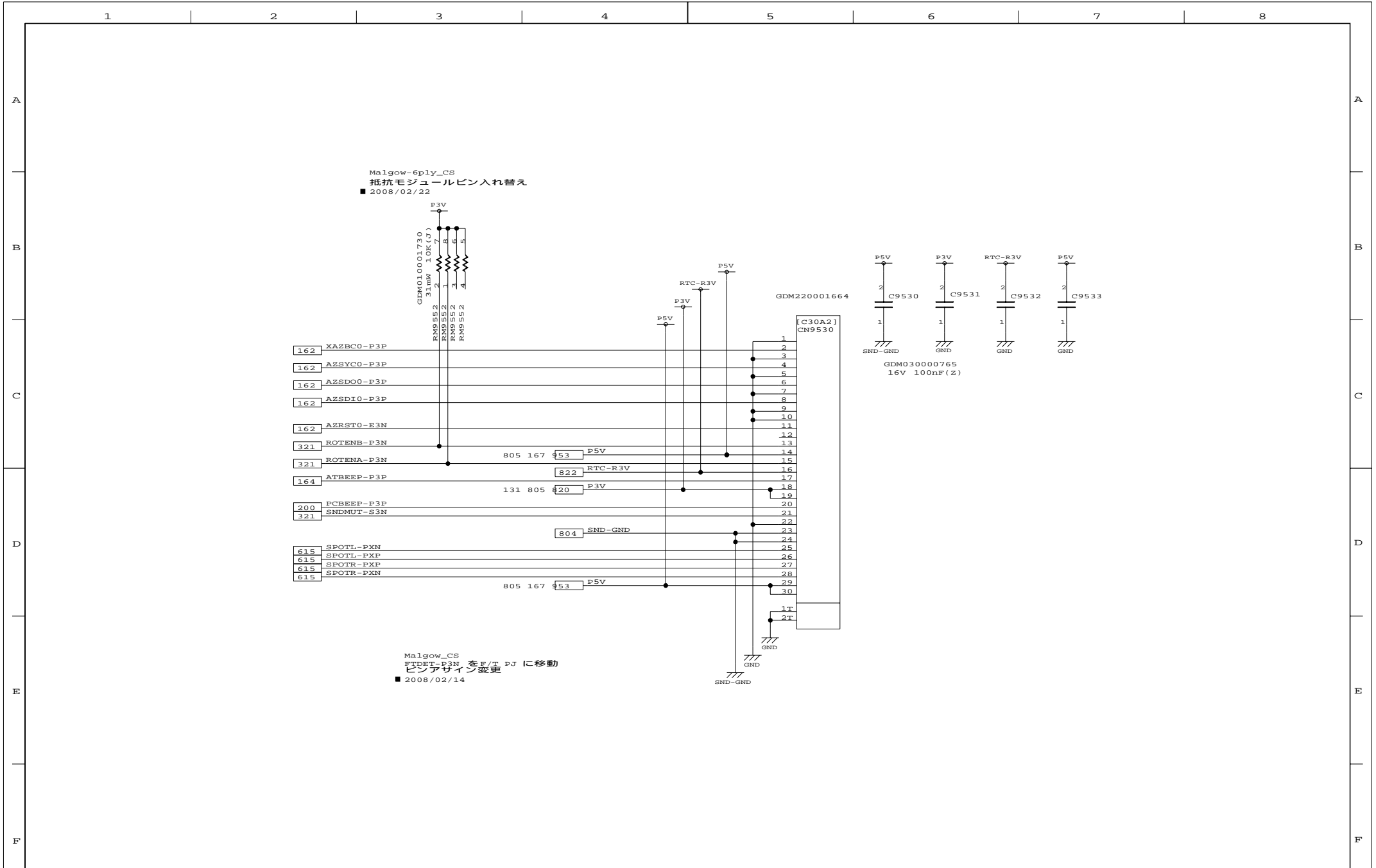
仕向け設定あり

	GM W/ Dock	GM/GL W/o Dock	PM W/ Dock
R9552-R9554	Mount	NotMount	Mount

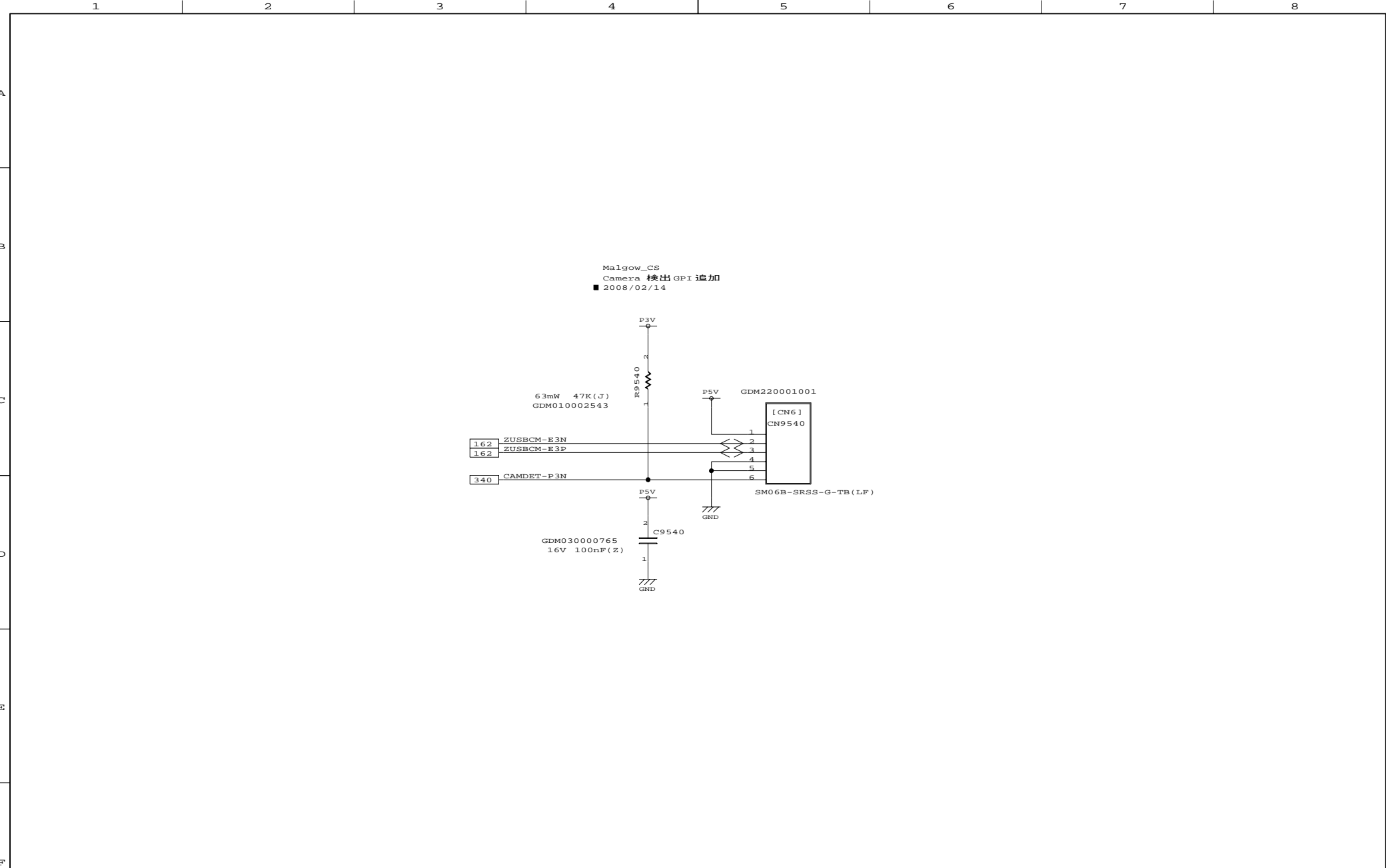




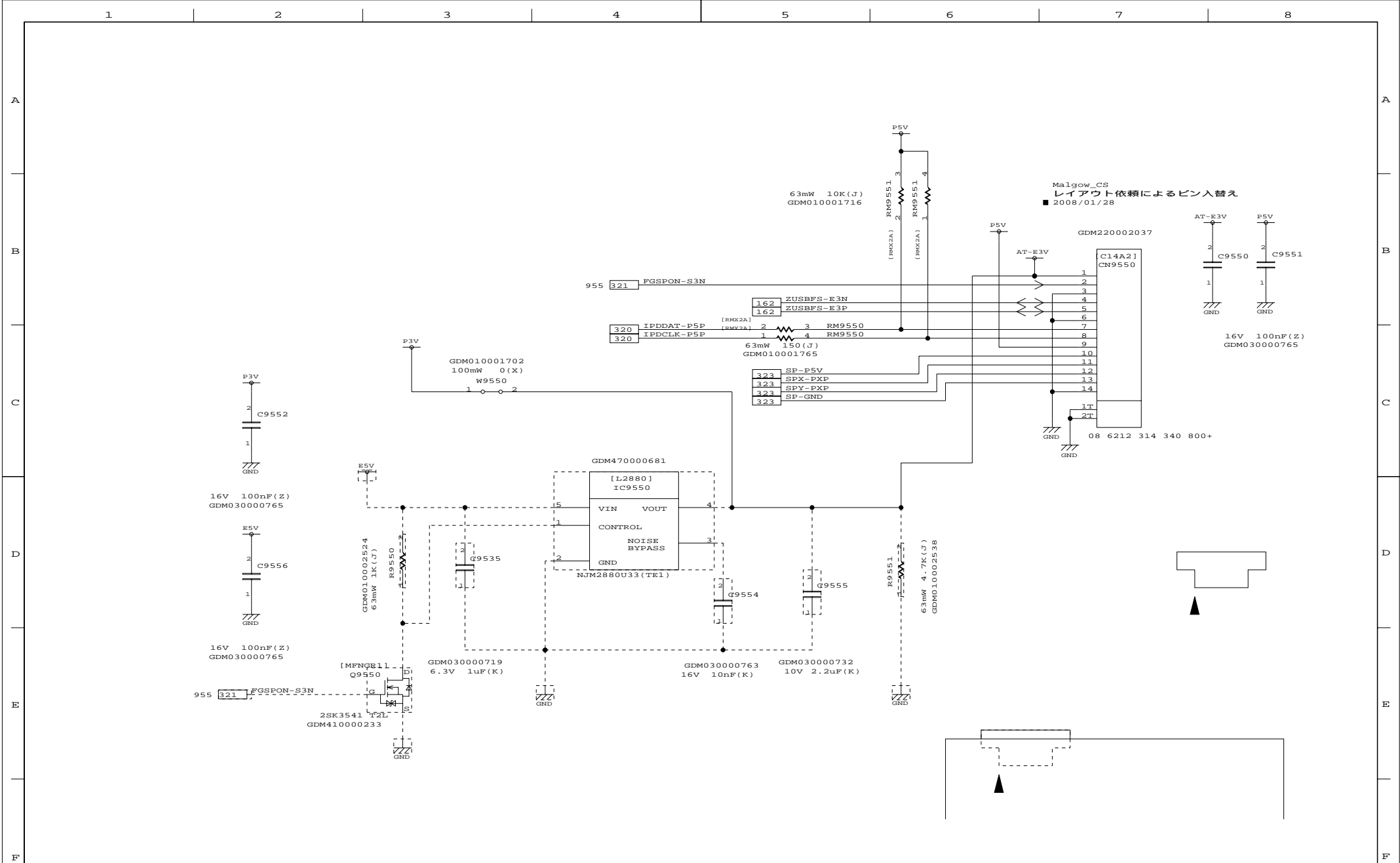
DESIGNED BY		TITLE		FUNCTION		SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
T.Ichimura		FG6IN1		FGGSR* I/F		952	122	00	360065815
2008.05.05		19:11		TOSHIBA CONFIDENTIAL		TOSHIBA CORPORATION			



DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
M.Yamaguchi/T.Ichimura	FG6IN1	FGGSN* I/F	953	123	00	360065815
2008.05.05	19:11					TOSHIBA CORPORATION

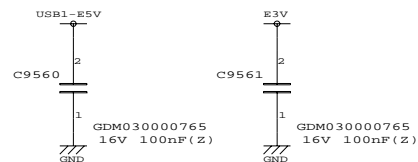


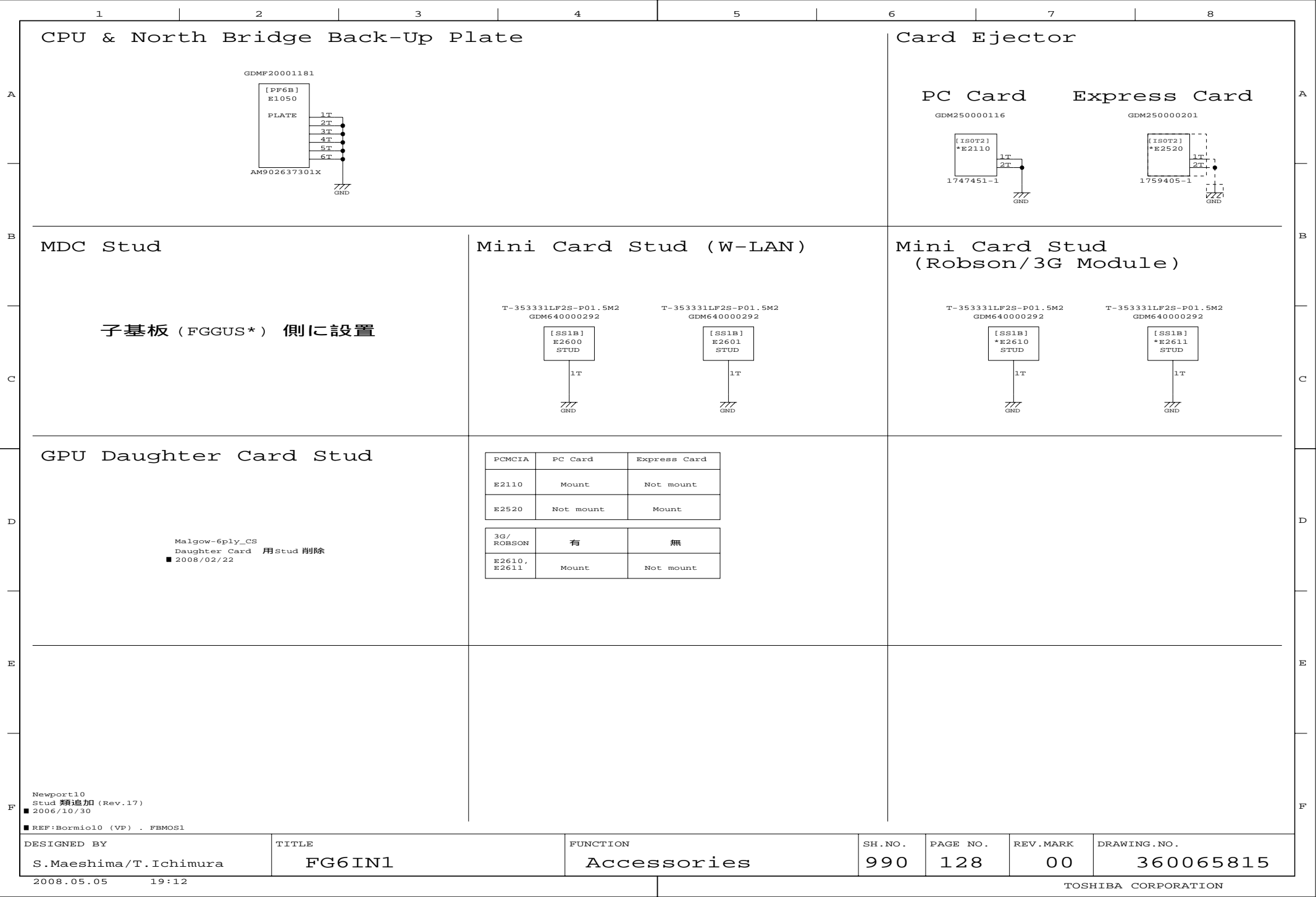
■ REF:Heidelberg10C (VP) . FHBIS2									
DESIGNED BY 2007/11/22 T.Ichimura		TITLE FG6IN1		FUNCTION WebCam I/F		SH.NO. 954	PAGE NO. 124	REV.MARK 00	DRAWING.NO. 360065815
2008.05.05 19:11		TOSHIBA CONFIDENTIAL			TOSHIBA CORPORATION				



REF:BoImic10 (VP) . FBMOS1

DESIGNED BY	2008/05/05	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
T.Ichimura		FG6IN1	FGGFS* I/F	955	125	00	360065815
2008.05.05 19:11				TOSHIBA CORPORATION			





Newport10
Stud 類追加 (Rev.17)
■ 2006/10/30

■ REF:Bormio10 (VP) . FBMS1